## MITSUBISHI

PROGRAMMABLE CONTROLLER


User's Manual
type A73CPU

## REVISIONS

*The manual number is given on the bottom left of the back cover.


## INTRODUCTION

Thank you for choosing the Mitsubishi MELSEC-A Series of General Purpose Programmable Controllers. Please read this manual carefully so that the equipment is used to its optimum. A copy of this manual should be forwarded to the end User.

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## 1. INTRODUCTION

This instruction manual describes the items related to the hardware (such as system configuration, components, handling precautions, and related units) required for positioning control with the A73CPU (P21/R21) multi-axis controller unit.

The A73CPU is the CPU incorporating a positioning control CPU (referred to as the PCPU in this manual) and a sequence control CPU (referred to as the SCPU in this manual).
(a) The PCPU monitors positioning control based on the servo program and the control status of the servo amplifier.
(b) The SCPU performs sequence control (the same as the A3NCPU), servo program start-up, manual pulse generator (MPG) operation enabling, JOG operation, etc.


Fig. 1.1 A73CPU Program and Data Preparation and Process Outline
(1) A73CPU Program and Data Preparation
(a) The sequence program used by the SCPU is prepared by an A6GPP/A6PHP started up by the SW[ ]GP-GPPA.

The sequence program is stored in the memory cassette loaded in the A73CPU.
(b) The servo program and positioning parameters used by the PCPU are prepared by an A6GPP/A6PHP or A6MD started up by the SW[ ]GP-A73P.

The servo program and positioning parameters are stored in the E2ROM of the A73CPU unit.
(c) The servo program designated by a sequence program is used to perform positioning control.
(d) The positioning control status can be monitored by an A6GPP/A6PHP or A6MD started up by the SW[ ]GP-A73P.
(e) Servo program execution, JOG operation, and other functions are tested by an A6GPP/A6PHP or A6MD started up by the SWI ]GP-A73P.

## (2) Reference Manual

For information required for the A73CPU operation but is not found in this manual, refer to the following manuals.
(a) A73CPU Reference Manual [IB (NA)-66233]

This manual describes the positioning parameters, devices, servo program, SCPU functions, and other factors required for positioning control.
(b) A6MD Monitor Display Unit User's Manual [IB (NA)-66234]

This manual describes the hardware-related information (on the appearance, installation, connecting cable preparation, etc.).
(c) A6MD Monitor Display Unit Operating Manual [IB (NA)-66235]

This manual describes positioning parameter and servo program preparation, testing, and monitoring with the A6GPP/A6PHP.

This manual describes positioning parameter and servo program preparation, floppy disk storage, printout, monitoring, and testing with the A6GPP/A6PHP.
(d) A6GPP/A6PHP (for the SW3GP-GPPA) Operating Manual [IB (NA)-66212]

This manual describes sequence program, comment, and other data preparation, floppy disk storage, printout, monitoring, and testing with the A6GPP/A6PHP.
(e) ACPU Program Manual [IB (NA)-66147]

This manual describes sequence programs, devices, and other things related to perform sequence control and servo program start up.
(f) Building Block Input/Output Module User's Manual [IB (NA)66140]

This manual describes the specifications and dimensions of the input/output modules used for sequence control and limit switch output.

### 1.1 General Information on Positioning Control

This section outlines the positioning control performed by the A73CPU when the MR-SB servo amplifier (referred to as the MR-SB in this manual) or a general-purpose servo amplifier is used.

### 1.1.1 When an MR-SB servo amplifier is used

The A73CPU and the MR-SB are connected by a digital bus (RS-485) via the servo interface unit (referred to as the A70SF in this manual).

This connection enables MR-SB status monitoring and servo diagnosis (position loop gain/velocity loop gain check) in addition to high-speed and high-precision positioning control.


Fig. 1.2 Outline of the Operation Performed When an MR-SB Is Used.
(1) Explanation of Operation
(a) The SCPU requests the PCPU to start the servo program specified by a sequence program.
(b) The PCPU executes the servo program specified by the SCPU and causes the A7OSF to output the command position data to the MR-SB via the digital bus.

At all times, it monitors the monitor data from the MR-SB (error counter value, actual position data relative to command position data, error information, etc.) and controls the command position data for the MR-SB.
(c) The MR-SB controls the servo motor with the command position data and the position/velocity feedback data specified by the PCPU.

### 1.1.2 When a general-purpose amplifier is used

The A73CPU is connected to the general-purpose amplifier on a one-to-one basis using the A70AF general-purpose servo interface unit (referred to as the A7OAF in this manual).
The velocity command (analog voltage) is delivered from the A70AF to the general-purpose servo amplifier.


Fig. 1.3 Outline of Operation Performed When a General-Purpose Servo Amplifier is Used.
(1) Explanation of operation
(a) The SCPU requests the PCPU to start the servo program specified by a sequence program.
(b) The PCPU executes the servo program specified by the SCPU and delivers the command position data to the A70AF.

At all times it monitors the monitor data from the A70AF (error counter value and actual position data relative to command position data) and controls the command position data for the A70AF.
(c) The ATOAF converts the command position data from the PCPU into travel distance and causes the error counter to integrate the travel distance. From the integrated value, it subtracts the number of feedback pulses arriving from PLG (pulse generator).

The A70AF converts the error counter value (D/A) and delivers the velocity command (analog voltage) to the servo amplifier.

## REMARKS

- The feedback pulse is sent to the A70AF either directly or via the servo amplifier depending on the type of the servo motor.


### 1.2 Features

The A73CPU has the following features.
(1) Various positioning functions

The A73CPU is capable of positioning controls, such as 8 -axis independent control, 2-axis/3-axis linear interpolation control, and 2-axis circular interpolation control.
(2) Bus-connection control is enabled (when connected to an MR-SB servo amplifier).
(a) The A73CPU is capable of servo data collection, parameter change, and monitor diagnosing.
(b) High-precision positioning is enabled with the velocity command issued at a maximum output of 1 Mpps .
(3) Dedicated language-dependent positioning control

Programming is easily performed by describing the positioning operation in the dedicated language. Programming is performed with the A6MD monitor display unit or an A6GPP/A6PHP started up by the SWOGP-A73P multi-axis positioning unit software package.
(4) An absolute value system can be configured.

The use of the MR-SB servo amplifier capable of handling absolute values enables the configuration of the absolute value system.
(5) Connectable to a general-purpose servo amplifier.

The A73CPU can be connected not only to an MR-SB servo amplifier, but also to a general-purpose servo amplifier.

### 1.3 Comparison of the A73CPU (SCPU Section) and the A3NCPU

The A73CPU uses the SCPU for sequence control and the PCPU for positioning control. Sequence control is performed by the SCPU in accordance with the same specifications as those for the A3NCPU.

Table 1.1 Difference Between the A73CPU (SCPU Section) and the A3NCPU

| Item |  |  | A73CPU (SCPU Section) | A3NCPU |
| :---: | :---: | :---: | :---: | :---: |
| Usable extension base |  |  | A65B, A68B <br> (A55B or A58B not used) | A65B, A68B A55B, A58B |
| Main base |  |  | A74B | A32B, A35B, A38B |
| Power supply module for main base |  |  | A61P * 1 | A61P, A62P, A63P, A65P |
| Number of instructions | Sequence instruction |  | 22 | 22 |
|  | Basic instruction |  | 132 | 132 |
|  | Application instruction |  | (DSFL, DSFR: 107 DSFLP, DSFAP: Application changed) | 109 |
| Number of I/O points | For sequence control |  | 1920 points (X/Y80 to X/Y7FF) | 2048 points (X/YO to X/Y7FF) |
|  | For positioning control |  | 128 points (X/Y0 to $\mathrm{X} / \mathrm{Y} 7 \mathrm{~F}$ ) | - |
| Devices | Internal relay/latch relay | For sequence control | 2000 points (M/LO to M/L1999) | 2048 points (M/LO to M/L2047) |
|  |  | For positioning control | 48 points (M2000 to M2047) | - |
|  | Data register | For sequence control | 800 points (D0 to D799) | 1024 points (D0 to D1023) |
|  |  | For positioning control | 224 points (D800 to D1023) | - |
|  | Special relay | For positioning control | M9073 to M9079 applications added | - |
|  | Special register | For positioning control | D9180 to D9199 applications added | - |
| Latch range *2 |  |  | D0 to D799 <br> (D800 to D1023 are not latched even when they are set in the latching range.) | Do to D1023 |

## POINT

(1) *1: The A62P or the A65P cannot be used because their 5 VDC current capacity is too low.
(2) *2: This range refers to that in which latching is enabled when latch range setting is affected by parameters.

## REMARKS

With the A73CPU, the application of the DSFRP and DSFLP instructions is changed as follows:

1) The DSFRP instruction is a servo program start request instruction.
2) The DSFLP instruction is an actual position data and velocity change instruction.

## 2. SYSTEM CONFIGURATION

The system configuration required to use the A73CPU falls into 4 categories:
(a) When an MR-SB servo amplifier is used.
(b) When a general-purpose servo amplifier is used.
(c) When both the MR-SB and the general-purpose servo amplifier are used.
(d) As the data link system.

This section describes the overall configuration of this system, operating precautions, and components.

### 2.1 When an MR-SB Servo Amplifier Is Used

2.1.1 Overall configuration


Fig. 2.1 Overall Configuration Established When an MR-SB Servo Amplifier Is Used

## POINT

(1) The unit shown in Fig. 2.1 can be mounted on the A74B. The mounting position should be as indicated in Fig. 2.1.
(2) *1: SCPU extension connector (for connecting to the SCPU extension base)
(3) *2: The MR-SB system is divided in half with 4 axes in half. The system is connected to the A70SF.
(4) *3: The A70MDF and the AY42 are optional equipment.

A70MDF : Necessary for monitoring, testing, and servo program preparation with the A6MD
AY42: Necessary for limit switch output

2.1.2 System configuration precautions

Take the following precautions when configuring the system in which the MR-SB servo amplifier is to be used.
(1) Limitations on the Base Unit

Load the A73CPU onto the A74B base unit (referred to as the A74B in this manual).

With the A73CPU loaded to the other base units (the A32B, A35B, A38B, and A78B), the A73CPU will not operate.
(2) Limitations on units loaded to the A74B

The units that can be loaded to the A74B are as listed below. Their loading positions are predetermined. See Fig. 2.1 for the loading positions.
(a) Power supply module : A61P
(b) CPU module : A73CPU
(c) Servo interface unit : A70SF
(d) Monitor display interface unit : A70MDF
(e) Output module (limit switch output) : AY42
(3) MR-SB connection

The eight MR-SB axes ( 4 axes each for 2 systems) can be connected to the A70SF loaded to the A74B.
(4) Extension base unit connected to the A74B
(a) The A65B or the A68B can be connected to the A74B.

Do not use the A55B or the A58B.
(b) The following number of extension base stages can be connected to the A74B.

For the SCPU:

- Up to 7 stages (for 1920 points) can be connected under the control of an SCPU sequence program.
- All the input/output modules of the A series and the special function modules can be loaded to the extension base for control purposes.
- It is necessary to set the number of stages with the extension stage number setting switch. (For the setting method, see Section 7.2.3.)
(5) Limitations on the use of extension cables

Up to 6.6 m ( 21.65 ft ) (total length) of extension cables can be used for the extension base.
(6) Absolute value system

For the MR-SB, the use of a servo amplifier that is capable of handling absolute values enables positioning control in the absolute value system.

## POINT

(1) Ground the MR-BUS[]M cable (connecting the A70SF and the MR-SB) of the A70SF to increase noise resistance. Use the cable clamp supplied with the A70AF.
(2) The servo amplifier must be grounded.

Ground LG and FG of the A73CPU system, too.
(3) Make external wiring connections for the following signals to be connected to the A70SF. Positioning control is disabled unless the signal is off (at high level).
(a) STOP: Stop signal
(b) FLS : Upper limit switch signal
(c) RLS : Lower limit switch signal
(d) EMG : Emergency stop signal

## 2. SYSTEM CONFIGURATION

### 2.2 When a General-Purpose Servo Amplifier Is Used

### 2.2.1 Overall configuration



Fig. 2.2 Overall Configuration Established When a General-Purpose Servo Amplifier is Used

## POINT

(1) The unit shown in Fig. 2.2 can be mounted on the A74B. The mounting position should be as indicated in Fig. 2.2.
(2) *1: SCPU extension connector (for connecting to the SCPU extension base)
(3) *2: PCPU extension connector (for connecting to the PCPU extension base)
(4) *3: The A70MDF and the AY42 are optional equipment. A70MDF : Necessary for monitoring, testing, and servo program preparation with the A6MD
AY42 : Necessary for limit switch output


### 2.2.2 System configuration precautions

Take the following precautions when configuring a system with general-purpose servo amplifier.
(1) Limitations on the Base Unit

Load the A73CPU onto the A74B base unit (referred to as the A74B in this manual).

With the A73CPU loaded to the other base units (the A32B, A35B, A38B, and A78B), the A73CPU will not operate.
(2) Limitations on units loaded to the A74B

The units that can be loaded to the A74B are as listed below. Their loading positions are predetermined. See Fig. 2.2 for the loading positions.
(a) Power supply module : A61P
(b) CPU module : A73CPU
(c) Servo interface unit : A70SF
(d) Monitor display interface unit : A70MDF
(e) Output module (limit switch output) : AY42
(3) General-purpose servo amplifier connections

On a one-to-one basis, connect the general-purpose servo amplifier to the A70AF which is loaded on the extension base unit (A65B or A68B) connected to the PCPU extension connector.

The general-purpose servo amplifier is capable of controlling up to 8 axes.

When the A70AF is used, it is necessary to supply $\pm 15$ VDC from the external power source.

## POINT

(1) When a general-purpose servo amplifier is used, connect the near-zero point signal to the A7OAF. Connect the following external signals to the A70SF.
(a) STOP : Stop signal
(b) FLS : Upper limit switch signal
(c) RLS : Lower limit switch signal
(d) DOG/CHANGE : Used as the velocity-position switching
(2) Make external wiring connections for the following signals to be connected to the A7OSF. Positioning control is disabled unless the signal is off (at high level).
(a) STOP : Stop signal
(b) FLS : Upper limit switch signal
(c) RLS : Lower limit switch signal
(3) When a general-purpose servo amplifier is used, EMG (emergency stop signal) from the A7OSF is ignored.
Request general-purpose servo amplifier emergency stop from the general-purpose servo amplifier.
(4) Absolute system disallowed

The general-purpose servo amplifier is not capable of positioning control in the absolute value system.
(5) Extension base connectable to the A74B
(a) Either the A65B or the A68B can be connected to the A74B.
(b) Up to the following number of extension base stages can be connected to the A74B.

For the PCPU:

- Only one stage of this base can be connected for loading the A70AF (interface unit for the general-purpose servo amplifier).
- Units other than the A70AF cannot be used for the PCPU extension base.
- Set the extension stage number setting switch to "1". (For the setting method, see Section 7.2.4.)
(6) Some of the A6MD monitor display functions cannot be performed when a general-purpose servo amplifier is connected.

Example:
Monitor mode : Servo monitor
Testing mode : Servo start-up
Servo diagnosis (position loop gain/velocity loop gain check)

For further details, see the A6MD Operating Manual IB (NA) 66235.

## IMPORTANT

As long as $\pm 15$ VDC remains at the A7OAF terminal block, the analog voltage (velocity command) to the servo amplifier can be delivered even when the PC power is turned off. When the A73CPU is turned off, the A70AF SVON signal (see Section 5.4.3) is turned off.
When the servo amplifier of which control can be stopped in response to the SVON signal is used, always connect the SVON signal from the servo amplifier to the ATOAF.
When the servo amplifier of which control cannot be stopped in response to the SVON signal is to be used, take the following precautions.

- When the PC is to be turned off, make sure that the analog voltage is 0 V (the motor is at rest).
- Before turning off the PC, set up an external circuit which turns off the $\pm 15$ VDC supply at the same time


### 2.3 When an MR-SB Servo Amplifier and a General-Purpose Servo Amplifier Are Used

### 2.3.1 Overall configuration



Fig. 2.3 Overall Configuration Established When an MR-SB Servo Amplifier and a General-Purpose Amplifier Are Used

## POINT

(1) The unit shown in Fig. 2.3 can be mounted on the A74B. The mounting position should be as indicated in Fig. 2.3.
(2) *1: SCPU extension connector (for connecting to the SCPU extension base)
(3) *2: Divide the MR-SB into 2 systems each having 4 axes before connecting it to the A70SF.
(4) *3: PCPU extension connector (for connecting to the PCPU extension base)
(5) *4: The A70MDF and the AY42 are optional equipment.

A70MDF : Necessary for monitoring, testing, and servo program preparation with the A6MD
AY42 : Necessary for limit switch output


### 2.3.2 System configuration precautions

Take the following precautions when establishing the system in which an MR-SB and a general-purpose servo amplifier are used.
(1) Limitations on the base unit

Load the A73CPU onto the A74B base unit (referred to as the A74B in this manual).

With the A73CPU loaded to the other base units (the A32B, A35B, A38B, and A78B), the A73CPU will not operate.
(2) Limitations on units loaded to the A74B

The units that can be loaded to the A74B are as listed below. Their loading positions are predetermined. See Fig. 2.3 for the loading positions.
(a) Power supply module
: A61P
(b) CPU module
: A73CPU
(c) Servo interface unit : A70SF
(d) Monitor display interface unit : A70MDF
(e) Output module (limit switch output): AY42
(3) Connection between the MR-SB and the general-purpose servo amplifier
(a) Connect the MR-SB to the A70SF loaded on the A74B.
(b) On a one-to-one basis, connect the general-purpose servo amplifier to the A7OAF which is loaded on the extension base unit (A65B or A68B) connected to the PCPU extension connector.
When using the A70AF, supply $\pm 15$ VDC from the external source.
(c) A total of up to 8 axes can be controlled by the MR-SB and the general-purpose servo amplifier.
(4) Extension base unit connected to the A74B
(a) The A65B or the A68B can be connected to the A74B.

Do not use the A55B or the A58B.
(b) The following number of extension base stages can be connected to the A74B.

1) For the SCPU:

- Up to 7 stages (for 1920 points) can be connected under the control of an SCPU sequence program.
- All the input/output modules of the A series and the special function modules can be loaded to the extension base for control purposes.
- It is necessary to set the number of stages with the extension stage number setting switch. (For the setting method, see Section 7.2.3.)


## 2) For the PCPU

Only one stage can be connected for loading the A70AF (interface unit for the general-purpose servo amplifier). Units other than the A70AF cannot be loaded on the extension base for the PCPU.

- Set the extension stage number setting switch to "1". (For the setting method, see Section 7.2.4.)
(5) Absolute value system
(a) For the MR-SB, the use of a servo amplifier that is capable of handling absolute values enables positioning control in the absolute value system.
(b) The general-purpose servo amplifier is not capable of positioning control in the absolute value system.
(6) Some of the A6MD monitor display functions cannot be performed when a general-purpose servo amplifier is connected.


## Example:

Monitor mode : Servo monitor
Testing mode : Servo start-up Servo diagnosis (position loop gain/velocity loop gain check)

For further details, see the A6MD Operating Manual IB (NA) 66235.

## POINT

(1) Positioning control of a total of up to 8 axes can be performed when the MR-SB and the general-purpose servo amplifier are used.
(2) When a general-purpose servo amplifier is used, connect the near-zero point signal to the A70AF. Connect the following external signals to the A70SF.
(a) STOP : Stop signal
(b) FLS : Upper limit switch signal
(c) RLS : Lower limit switch signal
(d) DOG/CHANGE : Used as the velocity-position switching
(3) The servo amplifier must be grounded.

Ground LG and FG of the A73CPU system, too.
(4) Make external wiring connections for the following signais to be connected to the A70SF. Positioning control is disabled unless the signal is off (at high level).
(a) STOP : Stop signal
(b) FLS : Upper limit switch signal
(c) RLS : Lower limit switch signal
(d) EMG : Emergency stop signal (for the MR-SB only)
(5) When a general-purpose servo amplifier is used, EMG (emergency stop signal) from the A70SF is ignored.

Request general-purpose servo amplifier emergency stop from the general-purpose servo amplifier.

## IMPORTANT

As long as $\pm 15$ VDC remains at the A70AF terminal block, the analog voltage (velocity command) to the servo amplifier can be delivered even when the PC power is turned off. When the A73CPU is turned off, the A70AF SVON signal (see Section 5.4.3) is turned off.

When the servo amplifier of which control can be stopped in response to the SVON signal is used, always connect the SVON signal from the servo amplifier to the ATOAF.
When the servo amplifier of which control cannot be stopped in response to the SVON signal is to be used, take the following precautions.

- When the PC is to be turned off, make sure that the analog voltage is 0 V (the motor is at rest).
- Before turning off the PC, set up an external circuit which turns off the $\pm 15$ VDC supply at the same time.


### 2.4 When the MELSECNET Data Link is Required

### 2.4.1 Overall configuration

This section describes the configuration required for the MELSECNET data link.
(For connection between the servo amplifier and the extension base, see Sections 2.1 through 2.3.)
(1) The A73CPUP21/R21 for the MELSECNET data link can be used for the master and local stations (see the hatched circles in Fig. 2.4).


Fig. 2.4 Data Link System

### 2.4.2 Precautions in system configuring

Take the following precautions when configuring a data link system.
(1) A73CPUP21/R21 independent system

This system is connected with a servo amplifier, extension base, and other equipment in the same manner as independent system (see Sections 2.1 through 2.3).
(2) MELSECNET data link system

The A73CPUP21/R21 can be used as the master and local stations of the MELSECNET data link system composed of the ACPUP21/R21 and the A7LMS.
(3) Limitations on $X / Y 0$ through $X / Y 7 F$
$X / Y 0$ through the $X / Y 7 F$ cannot be used in the data link system even when the extension base is not connected for the SCPU.

### 2.5 Peripheral Device Configuration

(1) Peripheral device for the PCPU

As the peripheral devices of the PCPU, the A6GPP, A6PHP and A6MD can be used.


Fig. 2.5 Peripheral Device Configuration for PCPU
(2) Peripheral device for the SCPU

All the A series peripheral devices can be used for the SCPU.


Fig. 2.6 Peripheral Device Configuration for SCPU

### 2.6 System Equipment

Table 2.1 List of Equipment

| Module |  | Type | Description | Occupied Points (I/O assignment) |
| :---: | :---: | :---: | :---: | :---: |
| CPU module |  | A73CPU | ```[Sequence function] Program capacity: 30K steps, I/O points: }204 T: 256, C: 256, D: 1024, M, L, S: }204``` | - |
|  |  | [Positioning function] <br> Program capacity: 13 K steps, Positioning point: about 400 points/axis (varying with each program) |  |
|  |  | A73CPUR21 | For coaxial data link (Master/local selectable) |  |
|  |  | A73CPUP21 | For optical data link (Master/local selectable) |  |
| PCPU <br> special- <br> pur- <br> pose <br> unit | MR-SB servo interface |  | A70SF | For connecting the MR-SB servo amplifier | - |
|  | Generalpurpose servo interface | A70AF | For connecting the general-purpose servo amplifier | - |
|  | Monitor display interface unit | A70MDF | For connecting the A6MD | - |
|  | Unit for limit switch output | AY42 | For 64-point 12/24 VDC transistor output unit, 0.1 A | 64 points (output) |
|  | Main base unit | A74B | 4 input/output modules loadable | - |
| Memory cassette |  | A3NMCA-O | Without IC-RAM memory | - |
|  |  | A3NMCA-2 | With IC-RAM memory 16 K bytes |  |
|  |  | A3NMCA-4 | With IC-RAM memory 32K bytes |  |
|  |  | A3NMCA-8 | With IC-RAM memory 64K bytes |  |
|  |  | A3NMCA-16 | With IC-RAM memory 128 K bytes |  |
|  |  | A3NMCA-24 | With IC-RAM memory 192K bytes |  |
|  |  | A3NMCA-40 | With IC-RAM memory 320K bytes |  |
|  |  | A3NMCA-56 | With IC-RAM memory 448K bytes |  |
| Memory | IC-RAM | 4KRAM | 8 K bytes (max. 3K steps) | - |
|  | EP-ROM | 4 KROM | 8K bytes (max. 3K steps) |  |
|  |  | 8KROM | 16 K bytes (max. 7 K steps) |  |
|  |  | 16 KROM | 32 K bytes (max. 15K steps) |  |
| Input module |  | AX10 | 16 points, 100 VAC | 16 points (input) |
|  |  | AX11 | 32 points, 100 VAC | 32 points (input) |
|  |  | AX20 | 16 points, 200 VAC | 16 points (input) |
|  |  | AX21 | 32 points, 200 VAC | 32 points (input) |
|  |  | AX40 | 16 points, 12/24 VDC | 16 points (input) |
|  |  | AX41 | 32 points, 12/24 VDC | 32 points (input) |
|  |  | AX42 | 64 points, 12/24 VDC | 64 points (input) |
|  |  | AX60 | 16 points, 100/110 VDC | 16 points (input) |

*1: Positioning control *2: Sequence control
M : Master station
R : Remote station

- : Indieates that it is usable.

| Current Consumption |  | System Used |  | Applicable System |  |  |  |  |  |  |  | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | independent | Coaxial Data Link |  |  | Optical Data Link |  |  | Computer Link |  |
| 5 VDC | 24 VDC |  | *1 | *2 | M | L | R | M |  | L | R |  |
| - | - | - | - | 0 | - | - | - | - | - | - | 0 | Memory cassette to be arranged separately. |
|  |  |  | 0 | - | $\bigcirc$ | $\bigcirc$ | - | - | - | - | - |  |
|  |  |  |  | - | - | - | - | 0 | $\bigcirc$ | - | - |  |
| - | - | 0 | - | 0 | 0 | 0 |  | - | $\bigcirc$ |  | $\bigcirc$ | Cable clamp supplied |
| - | - | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ |  | - | $\bigcirc$ |  | - |  |
| - | - | $\bigcirc$ | - | - | - | 0 |  | - | $\bigcirc$ |  | 0 |  |
| - | 0.08 A | 0 | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |  | 0 | 0 |  | 0 |  |
| - | - | 0 | 0 | 0 | 0 | $\bigcirc$ |  | 0 | 0 |  | 0 |  |
| - | - | - | 0 | 0 | 0 | $\bigcirc$ | - | $\bigcirc$ | 0 | - | $\bigcirc$ | With two memory sockets <br> A3NMCA-O can be loaded with IC-RAM or EP-ROM memory. <br> A3NMCA-2 to -56 can be loaded with only EP-ROM memory. |
| - | - | - | 0 | 0 | 0 | 0 | - | 0 | $\bigcirc$ | - | 0 |  |
| 0.06 A | - | - 0 |  | 0 | 0 | 0 | 0 | $\bigcirc$ | 0 | - | - |  |
| 0.11 A | - |  |  |  |  |  |  |  |  |  |  |  |
| 0.06 A | - |  |  |  |  |  |  |  |  |  |  |  |
| 0.11 A | - |  |  |  |  |  |  |  |  |  |  |  |
| 0.06 A | - |  |  |  |  |  |  |  |  |  |  |  |
| 0.11 A | - |  |  |  |  |  |  |  |  |  |  |  |
| 0.12 A | - |  |  |  |  |  |  |  |  |  |  |  |
| 0.06 A | - |  |  |  |  |  |  |  |  |  |  |  |

Table 2.1 List of Equipment (Continued)

| Module | Type | Description | Occupied Points (/O assignment) |
| :---: | :---: | :---: | :---: |
| Input module | AX70 | 16 points for sensor | 16 points (input) |
|  | AX71 | 32 points for sensor | 32 points (input) |
|  | AX80 | 16 points, 12/24 VDC, source loading | 16 points (input) |
|  | AX80E | 16 points, 12/24 VDC, source loading | 16 points (input) |
|  | AX81 | 32 points, 12/24 VDC, source loading | 32 points (input) |
|  | AX82 | 64 points, 12/24 VDC, source loading | 64 points (input) |


| Current Consumption |  | System Used |  | Applicable System |  |  |  |  |  |  |  | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Independent | Coaxial Data Link |  |  | Optical Data Link |  |  | Computer Link |  |
| 5 VDC | 24 VDC |  | ${ }^{*} 1$ | *2 | M | L | R | M |  | L | R |  |
| 0.06 A | - | - | - | - | - | $\bigcirc$ | - | - | $\bigcirc$ | - | - |  |
| 0.11 A | - |  |  |  |  |  |  |  |  |  |  |  |
| 0.06 A | - |  |  |  |  |  |  |  |  |  |  |  |
| 0.06 A | - |  |  |  |  |  |  |  |  |  |  |  |
| 0.11 A | - |  |  |  |  |  |  |  |  |  |  |  |
| 0.12 A | - |  |  |  |  |  |  |  |  |  |  |  |

## POINT

(1) The maximum parameter setting range of the memory cassette A3NMCA-16 is 96 K bytes. For details see Section 8.1.1. The unused memory area of this memory cassette may be used as extension file registers by using the SW[ ]GHP-UTLP-FN1.
(2) The maximum parameter setting range of the memory cassettes A3NMCA-24, 40, and 56 is 144 K bytes. For details see Section 8.1.1.
(a) A memory area of more than 114 KB can be used as an extension file register using the SW[ ]GHP-UTLP-FN1.
(3) Any conventional memory cassette A3MCA-[] (without N) can be used.

Table 2.1 List of Equipment (Continued)

| Module | Type | Description | Occupied Points (I/O assignment) |
| :---: | :---: | :---: | :---: |
| Output module | AY10 | 16 points, relay contact | 16 points (output) |
|  | AY10A | 16 points, relay contact, for independent contact output | 16 points (output) |
|  | AY11 | 16 points, relay contact, with surge suppression | 16 points (output) |
|  | AY11A | 16 points, relay contact, for independent contact output, with surge suppression | 16 points (output) |
|  | AY11E | 16 points, relay contact, (with fuse) | 16 points (output) |
|  | AY13 | 32 points, relay contact | 32 points (output) |
|  | AY13E | 32 points, relay contact, (with fuse) | 32 points (output) |
|  | AY22 | 16 points, triac for 2 A (with fuse) | 16 points (output) |
|  | AY23 | 32 points, triac for 0.6 A (with fuse) | 32 points (output) |
|  | AY40 | 16 points, 12/24 VDC transistor for 0.1 A | 16 points (output) |
|  | AY40A | 16 points, $12 / 24$ VDC transistor for independent contact output, 0.3 A | 16 points (output) |
|  | AY41 | 32 points, $12 / 24$ VDC transistor for 0.1 A | 32 points (output) |
|  | AY42 | 64 points, $12 / 24$ VDC transistor for 0.1 A | 64 points (output) |
|  | AY50 | 16 points, $12 / 24$ VDC transistor for 0.5 A (with fuse) | 16 points (output) |
|  | AY51 | 32 points, 12/24 VDC transistor for 0.5 A (with fuse) | 32 points (output) |
|  | AY60 | 16 points, 12/24/48 VDC transistor for 2 A (with fuse) | 16 points (output) |
|  | AY60E * 1 | 16 points, 12/24/48 VDC transistor for 2 A (with fuse) | 16 points (output) |
|  | AY60EP *1 | 16 points, 12/24 VDC transistor for 2 A with short and overheat protection functions | 16 points (output) |
|  | AY70 | 16 points ( $5 / 12 \mathrm{VDC}$ ) for TTL, CMOS | 16 points (output) |
|  | AY71 | 32 points ( $5 / 12 \mathrm{VDC}$ ) for TTL, CMOS | 32 points (output) |
|  | AY80 *1 | 16 points, $12 / 24 / 48$ VDC transistor for 0.5 A (with fuse) | 16 points (output) |
|  | AY80EP * 1 | 16 points, 12/24 VDC transistor 0.8 A with short and overheat protection functions | 16 points (output) |
|  | AY81 *1 | 32 points, 12/24 VDC transistor for 0.5 A | 32 points (output) |
|  | AY81EP *1 | 32 points, 12/24 VDC transistor for 0.8 A with short and overheat protection functions | 32 points (output) |
|  | AY82EP * 1 | 64 points, 12/24 VDC transistor for 0.1 A with short and overheat protection functions. | 64 points (output) |
| Dynamic combined I/O module | A42XY | 64 inputs, 64 outputs Dynamic scanning mode | 64 points (output) |



Table 2.1 List of Equipment (Continued)

| Module |  | Type | Description | Occupied Points <br> (I/O assignment) |
| :---: | :---: | :---: | :---: | :---: |
| Special function module | Single-axis positioning module | AD70 | For single-axis, velocity control, and velocity/position control <br> Analog voltage output ( 0 to $\pm 10 \mathrm{VDC}$ ) <br> The analog input type general-purpose servo amplifier may be used. | 32 (32 special points) |
|  | Positioning | AD71 | For positioning control Pulse chain output, 2 axes (independent, simultaneous 2 axes, linear interpolation) The stepping motor may be used in conjunction with the AD76. | 32 (32 special points) |
|  |  | AD71S1 | For positioning control (Dedicated for the MELDAS-S1 servo driver) Pulse chain output, 2 axes (independent, simultaneous 2 axes, linear interpolation) | 32 (32 special points) |
|  |  | AD71S2 | For positioning control, and velocity control Pulse chain output, 2 axes (independent, simultaneous 2 axes, linear interpolation) The stepping motor may be used in conjunction with the AD76. | 32 (32 special points) |
|  |  | AD72 | For positioning control <br> Analog voltage output ( 0 to $\pm 10 \mathrm{VDC}$ ) 2 axes (independent, simultaneous 2 axes, linear interpolation) | 48 <br> (First half: <br> 16 vacant points) <br> (Second half: <br> 32 special points) |
|  |  | AD76 | Driver for the stepping motor (Used in conjunction with the AD71 or the AD71S2) | 16 (16 vacant points) |
|  | Position detection module | A61LS | Absolute detection system <br> Resolution : Each rotation of the resolver $=4096$ divisions <br> Response speed : 6 ms or less | 48 <br> (First half: <br> 32 special points) (Second half: 16 vacant points) |
|  | High-speed counter | AD61 | Binary 24 bits, $1 / 2$ phase input, reversible counter 50 kpps, 2 channels | 32 (32 special points) |
|  |  | AD61S1 | Binary 24 bits, $1 / 2$ phase input, reversible counter <br> 1 phase... 10 kpps, 2 phases ... 7 kpps 2 channels | 32 (32 special points) |
|  | A/D converter | A68AD | 4 to $20 \mathrm{~mA} / 0$ to $\pm 10 \mathrm{~V}$ Analog input, 8 channels | 32 (32 special points) |
|  |  | A68ADS2 |  | 32 (32 special points) |
|  | D/A converter | A62DA | 4 to $20 \mathrm{~mA} / 0$ to $\pm 10 \mathrm{~V}$ Analog output, 2 channels | (32 special points) |
|  |  | A620AS 1 | 4 to $20 \mathrm{~mA}, 0$ to $20 \mathrm{~mA} / 1$ to $5 \mathrm{~V}, 0$ to $5 \mathrm{~V}, 0$ to 10 V Analog output, 2 channels |  |
|  | A/D, D/A converter | A84AD | 4 to $20 \mathrm{~mA} / 0$ to $\pm 10 \mathrm{~V}$ Analog I/O, 4 channels | 48 <br> (First half: 16 vacant points) (Second half: 32 special points) |
|  | Display Control | AD57 | CRT display, semigraphic Color/monochrome selectable | 64 (64 special points) |
|  |  | AD57S1 |  |  |
|  |  | AD57-S2 | Indication of plasma display (A6MD), semigraphic | 64 (64 special points) |
|  |  | AD58 | LCD display, semigraphic | 64 (64 special points) |


| Current Consumption |  | System Used |  | Applicable System |  |  |  |  |  |  |  | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Independent | Coaxial Data Link |  |  | Optical Data Link |  |  | Computer Link |  |
| 5 VDC | 24 VDC |  | * 1 | * 2 | M | L | R | M |  | L | R |  |
| 0.3 A | - | - | $\bigcirc$ | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | $\bigcirc$ | - |  |
| 1.5 A | - | - | - | - | - | - | - | - | - | $\bigcirc$ | - |  |
| 1.5 A | - | - | - | - | - | - | - | - | - | - | - |  |
| 1.5 A | - | - | $\bigcirc$ | - | - | - | - | - | - | - | - |  |
| 0.9 A | - | - | $\bigcirc$ | - | - | $\bigcirc$ | $\bigcirc$ | - | - | - | - |  |
| - | - | - | - | - | - | - | - | - | - | - | - |  |
| 0.8 A | - | - | $\bigcirc$ | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - | - | - |  |
| 0.3 A | - | - | - | - | - | - | - | - | - | - | - |  |
| 0.3 A | - | - | - | - | - | $\bigcirc$ | - | - | - | - | - |  |
| 0.9 A | - | - | - | - | - | - | - | - | - | - | - |  |
| 0.9 A | - |  |  |  |  |  |  |  |  |  |  |  |
| 0.6 A | 0.35 A | - | $\bigcirc$ | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - | - |  |
| 0.24 A | 0.53 A | - | - | - | $\bigcirc$ | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | - |  |
| 1.21 A | - | - | 0 | - | - | $\bigcirc$ |  | - | - |  | - |  |
| 1.55 A | - |  |  |  |  |  |  |  |  |  |  |  |
|  | - | - | - | $\bigcirc$ | - | - |  | - | 0 |  | - |  |
| 1.27 A | - | - | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ |  | $\bigcirc$ | $\bigcirc$ |  | - |  |

Table 2.1 List of Equipment (Continued)

| Module |  | Type | Description | Occupied Points (1/O assignment) |
| :---: | :---: | :---: | :---: | :---: |
|  | Memery card Centronics interface | AD59 | 32K bytes memory, battery backed May be connected with any printer conforming to Centronics Standards. | 32 (32 special points) |
|  |  | AD59S1 |  |  |
|  | Voice output | A11VC | Messages can be recorded/replayed on max. 60 channels. <br> $1,2,4$ or 8 seconds may be selected per channel. <br> Total recording time: 64 seconds | 16 points (output) |
| Data link module | Coaxial data link | A73CPUR21 | For master or local station <br> [Sequence function] <br> Program capacity : 30K steps <br> Number of inputs/outputs: 2048 <br> [Positioning function] <br> Program capacity : 13 K steps <br> Positioning point : about 400 points/axis <br> (varying with each program) | - |
|  |  | A3MCPUR21 | For master or local stationProgram capacity $\quad$ :Number of inputs/outputs:Nteps | - |
|  |  | A3HCPUR21 |  |  |
|  |  | A3NCPUR21 |  |  |
|  |  | A2NCPU(Si)R21 |  | - |
|  |  | A1NCPUR21 | For master or local station Program capacity : 6K steps Number of inputs/outputs: 256 | - |
|  |  | A0J2CPUR23 | For local station <br> Program capacity <br> Number of inputs/outputs$\quad: 7 K$ steps | - |
|  |  | AJ72R25 | For remote I/O station | - |
|  |  | A0J2CPUR25 | For remote I/O station | - |
|  |  | AJ71R22 | For tier 3 master station in three-tier system | 32 (32 special points) |
|  | Optical data link | A73CPUP21 | For master or local station <br> [Sequence function] <br> Program capacity: 30K steps <br> Number of inputs/outputs: 2048 <br> [Positioning function] <br> Program capacity : 13K steps <br> Positioning point : about 400 points/axis <br> (Varying with each program) | - |
|  |  | A3MCPUP21 | For master or local stationProgram capacityNumber of inputs/outputs::N | - |
|  |  | A3HCPUP21 |  |  |
|  |  | A3NCPUP21 |  |  |
|  |  | A2NPU(S1)P21 | For master or local station <br> Program capacity <br> Number of inputs/outputs 14 K steps <br>  <br>  <br>  <br>  <br>  <br>  <br>  <br> for S S $)$ | - |
|  |  | A1NCPUP21 | For master or local station Program capacity : 6K steps Number of inputs/outputs: 256 | - |
|  |  | A0J2CPUP23 | For local station <br> Program capacity : 7K steps <br> Number of inputs/outputs: 336 | - |



Table 2.1 List of Equipment (Continued)

| Module |  | Type | Description |  | Occupied Points (I/O assignment) |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Data <br> link <br> module | Optical data link | AJ72P25 | For remote I/O station |  | - |
|  |  | A0,2CPUP25 | For remote I/O station |  | - |
|  |  | AJ71 P22 | For tier 3 master station in three-tier system |  | 32 (32 special points) |
| Computer link module |  | AJ71C24 | Communicates data with the computer in the fixed format. <br> Transmission speed: 300 BPS to 19.2 KBPS One RS-232C, one RS-422 channels |  | 32 (32 special points) |
|  |  | AJ71C24-S3 |  |  |  |
| Intelligent communication module |  | AD51 | Allows max. 8 multitaskings of GPC-BASIC programs for data transfer between the PC and computer and control status monitoring. Data communication with the computer in free format. Two RS-232C, two RS-422 channels. |  | 48 <br> (First half: <br> 16 vacant points) <br> (Second half: <br> 32 special points) |
|  |  | AD51S3 |  |  |  |
| Terminal interface unit |  | AJ71C21 | Link unit used for data transmission based on either BASIC-function terminal interface or no procedure. <br> Transmission rate: 600 BPS to 19.2 KBPS 1 channel each for RS-232C and RS-422 |  | 32 (32 special points) |
|  |  | AJ71C21S1 |  |  |  |
| Multidrop data link module and units |  | AJ71 C22 | Multidropped with max. 8 slave stations to make bit data transfer. <br> For multidrop link master station. <br> Transmission speed: 38.4 KBPS <br> One RS-422 channel |  | 32 (32 special points) |
|  |  | A0J2C25 | For multidrop link remote I/O station |  | - |
|  |  | A0J2C214 | For multidrop link local station (May be used as the computer link or multidrop link master station in an AOJ2CPU system) |  | 64 |
| Interrupt module |  | Al61 | For specifying interrupt program execution. (16 interrupt inputs) |  | 32 (32 special points) |
| Dummy module |  | AG62 | 16, 32, 48 or 64 points may be selected. |  | Setting range (input [number of points set]) |
| Blank cover |  | AG60 | Dustproof cover for use in vacant slot |  | 16 (16 vacant points) |
| Power supply module |  | A61P | $\begin{array}{l:l} \hline \text { Input } & 100 / 200 \mathrm{VAC} \\ \text { Output } & 5 \mathrm{VDC} 8 \mathrm{~A} \\ \hline \end{array}$ | (For use in power supply siot) | $\square$ |
|  |  | A62P | Input $:$ $100 / 200 \mathrm{VAC}$ <br> Output 5 VDC 5 A <br>  24 VDC 0.8 A | (For use in power supply slot) |  |
|  |  | A63P | Input : 24 VDC <br> Output : 5 VDC 8 A | (For use in power supply siot) |  |
|  |  | A65P | $\begin{array}{ll} \hline \text { Input } & \text { 100/200 VAC } \\ \text { Output } & 5 \mathrm{VDC} 2 \mathrm{~A} \\ & 24 \mathrm{VDC} 1.5 \mathrm{~A} \end{array}$ | (For use in power supply slot) |  |
|  |  | A66P | $\begin{array}{lll} \hline \text { Input } & 100 / 200 \mathrm{VA} \\ \text { Output } & 24 \mathrm{VDC} & 1.2 \mathrm{~A} \end{array}$ | (For use in $1 / 0$ slot) | 16 (16 vacant points) |
| Extension base unit |  | A68B | Can accommodate $81 / O$ modules. |  | - |
|  |  | A65B | Can accommodate $51 / 0$ modules. |  |  |
| Extension cable |  | AC06B | 600 mm (23.62 in) long | For use between base unit | - |
|  |  | AC12B | 1200 mm (47.24 in) long |  |  |
|  |  | AC30B | 3000 mm (118.11 in) long |  |  |



Table 2.1 List of Equipment (Continued)

| Module |  | Type | Description | Occupied Points (I/O assignment) |
| :---: | :---: | :---: | :---: | :---: |
| Simulation switch |  | A6SW16 | 16 points simulation switch | - |
|  |  | A6SW32 | 32 points simulation switch |  |
| Others | Battery | A6BAT | IC-RAM backup | - |
|  | Fuse for AY22 | HP-70K | Plug type 7 A | - |
|  | Fuse for AY23 | HP-32 | Plug type 3.2 A |  |
|  | Fuse for AY50, AY80 | MP-20 | Plug type 2 A |  |
|  | Fuse for AY60 | MP-32 | Plug type 3.2 A |  |
|  | Fuse for AY60E | MP-50 | Plug type 5 A |  |
|  | Fuse for power supply | GTH-4 | Cartridge type 4 A |  |



Table 2.2 Peripheral Device List

| Unit | Description | Type | Current Consumption |  | Remarks |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 5 VDC | 24 VDC |  |  |
| Programming unit with CRT | Intelligent GPP | A6GPP. SET | - | - | Consists of the following models: |  |
|  |  |  |  |  | Type | Remarks |
|  |  |  |  |  | AGGPP | Programming unit with CRT Equipped with ROM writer, FDD and printer interface functions. |
|  |  |  |  |  | SW[ ]GP-GPPA | A series system disk |
|  |  |  |  |  | SW[ ]GP-GPPK | K series system disk |
|  |  |  |  |  | SWO-GPPU | User disk ( 3.5 inch, formatted) |
|  |  |  |  |  | AC30R4 | Cable for connection of CPU and A6GPP 3 m ( 9.84 ft ) length |
|  | Composite video cable | ACIOMD | - | - | Cable for connection of GPP and expanded monitor display. $1 \mathrm{~m}(3.28 \mathrm{tt})$ length. |  |
| Programming unit with LCD | Handy graphic programmer | A6HGP. SET | - | - | Consists of the following models: |  |
|  |  |  |  |  | Type | Remarks |
|  |  |  |  |  | A6HGP | - Programming unit with LCD <br> - Equipped with FDD, printer interface and memory card interface functions. |
|  |  |  |  |  | SW[ ]-HGPA | A series system disk |
|  |  |  |  |  | SW[ ]-HGPK | K series system disk |
|  |  |  |  |  | SWO-GPPU | User disk ( 3.5 inch, formatted) |
|  |  |  |  |  | AC30R4 | Cable for connection of CPU and A6PHP 3 m ( 9.84 ft ) length |
| Programming unit with plasma display | Plasma handy programmer | A6PHP. SET | - | - | Consists of the following models: |  |
|  |  |  |  |  | Type | Remarks |
|  |  |  |  |  | A6PHP | - Programming unit with plasma display <br> - Equipped with FDD, printer interface and memory card interface functions. |
|  |  |  |  |  | SW[ ]GP-GPPA | A series system disk |
|  |  |  |  |  | SW[ ]GP-GPPK | K series system disk |
|  |  |  |  |  | SWO-GPPU | User disk (3.5 inch, formatted) |
|  |  |  |  |  | AC30R4 | Cable for connection of CPU and A6HGP 3 m ( 9.84 ft ) length |
| Plasma display | Monitor display | A6MD | - | - | - Plasma display + operation key (touch key and general-purpose key) <br> For positioning data monitoring, servo programming, and testing <br> - Connectable to the A70MDF and the AD57S2 (Provided with the A70MDF/AD57S2 switch) |  |
| Common to programming units with CRT and LCD | $R S-422$cable | AC30R4 | - | - | Cable for connection of CPU and A6GPP/A6HGP/A6PHP |  |
|  |  | AC300R4 | - | - |  |  |
|  | User disk | SWOGPPU | - | - | User disk (3.5 inch, formatted) for storing programs |  |
|  | Cleaning disk | SWO-FDC | - | - | Cleaning disk for disk drive |  |

Table 2.2 Peripheral Device List (Continued)

| Unit | Description | Type | Current Consumption |  | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | 5 VDC | 24 VDC |  |
| For programming unit with CRT | Software package for PC servo | $\begin{aligned} & \text { SWOGP. } \\ & \text { A73P } \end{aligned}$ | - | - | - Servo programming and monitoring <br> - Data storage and printout <br> - Usable for the A6GPP/A6PHP |
| Printer | Printer | A6PRE | - | - | For print out of program ladder diagrams and lists. |
|  |  | A7PRE | - | - |  |
|  | $\begin{aligned} & \text { RS-232C } \\ & \text { cable } \end{aligned}$ | AC30R2 | - | - | Cable for connection of A6GPP/A6HGP/A6HGP and printer. 3 m ( 9.84 ft ) length. |
|  | Printer paper | K6PR-Y | - | - | Paper for K6PRE. 9 inch. Available in units of 2000. |
|  | K6PR (K) Ink ribbon | K6PR-R | - | $\sim$ | Replacement ink ribbon for A6PRE |
| Programming unit | Programming unit | A7PU | 0.3 A | - | - Connected to the CPU directly or via cable to read and write programs. Equipped with MT function. <br> - The A7PU is supplied with a cable for connection of the A7PU and audio cassette recorder. |
|  | $\begin{aligned} & \text { RS-422 } \\ & \text { cable } \end{aligned}$ | AC30R4 AC300R4 | - | - | Cable for connection of CPU and A7PU. $3 \mathrm{~m}(9.84 \mathrm{tt}) /$ 30 m ( 98.4 ft ) length. |
| P-ROM writer unit | P-ROM writer unit | A6WU | 0.8 A | - | - Used to store programs onto ROM and read programs from ROM to the CPU. <br> - Connected to the CPU directly or via the AC30R4 cable. |
|  | $\begin{aligned} & \text { RS-422 } \\ & \text { cable } \end{aligned}$ | $\begin{aligned} & \text { AC30R4 } \\ & \text { AC300R4 } \end{aligned}$ | - | - | Cable for connection of CPU and A6WU. $3 \mathrm{~m}(9.84 \mathrm{ft})$ / 30 m ( 98.4 ft ) length. |

## 3. GENERAL SPECIFICATIONS

Table 3.1 shows the general specifications for the A6MD.
Table 3.1 General Specifications

| Item | Specifications |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Operating ambient temperature | 0 to $55^{\circ} \mathrm{C}$ |  |  |  |  |
| Storage ambient temperature | -20 to $75^{\circ} \mathrm{C}$ |  |  |  |  |
| Operating ambient humidity | 10 to 00\% RH (dewing unallowable) |  |  |  |  |
| Storage ambient humidity | 10 to 90\% RH (dewing unallowable) |  |  |  |  |
| Vibration resistance | Conforms to -JIS C 0911 | Frequency | Acceleration | Amplitude | Sweep Count |
|  |  | 10 to 55 Hz | - | $\begin{aligned} & 0.075 \mathrm{~mm} \\ & (0.003 \mathrm{in}) \end{aligned}$ | 10 times <br> **(1 octave <br> /minute) |
|  |  | 55 to 150 Hz | 1 g | - |  |
| Shock resistance | Conforms to JIS C 0912 (10gx 3 times in 3 directions) |  |  |  |  |
| Noise durability | By noise simulator of 1500 Vpp noise voltage, $1 \mu$ s noise width and 25 to 60 Hz noise frequency |  |  |  |  |
| Dielectric withstand voltage | 1500 VAC for 1 minute across AC external terminals and ground 500 VAC for 1 minute across DC external terminals and ground |  |  |  |  |
| Insulation resistance | $5 \mathrm{M} \Omega$ or larger by 500 VDC insulation resistance tester across $A C$ external terminals and ground |  |  |  |  |
| Grounding | Class 3 grounding; grounding is not required when it is impossible. |  |  |  |  |
| Operating atmosphere | Free of corrosive gases. Dust should be minimal. |  |  |  |  |
| Cooling method | Self-cooling |  |  |  |  |

## REMARKS

One octave marked ** indicates a change from the initial frequency to double or half frequency. For example, any of the changes from 10 Hz to 20 Hz , from 20 Hz to 40 Hz , from 40 Hz to 20 Hz , and 20 Hz to 10 Hz are referred to as one octave.

Note: *JIS: Japanese Industrial Standard

MEMO

## 4. CPU MODULE

This section describes the performance, function, part identification and settings, and input/output interface specifications.

For details on the performance and functions of the A73CPU, see the A73CPU Multi-axis Positioning Unit Reference Manual IB (NA) - 68142.

### 4.1 Performance

Tables 4.1 and 4.2 detail the performance of the A73CPU (PCPU and SCPU).
Table 4.1 PCPU Performance Specifications List

| Item | Type | PCPU |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Number of control axes |  | 8 axes (2 simultaneous, 3 simultaneous, and 8 independent) |  |  |  |  |
| Interpolation function |  | Linear interpolation (2 and 3 axes) and circular interpolation (2 axes) |  |  |  |  |
| Control system |  | PTP (point-to-point), velocity control, velocity-position control, fixed-rate feed, and constant velocity control |  |  |  |  |
| Control unit |  | mm, inches, degrees, pulses |  |  |  |  |
| Program | Language | Dedicated instruction |  |  |  |  |
|  | Capacity | 13K steps (13312 steps) |  |  |  |  |
|  | Number of positioning points | About 400 points/axis (varies with the program) Indirect designation of positioning data enabled. |  |  |  |  |
|  | Setting method | Set by an A6GPP or A6PHP started up by the A6MD or SWOGHP-A73P. |  |  |  |  |
| Positioning | Method | PTP : Can be switched between absolute and increment <br> methods  <br> Velocity-position control : Increment method <br> and fixed-rate feed  <br> Constant velocity control : Absolute and increment methods mixed |  |  |  |  |
|  | Position command | The command unit is selected for each axis from the following units. |  |  |  |  |
|  |  | Control unit | Command unit | Non-absolute value system |  | Absolute value system |
|  |  |  |  | Setting range | Maximum setting value | Setting range |
|  |  | mm | $\times 10^{-1}$ um | $\begin{aligned} & -2^{34} \text { to } \\ & \left(2^{31}-1\right) \end{aligned}$ | 429 m | -196596000 to 196596000 |
|  |  | inch | $\times 10^{-5}$ inch |  | 42949 inches | -196596000 to 196596000 |
|  |  | degree | $\times 10^{-5}$ degree |  | 360 degrees | 0 to 35999999 |
|  |  | pulse | pulse |  | $\begin{aligned} & 4294967296 \\ & \text { pulses } \\ & \hline \end{aligned}$ | -196596000 to 196596000 |
|  | Velocity command | 0.01 to $6000000.00(\mathrm{~mm} / \mathrm{min})$ <br> 0.001 to 600000.000 (inches $/ \mathrm{min}$ ) <br> 0.001 to 600000.000 (degrees/min) <br> 1 to 1000000 (pulses $/ \mathrm{sec}$ ) |  |  |  |  |
|  | Acceleration/ deceleration process | Automatic trapezoidal acceleration and deceleration Acceleration time ....... 1 to 65535 (ms) Deceleration time ....... 1 to 65535 (ms) |  |  |  |  |
| Com-pensation | Electronic gear gear | (0 to 65535) x position command unit (the unit is converted to pulses; 0 to 255 pulses) |  |  |  |  |
|  | Backlash compensation | Actual travel distance error compensation function relative to the command data |  |  |  |  |

Table 4.1 PCPU Performance Specifications List (Continued)

| Type <br> Item | PCPU |
| :---: | :---: |
| Zero return function | Non-absolute value system: Switched between near-zero point dog and count types Absolute value system : Data set |
| Jog operation function | Provided |
| MPG operation function | Up to 3 units can be connected; 3 axes/unit controllable |
| M function | M code; output function provided |
| Limit switch output function | 8 points per axis; Up to 10 ON/OFF set points selectable |
| Absolute value system | Provided (optional) |

## 4. CPU MODULE

Table 4.2 SCPU Performance Specifications List


Table 4.2 SCPU Performance Specifications List (Continued)

| Type | SCPU |
| :--- | :--- |
| Item | Max. 4032 (Specity in batches of 64 points) |
| Comment | WDT error monitor, memory error detection, CPU error detection, <br> l/O error detection, battery error detection, etc. |
| Self-diagnostic functions | STOP/CONTINUE |
| Operation mode at the time of error | Output data at time of STOP restored/data output after operation ex. <br> ecution |
| STOP $\rightarrow$ RUN output mode |  |

## REMARKS

- Indicates items not in the A3NCPU specifications.


### 4.2 Functions List

Tables 4.3 and 4.4 detail the functions of the A73CPU (PCPU and SCPU).

Table 4.3 PCPU Functions List

| Function |  | *1 | *2 | *3 | * 4 | Function Outline |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Position. ing | Positioning | $\bigcirc$ | - | $\bigcirc$ | - | - Positioning control at velocity 1 <br> - Switched between absolute and increment modes |
|  | Fixedrate foed | $\bigcirc$ | $\bigcirc$ | $\bigcirc$ | $\times$ | - Setting the travel enables infinitely repeated positioning control for the fixed distance. |
|  | Velocity control | - | $\times$ | x | x | - Once a start command is received, operation is controlled at the designated velocity until a stop command is received. |
|  | Velocityposition control | - | x | $x$ | x | - Once a start command is received, operation starts at the controlled velocity. The set travel is controlled in relation to position starting from the instant the external position switching input signal (CHANGE) is received from the external source. <br> - Changing of the set travel distance and restart after interruption can be accomplished while the velocity is being controlled. |
|  | Velocity switching control | - | $\bigcirc$ | $\bigcirc$ | x | - Once a start command is received, positioning proceeds while the operating velocity is being adjusted to the preset rate at the predetermined points. |
|  | Constant velocity contro | - | $\bigcirc$ | - | $\bigcirc$ | - Once a start command is received, positioning proceeds at the constant velocity until the end point is reached while the preset position control (randomly selected from circular, linear, and other forms of control) is being performed toward the predetermined pass point. <br> - The repeating command (FOR/NEXT) enables repeating of the same control. |
| Jog operation |  | $\bigcirc$ | x | x | x | - Jog operation is enabled while the jog start signal (Yn2/Yn3) remains on. <br> - Simultaneous start of the jog operation (chosen for each axis between forward and reverse rotations for up to 8 axes) is enabled. |
| MPG operation |  | $\bigcirc$ | $\times$ | $x$ | $\times$ | - Pulse input with the MPG enables positioning in accordance with the number of input pulses. <br> - It is possible to perform up to 3 -axis independent and 3 -axis simultaneous MPG operation. |
| Zero return |  |  |  |  |  | - The zero return/start command enables zero return. The present position data obtained at the time of stop is corrected to the zero point. <br> - The zero return method can be selected from the near-zero point dog, counting, and data setting types. |
| Simultaneous start |  |  |  |  |  | - Up to 3 servo programs for positioning control, zero return, and other functions can be started simultaneously. |
| Control change | Velocity change |  |  |  |  | - Velocity can be changed during positioning control and Jog operation. However, velocity changes are not allowed during circular interpolation and zero return. |
|  | Travel distance change |  |  |  |  | - For positioning control based on velocity and positioning, the preset travel distance can be changed during velocity-controlled operation after the input of the position switching input signal (CHANGE). |
|  | Present position data change |  |  |  |  | - The present position data can be changed during stop. |
| M code |  |  |  |  |  | - For positioning control, an M code ( 0 to 255) can be delivered. <br> - For velocity switching control, an M code can be set at each velocity switching point. <br> - For constant velocity control, an M code can be set at each pass point. |

*1: 1-axis linear interpolation
*3: 3-axis linear interpolation

2: 2-axis linear interpolation
*4: 2-axis circular interpolation

Table 4.3 List of PCPU Functions (Continued)

| Function | Function Outline |
| :--- | :--- |
| Backlash compensation | - Backlash compensation process can be performed at each startup. <br> - The amount of backlash compensation is set on a peripheral <br> device. |
| Electronic gear | - The difference between the command position data and the actual <br> (travel distance can be compensated. |
| Torque limit | Effective for all operation. |

## POINT

(1) - Torque limit values can be changed only when an MR-SB is used.
When an general-purpose servo amplifier is used, the setting of torque limit values is ignored.

Table 4.4 List of SCPU Functions

| Function | Description |
| :---: | :---: |
| Constant scan | - Executes the sequence program at the predetermined intervals independently of the scan time. <br> - Setting allowed between 10 and 1990 ms . |
| LATCH | - Retains device data if the PC is switched off or reset of instantaneous power fallure occurs 20 ms or longer. <br> - L, B, T, C, D and W can be latched. |
| Remote RUN/STOP | - Allows remote run/stop from external device (e.g. peripheral, external input, computer) with RUN/STOP switch in RUN position. |
| PAUSE | - Stops operation with the output $M$ status retained. <br> - Pause function may be switched on by any of the following ways: <br> - RUN/STOP switch on the front of the CPU <br> - Remote pause contact <br> - Peripheral |
| Status latch | - Stores all device data to the status latch area of the memory cassette when the status latch condition is switched on. <br> - The stored data can be monitored by the peripheral. |
| Sampling trace | - Samples the specified device operating status at predetermined intervals and stores the sampling result in the sampling trace area of the memory cassette. <br> - The stored data can be monitored by the peripheral. |
| STEP RUN | - Executes the program one instruction at a time. <br> - Step run may be executed in either of the two ways: <br> - By specifying the loop count. <br> - Per instruction |
| Online I/O module replacement | - Allows any I/O module to be changed with the CPU running (power on). |

### 4.3 Handling

This section gives handling instructions, part identification and hardware setting instructions.

### 4.3.1 Handling instructions

(1) Do not subject the CPU module and memory cassette to impact or shock.
(2) Do not remove printed circuit boards from the housing. There are no user-serviceable parts on the boards.
(3) Ensure that no conductive debris can enter the module. If it does, make sure that it is removed. Guard particularly against wire offcuts.
(4) Tighten the module mounting and terminal screws as specified below.

| Screw | Tightening Torque kg-cm (lb-in) |
| :--- | :--- |
| Module terminal block installation screws (M4) | $10(8.66)$ to 14 (12.13) |
| Module mounting screws (optional) (M4) | $8(6.93)$ to 12 (10.39) |

(5) To load the module onto the base, hook the two lower lugs into the cut out and gently swing the module into place. Ensure that the top catch engages. To remove, press the top catch and swing the module out before unhooking the lower lugs. (See Section 9.5.)

### 4.3.2 Part identification and setting of A73CPU

## (1) Part identification

|  |  |  |
| :---: | :---: | :---: |
| No. | Name | Description |
| (1) | LED display | Displays up to 16 alphanumeric characters. <br> May be addressed from the user program using the LED commands and/or displays self-diagnosed error messages. |
| (2) | Reset key switch | RESET : Hardware reset. Used to reset the CPU after an operation error and to initialize operation. The latch memory is not cleared when the CPU is reset. <br> LATCH CLEAR : Sets all latch area data (as defined in parameters) to OFF or 0. (Valid when the CPU is in STOP status) |
| (3) | LED display reset switch | Clears the present LED annunciator message. The next message in the annunciator queue is then displayed where appropriate. |
| (4) | RUN LED | Indicates the run status of the CPU. <br> On : The CPU is in RUN or STEP-RUN status, no operation errors have occurred, the program is being run and the PC is active. <br> Off : The CPU is in STOP, PAUSE or STEP-RUN status and the program is not being run. <br> Flicker: Self-diagnosed error has occurred. (Operation will continue if the error detected has been specified in the parameter setting.) |
| (5) | RUN/STOP switch | RUN/STOP: To start/terminate running the PC program. PAUSE : To terminate running the PC program and maintain output status. STEP.RUN: To run the program step by step/scan by scan. |
| (6) | I/O control switch | Used to select direct/refresh mode. |
| (7) | Memory cassette loading connector | Used to connect the memory cassette to the CPU. |
| (8) | RS422 connector | Peripheral programmer port. Fit cover (supplied) when not in use. |

(2) Settings
(a) Set the I/O control switch to direct or refresh mode as indicated below.

| Switch Setting | Input ( $\times$ ) | Output (Y) | D9014 Value |
| :---: | :---: | :---: | :---: |
|  | Direct | Direct | 0 |
|  | Refresh | Direct | 1 |
|  |  |  |  |
|  | Refresh | Refresh | 3 |

## POINT

(1) The 1/O control switch must be set with power off.
(2) After the switch has been set, the switch status is checked every time the CPU is powered up or reset. Direct input/refresh output setting is processed as refresh input/refresh output (No. 3 of the above) by the CPU.
(3) The BIN value in special register D9014 can be monitored by the peripheral.
(b) For IC and memory cassette settings, see Section 9.2.

### 4.3.3 Part identification and setting of A73CPUP21/R21



| (3) | MODE | Mode select switch <br> By switching mode, the following functions are available. |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  | Setting Number | Name | Description |
|  |  | 0 | Online | Automatic return set during normal operation. |
|  |  | 1 | Online | Automatic return not set during normal operation. |
|  |  | 2 | Offline | Disconnect host station. |
|  |  | 3 | Test mode 1 | Forward loop test |
|  |  | 4 | Test mode 2 | Reverse loop test |
|  |  | 5 | Test mode 3 | Station-to-station test (main station) |
|  |  | 6 | Test mode 4 | Station-to-station test (subordinate station) |
|  |  | 7 | Test mode 5 | Self-loopback test |
|  |  | 8 | - | Not used |
|  |  | 9 | - | Not used |
|  |  | A | - | Not usable |
|  |  | B | - | Not usable |
|  |  | C to F | - | Not used |
| (4) |  | Optical fib Conne <br> OUT IN <br> Master stat | cable conne the cable as <br> Front <br> Equipment | tor shown below. <br> No. 1 Equipment No. 2 <br> IN : Connect to OUT of preceding atation OUT: Connect to IN of next station |
| (5) |  | Coaxial ca Conne <br> Master | le connector the cable as <br> Equipm <br> -sending -receiving JT-sending UT-receiving | shown below. <br> nt No. 1 Equidment No. 2 <br> Connect to OUT-receiving of preceding station Connect to OUT-sending of preceding station Connect to IN-receiving of succeeding station Connect to IN -sending of succeeding station |

(1) Section-to-Section Setting
(a) The following three items can be set for the A73CPUP21/R21 in the data link system.

1) Station Number Setting with the Station Number Setting Switch

- When a given station is to be used as a master station, set the switch to 00.
- When a given station is to be used as a local station, select the station number between 01 and 64 .

2) Mode Setting with the Mode Select Switch

Select the appropriate operation and self-diagnosis test states.
3) Link Parameter Setting on the GPP/HGP/PHP

Set the link parameters on the A73CPUP21/R21 which is in use in the two-tier master station and the two-tier local station provided with the AJ71P22/R22.

## MEMO

## 5. POSITIONING UNITS

The positioning units are those required for positioning control by the A73CPU.
(1) A70SF : For connecting to the MR-SB servo amplifier and fetching signals (upper and lower limit switch input, stop signal, etc.) from external sources.
(2) A70MDF: Interface unit to be connected to the A6MD monitor display unit.
(3) ATOAF : Interface unit to be connected with the general-purpose servo amplifier.
(4) AY42 : For delivering limit switch output. (For the specifications, see the Building Block Input/Output Module User's Manual.)

### 5.1 Handling Precautions

This section describes the precautions to be taken when the positioning unit (see Sections 5.2 through 5.4) is unpacked and installed.
(1) Do not subject the module, memory cassette, terminal block connector and pin connector to impact or shock.
(2) Do not remove printed circuit boards from the housing. There are no user-serviceable parts on the boards.
(3) Ensure that no conductive debris can enter the module. If it does, make sure that it is removed. Guard particularly against wire offcuts.
(4) Tighten the module mounting and terminal screws as specified below.

| Screw | Tightening Torque <br> kg-cm (1b-in) |
| :--- | :--- |
| Module terminal block installation screws (M4) | $10(8.66)$ to 14 (12.13) |
| Module mounting screws (optional) (M4) | $8(6.93)$ to 12 (10.39) |

(5) To load the module onto the base, hook the tow lower lugs into the cut out and gently swing the module into place. Ensure that the top catch engages. To remove, press the top catch and swing the module out before unhooking the lower lugs. (See Section 9.5)

### 5.2 A70SF Servo Amplifier Interface Unit

The A70SF servo amplifier interface unit is connected to the MR-SB, upper and lower limit switch, stop command, emergency stop command, near-zero point dog/velocity-position switching command, and MPG.

The A70SF specifications and part identification is listed below.

### 5.2.1 Specifications

The A70SF specifications are listed in Table 5.1.

Table 5.1 A70SF Specifications

| Item |  | Specifications |
| :---: | :---: | :---: |
| Connection to the MR-SB | Number of axes controlled | 8 max. |
|  | Connecting method | 2 systems (4 axes/system) |
|  | Command velocity | 1 MBPS max. |
|  | Connecting distance | 30 m (98.43 ft) max./system |
| Connection with external inputs | Connected to: | Upper and lower limit switches |
|  |  | Stop command |
|  |  | Near-zero point dog/velocity-position switching command |
|  |  | Emergency stop command |
|  | Supply voltage | 5 VDC to 24 VDC (4.75 VDC to 26.4 VDC; stabilized power supply to be used) 330 mA (1 point: 10 mA ) |
|  | HiGH level | 3.5 VDC or more/1.2 mA or more |
|  | LOW level | 1.5 VDC or less $/ 0.3 \mathrm{~mA}$ or less |
| Connection to the MPG | Number of connections | 3 max. |
|  | Rated input voltage | 5.5 VDC or less |
|  | HIGH level | 4 VDC or more/3 mA or more |
|  | LOW leve] | 1.5 VDC or less/0.3 mA or less |
| Dimensions (mm) (in) |  | $250(\mathrm{H}) \times 75.5(\mathrm{~W}) \times 121$ (D) (9.84 $\times 2.97 \times 4.76)$ |
| Weight (kg) (lb) |  | 1.12 (2.46) |

### 5.2.2 Part identification



| No. | Name | Application |
| :---: | :---: | :---: |
| (9) | Input LED | (1) Indicates the FLS, RLS, STP, and DOG input status of each axis. <br> $\begin{array}{ll}\text { (a) FLS } \\ \text { (b) RLS } & \text { Upper limit } \\ \text { Lower limit }\end{array}$ <br> (b) RLS :Lower limit <br> (c) STOP :Stop signal <br> (d) DOG :Near-zero point dog/velocity-position switching signal |
| (10) | Input terminal block | Terminal block for inputting FLS, RLS, STP and DOG of each axis, MPG1 to 3 (P1 to P3) and EMG. <br> (a) FLS <br> :Upper limit <br> (b) RLS <br> :Lower limit <br> (c) STOP <br> :Stop signal <br> (d) DOG <br> (a) P1 through P3 <br> :Near-zero point dog/velocity-position switching signal <br> :MPG (phase A/phase B) inputs <br> (f) EMG <br> :Emergency stop input |

## 5. POSITIONING UNITS

5.2.3 Interface with external device

The interface between the A70SF and external devices is indicated in Table 5.2.

No reference is made to the interface area for connecting the ATOSF and an MR-SB.

Table 5.2 Interface Between the A70SF and External Devices


## POINT

The use of a twisted-pair, shielded wire for the MPG signal wire is recommended.


### 5.3 A70MDF Monitor Display Unit

This unit is used to connect the A70MDF. It is required when the A6MD is used.
This section describes the A70MDF specifications, part identification, and the connection between the A70MDF and the A6MD.

### 5.3.1 Specifications

The A70MDF specifications are indicated in Table 5.3.
Table 5.3 A7OMDF Specifications

| Item | Specifications |
| :--- | :---: |
| To be connected | A6MD monitor display unit |
| Dimensions $(\mathrm{mm})(\mathrm{in})$ | $250(\mathrm{H}) \times 37.5(\mathrm{~W}) \times 121(\mathrm{D})(9.84 \times 0.15 \times 4.76)$ |
| Weight $(\mathrm{kg})(\mathrm{lb})$ | $0.57(1.25)$ |

The functions which are performed by the A6MD connected with the A70MDF are listed in Table 5.4.

Table 5.4 List of A6MD Functions

| Mode | Function | Application |  |
| :---: | :---: | :---: | :---: |
|  |  | MR-SB Servo Amplifier | General-purpose Servo Amplifier |
| Data setting mode | Setting, copying, checking, listing, and clearing of: <br> Fixed parameters <br> Servo parameters <br> Zero return data <br> Jog operation data <br> Parameter blocks | - | - |
|  | Limit switch point setting |  |  |
| Programming mode | Servo program read | - | $\bigcirc$ |
|  | Servo program write |  |  |
|  | List of servo programs used |  |  |
|  | Servo program check |  |  |
|  | Servo program sort |  |  |
|  | Servo program copy |  |  |
|  | Axis number batch change |  |  |
|  | Servo program areas |  |  |
| Monitor mode | Present position data monitor | - | - |
|  | Error list display |  |  |
|  | Axis monitor | - | $\Delta$ |
|  | Seroll monitor | 0 | - |
| Test mode | Servo start-up | - | $\times$ |
|  | Servo diagnosis |  |  |
|  | Jog operation | - | - |
|  | MPG operation |  |  |
|  | Zero return test |  |  |
|  | Servo program test operation |  |  |
|  | Teaching |  |  |
|  | Error reset |  |  |
|  | Present position date change |  |  |

a : Indicates that operation is allowed.
$x:$ Indicates that operation is disallowed.
$\Delta$ : Indicates that operation is partially disallowed.

### 5.3.2 Part identification



### 5.3.3 External wiring

This section describes the precautions to be taken when connecting the A70MDF with the A6MD and cable connecting method.
(1) Wiring precautions

To obtain optimum performance from the A6MD connected to the A70MDF and make a highly reliable system, external wiring that is resistant to noise is indispensable.

Precautions in making external wiring connections between the A70MDF and the A6MD are as follows.
(a) Do not bring the A6MD and the A70MDF connecting cable close to the whole circuit line, high-voltage wire, or load wire from units other than the A73CPU or the PC CPU. Do not bundle the connecting cable with line or wire.

If this instruction is not followed, the cable will be subject to excessive noise, surges, or induction.
(b) Run external AC and A6MD power cables separately to minimize the effect of surges or induction from the AC side.
(c) After connecting all interfaces, tighten the fixing screws.
(d) Use shielded cable for connections. Ground each shielded cable to A70MDF FG terminal.

## (2) Cable connections

This section describes the method of connecting the cable between the A70MDF and the A6MD to the connector plug.



- The shape of the connector and the number of screws differ from one interface section to another.

- Pin arrangement differs from one connector to another.
(3) Wiring connections for the plasma display of the A7OMDF and the A6MD

(4) Wiring connections for touch-key panel and operation panel



### 5.4 A70AF General-Purpose Servo (Analog) Interface Unit

The A70AF is connected to the general-purpose servo amplifier.
The A70AF and the general-purpose servo amplifier are connected on a one-to-one basis.
The specifications, part identification, and settings for the A70AF are described below.

### 5.4.1 Specifications

The A70AF specifications are listed in Table 5.5.
Table 5.5 Specifications

| Item | Specifications |
| :--- | :--- |
| Number of controlled <br> axes | 1 |
| Velocity command out- <br> put | 0 to $\pm 10$ VDC (can be set in the range $\pm 5$ to $\pm 10 \mathrm{~V}$ after adjustment) |
| Positioning feedback <br> pulse input | Pulse frequency: 100 KPPS <br> Type of encoder connected: Open collector, TTL, differential output <br> Pulse multiplication ratio: The number of feedback pulse inputs can be multiplied by <br> $4,2,1$, and $1 / 2$. |
| Internal current con- <br> sumption | $5 \mathrm{VDC}, 0.3 \mathrm{~A}$ |
| External supply volt- <br> age and current | $+15 \mathrm{VDC}, 0.2 \mathrm{~A},-15 \mathrm{VDC}, 0.02 \mathrm{~A}$ |
| Dimensions $(\mathrm{mm})$ (in) | $250(\mathrm{H}) \times 3.75(\mathrm{~W}) \times 121$ (D) $(9.84 \times 0.15 \times 4.76)$ |
| Weight $(\mathrm{kg})(\mathrm{lb})$ | $0.5(1.1)$ |

## POINT

(1) $\pm 15$ VDC are supplied to the A7OAF by one of the following methods.
(a) With an A68P power supply module referred to as the A68P:

The A68P can supply $\pm 15$ VDC to 6 units of the A70AF (see Section 6).
Load the A68P on the PCPU extension base unit or the SCPU extension base unit.
(b) Without an A68P power supply module:

To supply the required voltage to the A7OAF without an A68P, the following power requirements should be met.

| Item |  |
| :--- | :--- |
| Voltage | Specifications |
|  | $+15 \mathrm{VDC} \pm 3 \%(14.55 \mathrm{~V}$ to 15.45 V$)$ |
| Current (for 1 <br> A70AF unit) | $+15 \mathrm{VDC} \pm 3 \%(-14.55 \mathrm{~V}$ to $-15.45 \mathrm{~V})$ |
|  | +15 VDC |
| Ripple voltage | 0.2 A |
| Spike voltage | 0.02 A |
| Transient output fluctuations | $50 \mathrm{mVp}-\mathrm{p}$ or less |

### 5.4.2 Part identification



### 5.4.3 LED indication

This section describes the LEDs on the front of the A70AF that indicate the RUN status and error status.


| Details of LED Section | LED Name |  | Status Indicated by LED | Condition for ON |  | Condition for OFF |  |  |  |  | 4 Initial Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | WDT ERR | WDT error | Indication of A70AF watchdog timer | WDT error ( $\times 00$ ); ON when the hardware is faulty. |  | WDT error (XOO); OFF when the hardware is normal. |  |  |  |  | OFF |
|  | AXIS | Selected axis number | The axis number selected with the slide switch is indicated. |  |  |  |  |  |  |  | With the slide switch |
|  |  |  |  | \| Leo Axiz 1 | $2$ | 4 | 5 | 6 | 7 | 8 |  |
|  |  |  |  | $\bullet$ | 0 | $\bigcirc$ | - | 0 | $\bigcirc$ | 0 |  |
|  |  |  |  | 20 | - | 0 | 0 | - | - | $\bigcirc$ |  |
|  |  |  |  | 4 l | 010 | - | - | - | - | $\bigcirc$ |  |
|  |  |  |  | 80 | 010 | 0 | 0 | 0 | 0 | $\bullet$ |  |

*1 The initial condition refers to the CPU STOP status reached when the power switch is turned from OFF to ON.

### 5.4.4 Input/output interface with an external device

The input/output interface between the A70AF and an external device is summarized in Table 5.6.

Table 5.6 Input/Output Interface Between the A70AF and an External Device


## POINT

The use of a twisted-pair, shielded cable as the feedback pulse signal line to the A70AF is recommended.

| Specifications | Description |
| :---: | :---: |
| Supply power <br> 5 to 24 VDC ( 4.75 to 25.4 VDC; stabilized power <br> supply to be used) <br> HIGH level <br> 3.5 VDC or more/1.2 mA or more <br> LOW level <br> 1.5 VDC or less/0.3 mA or less | This signal is used to detect the near-zero point during zero return. Turned on when the near-zero point dog is detected. |
| Supply power <br> 5 to 24 VDC (4.75 to 25.4 VDC; stabilized power <br> supply to be used) <br> HIGH level <br> 3.5 VDC or more/1.2 mA or more <br> LOW level <br> 1.5 VDC or less/0.3 mA or less | Turned on when the servo drive unit is normal and ready to receive the feed pulse. |
| Output format: Open collector system; <br> Max. voltage drop in ON: 1.0 V or fess <br> Load voltage: 4.75 to 26.4 VDC; <br> Leakage current in OFF: 0.1 mA or less Load current: 30 mA max. | OFF signal is delivered when an error is exaggerated or generated during hardware self-diagnosis. |
| Output voltage: 0 to $\pm 10 \mathrm{VDC}(10 \mathrm{~mA})$ | The number of accumulated pulses is D/A converted and delivered as an analog amount. |
| Pulse frequency : 100 KPPS or less Use an RS-422 differential receiver. Use a SN75113 driver or equivalent. | Connected to the encoder pulse output. |
| Pulse frequency $: 100 \mathrm{Kpps}$ or less <br> Pulse rise time $: 1 \mu s e c$ or less <br> Pulse fall time $: 1 \mu$ sec or less <br> HIGH level $: 4 \mathrm{~V}$ or more <br> LOW level $: 1 \mathrm{~V}$ or more | Internally pulled up to 12 V when the collector is open. Connected to the encoder pulse output. |
| Pulse frequency $: 100$ Kpps or more <br> HIGH level $: 2.8 \mathrm{~V}$ or more <br> LOW level $: 0.8 \mathrm{~V}$ or more | Connected to the encoder pulse output. |
| $\begin{aligned} & +15 \mathrm{VDC} \pm 0.3 \%(+14.55 \text { to }+15.45 \mathrm{VDC}), 200 \mathrm{~mA} \\ & -15 \mathrm{VDC} \pm 0.3 \% \text { ( }-14.55 \text { to }-15.45 \mathrm{VDC}), \end{aligned}$ | Use the A68P power supply unit or a stabilized equivalent. |

## REMARKS

(1) *1 The load current for the SVON signal is 30 mA maximum. Pay careful attention to the load current when it is received by a miniature relay.
(2) *2 When the input impedance of the servo amplifier is too small, the analog output level may be lowered by this resistance. If this poses problems, re-adjust the gain with the servo amplifier connected.

### 5.4.5 Settings

(1) Encoder output setting

Set the output of the encoder with the encoder interface jumper on the A70AF side panel.

The jumper has been factory-set to the OPEN COLLECTOR OUTPUT setting.

|  | Shorting Pin Setting |
| :---: | :---: |
| Open collector output | Phase 2 Phase B Phase A <br> 0 O <br> 0 |
| TTL output | Phase 2 Phase B Phase A 0 O 0 |
| Differential output | Phase $Z$ Phase B Phaso A 5jgo Esgo |



A70AF side panel
(2) Velocity command voltage zero adjustment

Make velocity command voltage zero adjustment with the slide switch and the zero adjustment control on the A70AF side panel.

This voltage has been factory-set to 0 V .
However, re-adjustment is required after a servo motor is connected which can shift the 0 V .

If operation is performed with the 0 V shifted, the motor may rotate slightly upon power on.
© OUT
© GND
[Check pin on the A70AF front panel]
(1) GAIN - Gain setting control
Q ZERO - Zero adjustment control

[Control on the A70AF front panel]
[Slide switch on the ATOAF side panel]

Fig. 5.1 Zero Adjustment Setting
(a) Zero adjustment method

1) Servo-lock the servo motor.
2) Set slide switches [SW7] and [SW8] on the A70AF side panel to ON.
3) Turn the ZERO control on the front panel until the voltage across the check pin is 0 V .
4) After adjustment, set the [SW7] switch to the AXIS SETTING position and the [SWB] switch to the OFF position.

## GEMARKS

The TEST LED on the A7OAF front panel remains lit duping adjustment mode selection.
(3) Rated velocity command voltage and position loop gain setting
(a) To set the rated velocity command voltage and position loop gain for a general-purpose servo amplifier, make the gain adjustment with gain adjustment accumulated pulse switches SW1 through SW3.

ATOAF velocity command voltage is divided into 8 ranges as indicated in Table 5.7.

Example:
When a general-purpose servo amplifier with a rated velocity command voltage is 5 V is used, the rated velocity command voltage can be set to 5 V over ranges (3) through (7) given in Table 5.7.

Table 5.7 Rated Velocity Command Voltage Adjustment Range and Gain Adjustment Accumulated Pulse Switch Setting

|  | Rated Velocity Command Voltage Adjustment Range $M$ | Accumulated Pulse Switch for Gain Adjustment |  |  | Accumulated Pulse Number and Position Loop Gain by Setting Accumulated Pulse Number |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 1 | 2 | 3 | X1 | X2 | X3 | X4 |
| (1) | 1.7 to 3.7 | OFF | OFF | OFF | $\begin{aligned} & 1111^{*} \\ & (360) \\ & \hline \end{aligned}$ | $\begin{aligned} & 2222 \\ & (180) \end{aligned}$ | $\begin{aligned} & 3333 \\ & (120) \\ & \hline \end{aligned}$ | $\begin{aligned} & 4444 \\ & (90) \end{aligned}$ |
| (2) | 1.9 to 4.2 | OFF | OFF | ON | $\begin{array}{r} 1250 \\ (320) \\ \hline \end{array}$ | $\begin{aligned} & 2500 \\ & (160) \\ & \hline \end{aligned}$ | $\begin{aligned} & 3750 \\ & (107) \\ & \hline \end{aligned}$ | $\begin{aligned} & 5000 \\ & (80) \\ & \hline \end{aligned}$ |
| (3) | 2.2 to 5.0 | OFF | ON | OFF | $\begin{array}{r} 1429 \\ (280) \\ \hline \end{array}$ | $\begin{aligned} & 2858 \\ & (140) \end{aligned}$ | $\begin{array}{r} 4287 \\ (93) \\ \hline \end{array}$ | $\begin{gathered} 5714 \\ (70) \end{gathered}$ |
| (4) | 2.5 to 5.7 | OFF | ON | ON | $\begin{aligned} & 1667 \\ & (240) \\ & \hline \end{aligned}$ | $\begin{aligned} & 3334 \\ & (120) \\ & \hline \end{aligned}$ | $\begin{array}{r} 5000 \\ (80) \\ \hline \end{array}$ | $\begin{aligned} & 6668 \\ & (60) \end{aligned}$ |
| (5) | 3.0 to 7.0 | ON | OFF | OFF | $\begin{aligned} & 2000 \\ & (200) \\ & \hline \end{aligned}$ | $\begin{aligned} & 4000 \\ & (100) \\ & \hline \end{aligned}$ | $\begin{aligned} & 6000 \\ & (67) \\ & \hline \end{aligned}$ | $\begin{aligned} & 8000 \\ & (50) \\ & \hline \end{aligned}$ |
| (6) | 3.7 to 8.7 | ON | OFF | ON | $\begin{array}{r} 2500 \\ (160) \\ \hline \end{array}$ | $\begin{array}{r} 5000 \\ \text { (80) } \\ \hline \end{array}$ | $\begin{array}{r} 7500 \\ (53) \\ \hline \end{array}$ | $\begin{gathered} 10000 \\ (40) \\ \hline \end{gathered}$ |
| (7) | 5.0 to 12.0 | ON | ON | OFF | $\begin{array}{r} 3480 \\ (115) \\ \hline \end{array}$ | $\begin{array}{r} 6960 \\ (57) \\ \hline \end{array}$ | $\begin{gathered} 10440 \\ (38) \end{gathered}$ | $\begin{gathered} 13920 \\ (29) \\ \hline \end{gathered}$ |
| (8) | 5.5 to 12.5 | ON | ON | ON | $\begin{aligned} & 3700 \\ & (108) \\ & \hline \end{aligned}$ | $\begin{array}{r} 7400 \\ (54) \\ \hline \end{array}$ | $\begin{gathered} 11100 \\ (36) \\ \hline \end{gathered}$ | $\begin{aligned} & 14800 \\ & (27) \end{aligned}$ |

## REMARKS

- The number of accumulated pulses for delivering the rated velocity command voltage is given above. The position loop gain value obtained at a maximum velocity of 400 kpps is written in parentheses below.

When the maximum velocity is not 400 kpps , calculate the position loop gain using the formula shown in (4)(b) on page 5-21.

## Example:

5 V power is delivered at 1429 PLS when the gain adjustment accumulated pulse switch is set to (3), the gain value is adjusted to 5 V , and the accumulated pulse number switch is set to X1.
(The position loop gain will be $280 \mathrm{sec}^{-1}$ at a maximum speed of 400 kpps .)
(b) The unit has been factory-set so that it delivers 10 V when the number of accumulated pulses reaches 13920 PLS.
(c) Gain adjustment method

1) Set slide switches [SW7] and [SW8] to OFF and ON, respectively.
2) Refer to the rated velocity command voltage adjustment range and the position loop gain settings and set accumulated pulse switches [SW1], [SW2], and [SW3] for gain adjustment.
3) Turn the GAIN setting control on the A70AF front panel until the voltage across the check pin reaches the rated velocity command voltage.
4) After adjustment, set slide switches [SW1], [SW2], [SW3], and [SW7] to their normal positions and set the [SW8] switch to OFF.
(4) Accumulated pulse number setting
(a) The accumulated pulse number setting is to select the maximum number of pulses able to be counted on the error counter.
(b) When a servo motor is used, pulses, the number of which is represented by the following formula, are generated.

$$
\text { Maximum number of pulses }=\frac{\text { Speed command }[P P S]}{\text { Position loop gain }\left[\mathrm{sec}^{-1}\right]}
$$

(c) Set the accumulated pulse number range to exceed the maximum number of accumulated pulses obtained from the formula given above, using accumulated pulse number switches [SW1] and [SW2].
$\left.\begin{array}{|c|c|c|c|c|c|}\hline \text { Slide switch Accumulated pulse number } \\ \hline \text { setting }\end{array}\right]$
(d) The output voltage from the A70AF is as shown in Fig. 5.2 depending on the number of accumulated pulses.


Fig. 5.2 Relationship Between the Number of Accumulated Pulses and Analog Voltage Output

Example:
Set the number of accumulated pulses as follows.
Assume that:
(a) Maximum velocity $=400 \mathrm{KPPS}$
(b) Position loop gain $=30 \mathrm{sec}^{-1}$

Then:
(c) Accumulated pulses $=\frac{\text { Maximum velocity }}{\text { Position loop gain }}$

$$
=\frac{400000}{30}
$$

$$
=133333
$$

Therefore, selecting the gradient from Fig. 5.2 for which the output voltage is not saturated when the number of accumu: lated pulses is 13333 leads to 'SELECTION ([SW1]: ON; [SW2]: ON)".
(e) When the number of accumulated pulses exceeds the value marked with an asterisk (*) in Fig. 5.2, an excessive error occurs and the following status is developed.

1) Output voltage : OV
2) Accumulated pulses: Set to 0 .
3) SVON signal : OFF
4) ERR LED on the front panel : Lit
(5) Multiplier selection
(a) The feedback pulse multipliers from the puise generator (PLG) are selected.

This is necessary when the travel distance per pulse is to be changed.
(b) Through pulse multiplier selection, the count value for the number of feedback pulses can be multiplied by 4, 2, 1, or 1/2.

Therefore, this selection should be made when the travel distance per pulse needs be multiplied by $1 / 4,1 / 2,1$, and 2.


Fig. 5.3 Error Counter Counts Obtained by Multiplier Selection
(c) The relationship between multiplier switch positions and the multipliers is indicated below.

| Slide switch |  | Times 4 | Times 2 | Times 1 | Times 1/2 |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | [SW3] | OFF | ON | OFF | ON |
| $\square$ Multiplier switch | [SW4] | OFF | OFF | ON | ON |

### 5.4.6 Wiring connections

This section describes the precautions to be taken and external wiring connectors to be used when the ATOAF is connected to an external device.
(1) Wiring precautions

The wiring precautions for the A70AF and external devices (including the drive unit) are as follows.
(a) Length of cable connecting the A70AF to the drive unit

The cable connecting the A70AF to the drive unit is generally 1 to 3 m ( 3.28 to 9.84 ft ) long. However, this length varies with drive unit specifications.

Confirm the specifications of the drive unit used.
(b) Wiring connections for input/output signals

1) Avoid bundling the cables with the power line or main circuit line or bringing them close to such lines (run the cable at least 20 cm ( 7.87 in ) away from the line).
2) When the cable must be close to the line, separate the duct or run the conduits separately.
3) When the cable must be bundled with the line, use a batch shielded cable and ground it at the PC.
4) When running conduits or making wiring connections, ground the conduits.
(c) The length of the cable connecting the A70AF to the encoder is generally as follows, though it depends on the encoder specifications.

Check the specifications of the encoder.

1) $30 \mathrm{~m}(98.43 \mathrm{ft})$ max. for differential output encoder
2) 3 m ( 9.84 ft ) max. for TTL open collector encoder

Use a twisted-pair, shielded cable for the line connected to the A70AF.
(d) The A70AF does not need to be grounded since an antinoise means is provided.
However, in cases where excessive noises develop or cable dislocation occurs, ground the unit as suggested below.


1) When grounding the A70AF, perform independent grounding (Class 3 grounding) by separating the $F G$ terminal of the power supply module from the same terminal of the A7OAF as illustrated above.
2) Use a ground wire with a sectional area of at least $2 \mathrm{~mm}^{2}$ (14 AWG).

Use a ground point close to the PC, keeping the ground wire as short as possible.
(e) Install surge suppressors to the AC relay, AC bulb, and the AC solenoid brake near the drive unit amplifier. Install diodes to the DC relay and DC bulb in parallel with the relays and other parts. For more details, see the drive unit manual.

(a) Installation to AC relay, AC bulb, etc.

(b) Installation to DC relay, etc.

Fig. 5.4 Typical Installation of Surge Suppressors
(f) Always connect the A7OAF and drive unit SVON signal. Do not turn the signal on and off externally.
(2) Encoder connecting precautions

This section describes the precautions to be taken when connecting the encoder.
(a) The A70AF uses an up-and-down error counter.

Addition and subtraction processes are switched between them through comparison of feedback pulse phases.

1) When a feedback pulse in which phase $A$ is advancing ahead of phase $B$ by $90^{\circ}$ is received, the command pulse count is subtracted.

This subtraction is required when the positive command pulse is to be counted (when the motor is rotating forward), with the velocity command being the positive voltage.
2) When a feedback pulse in which phase $B$ is advancing ahead of phase $A$ by $90^{\circ}$ is received, the command pulse count is added.

This addition is required when the negative command pulse is to be counted (when the motor is rotating in reverse), with the velocity command being the negative voltage.

When feedback pulse phases $A$ and $B$ are reversed, both the command pulses and the feedback pulses are counted, resulting in excessive errors in the number of accumulated pulses. In that event, control will be discontinued.

| Feedback pulse with phase $A$ advancing by $90^{\circ}$ |
| :--- | :--- | :--- | :--- |
| Phase A |
| Phase B |

(b) Command pulse and feedback pulse counting processes are changed in the following cases.

1) Setting rotation direction by a peripheral device:

Rotation direction of the motor and the positive and negative counting processes are changed.
2) The rotation direction of the motor and the encoder are different:

The feedback pulse phases are reversed since the encoder rotates in reverse when the motor is rotating.
(c) Table 5 indicates the rotation direction set by a peripheral device and the connection method that depends on the difference in the motor and the encoder rotation direction.

Table 5.8 indicates the case when the motor rotates forward at the positive voltage applied to the servo amplifier while the motor and the encoder are rotating as illustrated below.


Table 5.8 Connection Methods

|  | Rotation Direction Set by a Peripheral Device | Connection | Remarks |
| :---: | :---: | :---: | :---: |
| Forward rotation | Forward rotation |  | The motor and the encoder rotate in the same direction. |
|  |  |  | The motor and the encoder rotate in the opposite direction to each other. |
| Reverse rotation | Reverse rotation |  | The motor and the encoder rotate in the same direction. |
|  |  |  | The motor and the encoder rotate in opposite directions. |

## POINT

If the A70AF is connected to the encoder incorrectly, the motor will rotate upon power up, causing excessive errors (ERR).
(d) Table 5.9 indicates the connection between the A70AF and the encoder.

Table 5.9 Connections Between the A70AF and the Encoder

| Encoder type | Connection |
| :---: | :---: |
| Open collector output encoder |  |
| TTL output encoder |  |
| Differential output encoder |  |

(3) Connection of external wiring connectors

This section describes the method of connecting the external wiring connectors.

The unit is supplied with the following connectors.
$\begin{array}{ll}9 \text {-pin connector (male) } \times 1 & \text { : For CONT connector } \\ 15 \text {-pin connector (male) } \times 1 & \text { :For SERVO connector }\end{array}$
The construction of each connector is illustrated below.


Use the following procedure when assembling the connector.

1) Run the wire through the protective tube (ior the 15-pin connector only).
$\downarrow$
2) Solder the wire to the connecting section.
$\downarrow$
3) Install the connecting section onto cover A and wind the protective seal around the wire portion that makes contact the wire clamp.

$\downarrow$
4) Slide the protective tube until it reaches the protective seal (for the 15-pin connector only).
$\downarrow$
5) Install the connecting section to cover $A$ and hold the wire (the protective seal or tube) with the wire clamp (using screw A).
$\downarrow$
6) Install screw $C$ to cover A.
$\Downarrow$
7) Place cover $B$ onto cover $A$; fasten them with screw $B$ and the nuts.

(4) Connector connection

The connector pins are arranged as shown below. Make the connections referring to the input and output numbers in Section 3.5.
(a) Cables with a sectional area of up to $0.3 \mathrm{~mm}^{2}$ (22 AWG) may be used. Thicker cables will not fit under the cable clamp.
(b) Make the connections by soldering. Strip properly the cable so that short will not be caused by the element wires or solder hairs.

It is recommended to slip an insulating tube on each soldered connection.

(Applicable to the CONT connector)

Pin arrangement as viewed from the cable connection side


15-pin connector
(Applicable to the SERVO connector)

## 6. POWER SUPPLY MODULE

## 6. POWER SUPPLY MODULE

### 6.1 Power Supply Module Specifications

### 6.1.1 Power supply module specifications

Table 6.1 Power Supply Module Specifications

| Hem |  | Specifications |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A61P | A62P | A63P | A65P | A66P | A68P |
| Base loading position |  | Power supply module loading slot |  |  |  | I/O module loading slot |  |
| Input voltage |  | $\begin{gathered} 100-120 \text { VAC }+10 \% \\ \text { (85 to } 132 \text { VAC) } \\ \hline \end{gathered}$ |  | $\begin{aligned} & 24 \text { VDC }+30 \% \\ & (15.6 \text { to } 31.2 \\ & \text { VDC) } \end{aligned}$ | $\begin{gathered} 100-120 \mathrm{VAC}+10 \% \\ (85 \text { to } 132 \mathrm{VAC} \text { ) } \\ \hline \end{gathered}$ |  |  |
|  |  | $\begin{gathered} 200-240 \text { VAC }+10 \% \\ (170 \text { to } 264 \text { VAC }) \\ \hline \end{gathered}$ |  |  | $\begin{gathered} 200-240 \text { VAC }+10 \% \\ (170 \text { to } 284 \text { VAC) } \end{gathered}$ |  |  |
| Input frequency |  | $50 / 60 \mathrm{~Hz} \pm 5 \%$ |  | - | $50 / 60 \mathrm{~Hz} \pm 5 \%$ |  |  |
| Max. input apparent power |  | 110 VA |  | 65 W | 110 VA | 95 VA |  |
| Inrush current |  | 20 A , within 8 ms |  | 100 A, within 1 ms | 20 A , within 8 ms |  |  |
| Rated output current | 5 VDC | 8 A | 5 A | 8 A | 2 A | - | - |
|  | 24 VDC | - | 0.8 A | - | 1.5 A | 1.2 A | - |
|  | +15 VDC | - |  |  |  |  | 1.2 A |
|  | -15 VDC | - |  |  |  |  | 0.7 A |
| * <br> Overcurrent protection | 5 VDC | 8.8 A or higher | 5.5 A or higher | 8.5 A or higher | 2.2 A or higher | - | - |
|  | 24 VDC | - | 1.2 A or higher | - | 2.3 A or higher | 1.7 A or higher | - |
|  | +15 VDC | - |  |  |  |  | 1.64 A |
|  | -15 VDC | - |  |  |  |  | 0.94 A |
| *2 Overvoltage protection | 5 VDC | 5.5 to 6.5 V | 5.5 to 6.5 V | 5.5 to 6.5 V | 5.5 to 6.5 V | - | - |
|  | 24 VDC | - - |  |  |  |  |  |
| Efficiency |  | 65\% or higher |  |  |  |  |  |
| Power indicator |  | Power LED display |  |  |  |  |  |
| Terminal screw size |  | M4 $\times 0.7 \times 6$ |  |  |  | M3 $\times 0.5 \times 6$ |  |
| Applicable wire size |  | 0.75 to $2 \mathrm{~mm}^{2}$ (18 to 14 AWG) |  |  |  |  |  |
| Applicable solderless terminal |  | V1.25-4, V1.25-YS4A, V2-S4, V2-YS4A |  |  |  | $\begin{gathered} \text { V1.25-3, V1.25-YS3A, } \\ \text { V2-S3, V2-YS3A } \end{gathered}$ |  |
| Applicable tightening torque |  | $12 \mathrm{~kg}-\mathrm{cm}(10.39 \mathrm{lb}-\mathrm{in})$ |  |  |  | $7 \mathrm{~kg} . \mathrm{cm}(6.06 \mathrm{lb}-\mathrm{in})$ |  |
| External dimensions mm (in) |  | $250(9.84) \times 55(2.17) \times 121$ (4.76) |  |  |  | $\begin{array}{r} 250(9.84) x \\ 37.5(1.48) \\ \times 121(4.76) \\ \hline \end{array}$ | $\begin{array}{r} 250(9.84) \times \\ 37.5(1.48) \times \\ \times 121(4.76) \\ \hline \end{array}$ |
| Weight kg (ib) |  | 0.98 (2.16) | 0.94 (2.07) | 0.8 (1.76) | 0.94 (2.07) | 0.75 (1.65) | 0.9 (1.98) |
| $\begin{aligned} & \text { *3 } \\ & \text { Allowable instantaneous } \\ & \text { power failure time } \\ & \hline \end{aligned}$ |  | within 20 ms |  | within 1 ms | within 20 ms | - | - |

## REMARKS

The number of slots occupied in the A66P and the A68P are as follows:
(a) 1 in the A66P
(b) 2 in the A68P

## POINT

*1: Overcurrent protection
(a) The overcurrent protection device shuts off the $5 \mathrm{~V}, 24$ VDC circuit and stops the system if the current flowing in the circuit exceeds the specified value.
(b) If a current larger than specified is allowed to flow through the +15 VDC circuit, the overcurrent protection device will be actuated to open the circuit and the following conditions will exist.

1) Both +15 VDC and -15 VDC will be turned off when overcurrent is present on the +15 VDC side.
2) -15 VDC is turned off and +15 VDC will be delivered when overcurrent is present on the -15 VDC side.
3) The LED indicator on the power supply unit goes out or dims due to reduced voltage supply.
(c) When this device is activated, the power supply module LED is switched off or dimly lit. In this case, remove any cause if overcurrent and start up the system.
*2: Overvoltage protection
The overvoltage protection device shuts off the 5 VDC circuit and stops the system if 5.5 to 6.5 V voltage is applied to the circuit.

When this device is activated, the power supply module LED is switched off. In this case, switch off, then on the input power to restart the system. The power supply module must be changed if the system is not booted and the LED remains off.
*3: Allowable instantaneous power failure time
The A73CPU allowable instantaneous power failure time varies according to the type of power supply module. In the case of the A63P module, the allowable instantaneous power failure time is defined from when the 24 VDC stabilized primary supply is cut off until the 24 VDC voltage drops to the defined voltage (15.6 VDC).

### 6.1.2 Selection of power supply module

Select the power supply module according to the total current consumption of I/O modules, special function modules, and peripheral device supplied by that power supply module.
(1) Notes on use of the A66P
(a) The A66P gives optimum power output when a vacant slot exists on either side of it. It must be located without an I/O module to the right, preferably with a vacant slot to the left.
(b) The A66P output current (24 VDC) depends on the left-hand adjacent module.

*1 Power supply module
*2 A66P
*3 Vacant
*4 Input module Dummy module

* 5 Output module

Special function module

### 6.1.3 Fuse specifications

This section describes the specification of fuses used for the power supply modules and output modules.

Table 6.2 Fuse Specifications

| Type Item | GTH4 | SM6.3A | MF51 NM8 | HP-32 | HP-70K | MP-20 | MP-32 | MP-50 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Application | For power supply A61P, A62P. A65P, A66P | For power supply A63P | For output AY11E AY13E | For output AY23 | For output AY22 | For output AY50, AY80 | For output AY60 | For output AY60E |
| Shape | Cartridge type | Cartridge type | Cartridge type | Plug type | Plug type | Plug type | Plug type | Plug type |
| Rated current | 4 A | 6.3.A | 8 A | 3.2 A | 7 A | 2 A | 3.2 A | 5 A |
| External dimensions mm (in) | $\begin{array}{r} 6(0.24) \\ \times 32(1.26) \end{array}$ | $\begin{array}{r} 06(0.24) \\ \times 32(1.26) \end{array}$ | $\begin{gathered} \$ 5.2 \\ (0.20) \times \\ 20(0.79) \end{gathered}$ | $\begin{aligned} & 30.3 \\ & (1.19) \times 8 \\ & (0.31) \times \\ & 20(0.79) \end{aligned}$ | $\begin{aligned} & 30.3 \\ & (1.19) \times 8 \\ & (0.31) \times x \\ & 20(0.79) \end{aligned}$ | $\begin{array}{\|l} 17.2 \\ 10.68) \times \\ 5.5(0.22) \\ \times 19(0.75) \end{array}$ | $\begin{aligned} & 17.2 \\ & 10.68) \times \\ & 5.5(0.22) \\ & \times 19(0.75) \end{aligned}$ | $\begin{aligned} & 17.2 \\ & 10.68) \times \\ & 5.5(0.22) \\ & \times 19(0.75) \end{aligned}$ |

### 6.2 Handling

This section gives handling instruction, part identification of PC and hardware setting instructions.

### 6.2.1 Handling instructions

This section describes precautions regarding the handling of the power supply module between unpacking and installation.
(1) The power supply module case, terminal, and pin connectors are made of plastic. Do not subject the power supply module to impact or shock.
(2) Do not remove printed circuit boards from the housing. There are no user-serviceable parts on the boards.
(3) Ensure that no conductive debris can enter the module. If it does, make sure that it is removed.
(4) Tighten the module mounting screws (if required) and terminal screws as specified below.

| Screw | Tightening Torque kg-cm (lb-in) |
| :--- | :---: |
| Module terminal block installation screws (M3) | $5(4.33)$ to 8 (6.93) |
| Module terminal block installation screws (M4) | $10(8.66)$ to 14 (12.13) |
| Module mounting screws (optional) (M4) | $8(6.93)$ to 12 (10.39) |

(5) To load the module onto the base, hook the tow lower lugs into the cut out and gently swing the module into place. Ensure that the top catch engages. To remove, press the top catch and swing the module out before unhooking the lower lugs. (See Section 9.5)

### 6.2.2 Part identification

This section describes the parts of the power supply module.
(1) Part identification of A61P module

(2) Part identification of A62P, A65P module

(3) Part identification of A63P module

(4) Part identification of A66P module

(5) Part identification of A68P module

(6) Supply power voltage setting

The input voltage of the A61P, A62P, A65P, A66P, and A68P power supply modules must be selected by placing a jumper (supplied) across two terminals as described below.


## POINT

If the setting differs from the supply line voltage, the following occurs. Therefore, do not mis-set.

|  | Supply Line Voltage |  |
| :--- | :--- | :--- |
|  | 100 VAC | 200 VAC |
| Setting to 100 VAC <br> (Install the short chip <br> to (2)) | - | The power supply module <br> is damaged. (The CPU is <br> not damaged.) |
| Setting to 200 VAC <br> (Install the short chip <br> to (3)) | No error occurs in the <br> module. However, the <br> CPU does not operate. |  |
| No setting <br> (The short chip is not <br> installed) | No error occurs in the module. However, the CPU does <br> not operate. |  |

## 7. BASE UNIT AND EXTENSION CABLE

### 7.1 Base Unit and Extension Cable Specifications

This section describes the specifications of the main and extension base units and the extension cable used in this system.
7.1.1 Specifications of the base units

Table 7.1 Base Unit Specifications

| Item | Main Base Unit | Extension Base Unit |  |
| :---: | :---: | :---: | :---: |
| Type | A74B | A65B | A68B |
| Loaded I/O modules | 4 can be loaded | 5 can be loaded | 8 can be loaded |
| Installation hole size | 6 mm (0.24 in) dia. pear-shaped hole (for M5 screw) |  |  |
| External dimensions mm (in) | $\begin{aligned} & 382(15.04) \times 250 \\ & (9.84) \times 29(1.14) \end{aligned}$ | $\begin{aligned} & 352(13.86) \times 250 \\ & (9.84) \times 29(1.14) \end{aligned}$ | $\begin{aligned} & 480(18.90) \times 250 \\ & (9.84) \times 29(1.14) \end{aligned}$ |
| Weight kg (lb) | 1.5 (3.3) | 1.4 (3.08) | 1.9 (4.18) |

### 7.1.2 Extension cable

Table 7.2 Extension Cable Specifications

|  | AC068 | AC12B | AC30B |
| :---: | :---: | :---: | :---: |
| Cable length $m$ ( H ) | 0.6 (1.97) | 1.2 (3.94) | 3 (9.84) |
| Resistance value of 5 VDC supply line ( $\Omega$ at $55^{\circ} \mathrm{C}$ ) | 0.019 | 0.028 | 0.052 |
| Application | For connection between main base and extension base and between extension bases |  |  |
| Weight kg (ib) | 0.34 (0.75) | 0.52 (1.14) | 1.06 (2.33) |

## POINT

The A55B or A58B extension base unit cannot be connected to the A74B.

### 7.2 Handling

This section describes handing instruction, PC part identification and hardware setting instructions.

### 7.2.1 Handling instructions

This section describes precautions regarding handling the base unit between unpacking and installation.
(1) The base unit terminal and pin connectors are made of plastic. Do not drop, nor give intense shocks to, the unit.
(2) Do not remove printed circuit boards from the housing. There are no user-serviceable parts on the boards.
(3) Ensure that no conductive debris can enter the module. If it does, make sure that it is removed. Guard particularly against wire offcuts.
(4) Tighten the module mounting screws (if required) and terminal screws as specified below.

| Screw | Tightening Torque <br> kg-cm (lb-in) |
| :--- | :--- |
| Module terminal block installation screws (M4) | $10(8.66)$ to 14 (12.13) |
| Module mounting scrows (optional) (M4) | $8(6.93)$ to 12 (10.39) |

### 7.2.2 Part identification

This section describes the parts of the base unit.
(1) Main base unit (A74B)


## (2) Extension base unit (A65B, A68B)



PEMARKS
It is necessary to set the shaded portion before installing the base and starting operation.

### 7.2.3 Extension stage number setting on the SCPU extension base

When the extension base unit is connected to the SCPU extension connector, it is necessary to set the extension stage number for each extension base unit.


Extension Base Unit Stage Number Setting

|  | Extension Stage Number Setting |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{gathered} 1 \mathrm{st} \\ \text { stage } \end{gathered}$ | 2nd stage | 3rd stage | $\begin{gathered} \text { 4th } \\ \text { stage } \end{gathered}$ | $\begin{aligned} & \text { 5th } \\ & \text { stage } \end{aligned}$ | $\begin{aligned} & \text { 6th } \\ & \text { stage } \end{aligned}$ | $\begin{aligned} & \text { 7th } \\ & \text { stage } \end{aligned}$ |
| Stage number setting |  |  |  |  |  |  |  |

## POINT

Set the stage number setting connector (CON3) to a number, from 1 to 7 , which matches the number of extension stages. If the same number has been set to two or more extension base units, or no stage number has been set, mis-input or mis-output will result.

### 7.2.4 Extension stage number setting on the PCPU extension base

When the extension base unit is connected to the PCPU extension connector, it is necessary to set the extension stage number for each extension base unit.


## 8. MEMORIES AND MEMORY CASSETTES

### 8.1 Specifications

This section describes the specifications of memories and memory cassettes which can be used with the A73CPU.
8.1.1 Memory cassette specifications

This section describes the specifications of memory cassettes to be used.

Table 8.1 Memory Cassette Specifications

| Hem Type | A3NACAO | ASNHCAT 2 | A3NMCA-4 | A3nmicas | Azanca-18 | ABHMCA-24 | АзNйСА-40 | AЗNMCA-58 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| *RAM memory capacity <br> (Parameter setting range) | None installed | 16K <br> (16K) | $\begin{gathered} 32 K \\ (32 K) \end{gathered}$ | $\begin{gathered} 64 K \\ (64 K) \end{gathered}$ | $\begin{aligned} & 128 \mathrm{~K} \\ & \text { (96K) } \end{aligned}$ | $\begin{gathered} 192 K \\ (144 K) \end{gathered}$ | $\begin{gathered} 320 K \\ (144 K) \end{gathered}$ | $\begin{gathered} 448 K \\ (144 K) \end{gathered}$ |
| Number of ROM loading sockets | 2 pcs. (for 28 pins) |  |  |  |  |  |  |  |
| Type of loadable ROM | $4 \mathrm{KROM}, 8 \mathrm{KROM}, 16 \mathrm{KROM}$ |  |  |  |  |  |  |  |
| Type of loadable RAM | 4KRAM | Unloadable |  |  |  |  |  |  |
| External dimensions mm (in) | $110(4.33) \times 79.5(3.13) \times 33(1.30)$ |  |  |  |  |  |  |  |
| Weight kg (lb) | $\begin{gathered} 0.13 \\ (0.29) \end{gathered}$ | $\begin{gathered} 0.13 \\ (0.29) \end{gathered}$ | $\begin{gathered} 0.13 \\ (0.29) \end{gathered}$ | $\begin{gathered} 0.13 \\ (0.29) \end{gathered}$ | $\begin{gathered} 0.13 \\ (0.29) \\ \hline \end{gathered}$ | $\begin{array}{r} 0.13 \\ (0.29) \\ \hline \end{array}$ | $\begin{gathered} 0.15 \\ (0.33) \end{gathered}$ | $\begin{gathered} 0.15 \\ (0.33) \end{gathered}$ |

- The RAM memory capacity is the total memory capacity of the RAM chips soldered to the pattern on the memory cassette printed circuit board. The parameter setting range is the memory area set by parameters to store parameters, main programs, subprograms, comments, etc. For details of the types of data and order in which it is stored, refer to Section 8.2.4.


### 8.1.2 Memory specifications

This section describes the specifications of the ROM and RAM memories that can be used in the memory cassette.

Table 8.2 Memory Specifications

| Type | 4KRAM | 4KROM | 8KROM | 16KROM |
| :--- | :--- | :--- | :--- | :--- |
| Memory specifications | IC-RAM (Read and <br> write possible) | EP-ROM (only read is possible) |  |  |
| Memory capacity (bytes) | 8K | 8 K | 16 K | 32K |
| Structure | 28-pin IC package | 28-pin IC package | 28-pin IC package | 28-pin IC package |
| Note | When loading memory into the memory cassette two memory ICs of the same <br> Specification are always required. |  |  |  |

### 8.1.3 Battery specifications

This section describes the specifications of battery used for RAM memory backup and power failure compensation.

Table 8.3 Battery Specifications

| Hem Type | A6BAT |
| :---: | :---: |
| Nominal voltage | 3.6 VDC |
| Guaranteed life | 5 years |
| Total power failure backup time | Depends on memory cassette type as indicated below: A3NMCA-0: Min. 4100 hours, A3NMCA-56: Min. 450 hours |
| Application | For IC-RAM memory backup and power failure compensation function |
| External dimensions mm (in) | $016(0.63) \times 30(1.18)$ |

### 8.2 Handling

This section explains the handling instructions from unpacking to installation and also part identification and setting.

### 8.2.1 Handling instructions

This section describes precautions regarding the handling of the memory cassette and battery from unpacking to installation.
(1) Memory cassettes and memories
(a) Do not subject the memory cassette and memories to impact or shock.
(b) Do not remove printed circuit boards from the housing. There are no user-serviceable parts on the boards.
(c) Ensure that no conductive debris can enter the module. If it does, make sure that it is removed. Guard particularly against wire offcuts.
(d) When loading the memory cassette into the main unit, press the memory cassette securely into the housing.
(e) When loading the memory into the socket, press the memory securely against the socket and lock it with the lever.

After loading, check that the memory is flush with the socket.
(f) Never place the memory on metal, which may allow current flow, or on an object which is charged with static electricity, such as wood, plastic, vinyl, fiber, cable, and paper.
(g) Do not touch the legs of the memory. Also, do not bend the legs.
(h) When mounting the memory, be sure to fit the memory the right way round as indicated on the socket. If reversely installed, the memory will be damaged.
(i) Do not touch the CPU memory cassette connector. Touching the connector may result in improper contact.

## IMPORTANT

Before installing or removing the memory cassette to or from the CPU or GPP, be sure to turn off the power. If installation or removal is performed with the power on, the contents of the memory cassette will be damaged.
(2) Battery
(a) Do not short-circuit the battery.
(b) Do not disassemble the battery.
(c) Do not throw the battery into flames.
(d) Do not heat the battery.
(e) Do not solder the poles of the battery.

### 8.2.2 Part identification

This section describes the parts of the memory cassette.


| No. | Name | Description | Note |
| :---: | :---: | :---: | :---: |
| (1) | CPU connector | Connects the memory cassette to the CPU. |  |
| (2) | Battery (A6BAT) | RAM backup and power failure compensation |  |
| (3) |  | IC socket with locking facility for IC-RAM/EP-ROM. <br> Identical types of memories must be loaded into the two sockets. When ROMs are used, the odd-address ROM must be loaded into SOC1 and the even-address ROM into SOC2. | * |
| (4) | Program memory sockets | IC socket with locking facility for EP-ROM only Identical types of memories must be loaded into the two sockets. The odd-address ROM must be loaded into SOC1 and the evenaddress ROM into SOC2. | * |
| (5) | Battery lead connector | Connect the battery leads to the connector (CON1). Before shipment, the wires are disconnected to prevent battery discharge. | * |
| (6) | Memory setting switch | Memory select switch | * |
| (7) | Memory protect switch | Set the protect range of the IC-RAM contents. (With protect ON, writing to the RAM is disabled.) | * |

* Requires setting before using the memory cassette.


### 8.2.3 Memory IC installation

This section describes the procedure to load RAM and ROM in the memory cassette and to set the IC type.
(1) Holding the IC

Hold the IC as shown in Fig. 8.1 when loading it into the memory cassette.

Touching the memory leads can result in memory damage due to static electricity or poor electrical contact due to bent pins.


Fig. 8.1 Holding the IC
(2) Loading the IC

Be sure to load the IC in the correct direction. The memory may be destroyed if the power supply is turned on with the IC mounted incorrectly.

To load the IC, note the orientation of the notch (ROM, RAM) or broken line (RAM) as indicated on the socket.

| Socket | EP-ROM | IC-RAM |  |
| :---: | :---: | :---: | :---: |
|  |  | Notch | Broken line |
|  |  |  |  |

Fig. 8.2 IC Loading Direction
(3) Setting the IC type

Set the jumper or switch to RAM or ROM in accordance with the IC used.


Fig. 8.3 Setting the IC Type
*1 Load the IC after setting its type.

* 2 When the A3NMCA-0 is used, set the jumper to CON3 (ROM) or CON4 (RAM).
* 3 When the A3NMCA-2 to 56 is used, set switch 1 of SW1 to the appropriate position.


## REMARKS

The IC type is set to RAM when the memory cassette is dispatched from the factory.
(4) IC loading procedure

Load the IC correctly according to the procedure below.


### 8.2.4 Memory protect switch setting

The RAM memory may be protected by switching the memory protect switch ON (See below). This protects the memory from accidental program changes.

When changes to the PC program are to be made, switch the memory protect switch OFF.
(1) The switch layout varies depending on the memory cassette type as shown below.

The memory protect switch is set OFF when the memory cassette is dispatched from the factory.

| A3NMCA-0 | A3NMCA-2, 4 | A3NMCA-8, 16, 24, 40, 56 |
| :---: | :---: | :---: |
| M.PRO <br> ON $\square$ Memory OFF $\square$ protect setting jumper |  |  |

(2) The memory ranges protected by each switch are shown below:

(3) Check the memory cassette areas where programs and data are stored before setting the protected memory ranges.

The order in which data is stored in the memory cassette is shown in the diagram below.

The types of data stored are set with the parameters.
(a) RAM operation

* The parameters, main program, and subprogram are stored in order from the head address of the parameter setting range.
* The comments, file register, status latch, and sampling trace are stored in order from the last address of the parameter setting range.
(b) ROM operation
* The parameters and main program are stored in ROM.
- The subprogram is stored after the head address of the parameter setting range.
* The comments, file register, status latch, and sampling trace are stored in order from the last address of the parameter setting range.


## 8. MEMORIES AND MEMORY CASSETTES



## POINT

(1) Do not protect the memory before executing sampling trace or status latch. This prevents data from being stored in memory.
(2) Turn OFF switch 10 of SW1 when using the SWOGHP-UTLPCFN1 utility package or the SWOGHP-MBASC software package.

### 8.2.5 Battery installation

The connector of battery is disconnected before shipment. When power failure compensation is required, connect by the following procedure.


## REMARKS

The connector of the battery has been disconnected in order to prevent discharging of the battery during transportation and storage. Couple the connector before use.

## MEMO

## 9. LOADING AND INSTALLATION

This section describes the loading and installation procedures and instructions for maximum reliability of the system.

### 9.1 Consideration for Safety

### 9.1.1 Consideration for safety

When the power of system is turned on or off, process output may not temporarily perform normal operation due to the difference between the delay time and rise time of the power supply of programmable controller main unit and the external power supply (especially DC) for the process. Also, at the time of an error of the external power supply, output process may possibly make an erroneous operation.

In order to prevent the aforementioned erroneous operations from resulting in an erroneous operation of the entire system and also for safety reasons, constitute circuits (such as emergency stop circuit, protection circuit, and interlock circuit), that prevent machine damage and/or accident due to erroneous operation outside the programmable controller.

A system design circuit example based on the above concept is shown on the following page.


The start-up procedure is as follows:

## For AC

(1) Switch on power.
(2) Set CPU to RUN.
(3) Switch on the start switch.
(4) When the magnetic contactor (MC) comes in, output equipment is powered and may be driven from the program.

## For AC/DC

(1) Switch on power.
(2) Set CPU to RUN.
(3) When DC power is established, RA2 turns on.
(4) Timer (TM) times out after the DC power reaches $100 \%$. (The set value of TM should be the period of time from when RA2 switches on to the establishment of $100 \%$ DC voltage. Set the set value to approximately 0.5 second.)
(5) Switch on the start switch.
(6) When the magnetic contactor (MC) comes in, the output equipment is powered and may be driven by the program.
9.1.2 Precautions in using a positioning system employing an MR-SB servo amplifier
(1) Wiring connections required when a motor with a solenoid brake is used
(a) The motor with a solenoid brake comes to a stop in an emergency through the application of a dynamic brake. Therefore, combined use of the solenoid brake is not helpful in reducing coasting distance.

To ensure safety, calculate the coasting distance moved by the motor in the event of failure of the dynamic brake.

1) Refer to the figure below when using a brake as a preventive means against drop of upper and lower axes.

Remember that this figure is given not for motor protection but for preventing the drop during initialization.

(b) Operation circuit for a motor with a solenoid brake

The solenoid brake is applied to the motor when the solenoid brake terminal is turned off. In the A73CPU system, the wiring connections required for the solenoid brake to be actuated are as follows:

(2) Operation sequence program of the A73CPU solenoid brake

When an A73CPU sequence program is used, turn off the solenoid brake output by turning off the servo error detection signal ( Xn 8 ) or servo ready signal ( XnF ).

Make sure that the solenoid brake output is turned off 200 ms after the servo is found normal (Xn8: OFF and XnF: ON).


## POINT

T200 is a 10 ms timer. It is subject to error.produced by scan time of the sequence program. The error is within the limits of -2 scans/+1 scan.

Disengage the solenoid brake and adjust the settings so that the servo is free of errors.
(3) Emergency stop methods

The MR-SB is brought to an emergency stop by the methods described below.
(a) By an emergency stop command to the A70SF

1) This enables a batch emergency stop for all axes of the MR-SB.
2) The batch emergency stop for all MR-SB axes is affected by turning on (low level) the ATOSF EMR (emergency stop) terminal.

After emergency stop, operation of the MR-SB can be resumed by removing the cause of the problem and turning off (high level) the EMR terminal to the A7OSF and the error detection signal ( $X n 7$ ).
(b) By making emergency stop wiring connections to the MR-SB

1) The MR-SB axes can be brought to an emergency stop individually.
2) This method is the most reliable since the emergency stop is affected by opening the emergency stop contact in the MR-SB.

After emergency stop, it is necessary to start up the MR-SB again.
(4) Allowable duration of a momentary power failure
(a) The allowable duration of a momentary power failure is 70 ms .

However, the MR-SB may stop control in the event of a momentary power failure since its allowable power failure duration is 15 ms at 200 V . In that event, the MR-SB will require initial startup.

### 9.2 Installation Environment

Never install the A73CPU, I/O module in the following environments:
(1) Locations where ambient temperature is outside the range 0 to $55^{\circ} \mathrm{C}$.
(2) Locations where ambient humidity is outside the range of 10 and $90 \% \mathrm{RH}$.
(3) Locations where dew condensation takes place due to sudden temperature changes.
(4) Locations where there are corrosive gasses or combustible gasses.
(5) Locations where there is a high level of conductive powder such as dust and iron filings, oil mist, salt, and organic solvent.
(6) Locations exposed to the direct rays of the sun.
(7) Locations where a strong power field or magnetic field is generated.
(8) Locations where vibration and shock are directly transmitted to the main unit.

### 9.3 PC Generated Heat Calculation

The ambient temperature around the PC installed in a panel must be kept below $55^{\circ} \mathrm{C}\left(131^{\circ} \mathrm{F}\right)$. To provide adequate cooling for the control box, the average current consumption (heat generation) for all equipment and instruments inside the panel must be calculated. The average current consumption for the A73CPU and the resulting temperature rise are calculated as follows:

Average power consumption

Power is consumed by the following PC areas:

(1) Power supply module power consumption

Approximately $70 \%$ of the power supply module current is converted into power with the remaining $30 \%$ dissipated as heart, i.e., $3 / 7$ of the output power is used.
$W_{p w}=\frac{3}{7}((15 \vee \times 5)+(124 \vee \times 24))(W)$
where, $15 \mathrm{~V}=5 \mathrm{VDC}$ logic circuit current consumption of each module.
$\mathrm{I}_{24} \mathrm{~V}=$ current consumption of the output modules (with an average number of points switched on)
...(Not for 24 V input power supply modules)
(2) Total 5 VDC power consumption

5 V is supplied to each module via the base plate, this powers the logic circuitry.
$W_{5 v}=15 \vee \times 5(W)$
(3) Total 24 VDC output module power consumption (with an average number of points switched on)

24 V is supplied to drive output devices.

$$
W_{24 V}=I_{24} \mathrm{~V} \times 24(W)
$$

(4) Power consumption of output circuits (with an average number of points switched on)

$$
\begin{aligned}
& \text { Wout }=\begin{array}{l}
\text { lout } \times V_{\text {drop }} \times \text { average number of outputs on at one } \\
\text { time }(W)
\end{array} \\
& \text { where, lout }=\text { output current (actual operating current) (A) } \\
& V_{\text {drop }}=\text { voltage dropped across each output load (V) }
\end{aligned}
$$

(5) Power consumption of input circuits (with an average number of points switched on)
$\mathrm{W}_{\text {in }}=\operatorname{lin} \times E \times$ average number of inputs on at one time (W)
Where, lin = input current (effective value for $A C$ ) (A)
$\mathrm{E} \quad=$ input voltage (actual operating voltage) (V)
(6) Power consumption of the special function module power supply is expressed as:

$$
W s=(15 \vee \times 5)+(124 . v \times 24)+(1100 \vee \times 100)(W)
$$

The sum of the above values is the power consumption of the entire PC system.

$$
W=W_{p w}+W_{5 v}+W_{24 v}+W_{\text {out }}+W_{\text {in }}+W_{s}(W)
$$

Further calculations are necessary to work out the power dissipated by the other equipment in the panel.

Generally temperature rise in the panel is expressed as:

$$
\begin{aligned}
& T=\frac{W}{U A}\left({ }^{\circ} \mathrm{C}\right) \\
& \text { where, } \mathrm{W}=\begin{array}{l}
\text { power consumption of the entire PC system (ob- } \\
\text { tained as above) }
\end{array} \\
& \qquad \begin{array}{l}
A=\text { panel inside surface area }\left(\mathrm{m}^{2}\right) \\
U= \\
\begin{array}{l}
6 \text { if the panel temperature is controlled by a fan, } \\
\text { etc. } 4 \text { if panel air is not circulated. }
\end{array}
\end{array} .
\end{aligned}
$$

## POINT

Fans, heat exchangers, or cooling units must be installed if the panel temperature is likely to exceed the specified temperature.
If using a fan for ventilation, beware of effects on the PC from dust drawn in with the air.

### 9.4 Mounting the Base Unit

This section describes precautions regarding installation of the main base and extension base units.

### 9.4.1 Mounting instructions

Explanation is given to the instructions for mounting the PC to a panel, etc.
(1) To improve ventilation or facilitate the replacement of unit, provide $80 \mathrm{~mm}(3.15 \mathrm{in}$ ) or more the clearance around the PC.
(2) Do not mount the base unit vertically or horizontally to allow ventilation.
(3) Ensure that the base unit mounting surface is uniform to prevent strain. If excessive force is applied to the printed circuit boards, this will result in incorrect operation. Therefore, mount the base unit on a flat surface.
(4) Avoid mounting the base unit close to vibration source, such as large-sized magnetic contactors and no-fuse breakers. Install the base unit in another panel or separate the base unit from the vibration source.
(5) Provide a wiring duct as necessary.

However, if the dimensions from the top and bottom of the PC are less than those shown in Fig. 9.1, note the following points:
(a) When the duct is located above the PC, the height of the duct should be 50 mm ( 1.97 in ) or less to allow for sufficient ventilation.

Set the distance form the top of the PC so that the hook latch at the top of the module can be pushed. If the hook latch at the top of the module cannot be pushed, the module cannot be replaced.
(b) When the duct is located under the PC, install the duct so that optical fiber cables or coaxial cables may be connected and also consider the minimum bending radius of the cable.
(6) All other equipment should be installed at least 100 mm ( 3.94 in ) away from the PC to protect it from heat and noise. The bases must be installed at least $50 \mathrm{~mm}(2 \mathrm{in})$ away from any equipment on both sides.

### 9.4.2 Installation

This section explains the mounting procedure for the main and extension base units.

Indicates the panel top, wiring duct. or any assembly.


For coaxial data link For optical data link

Fig. 9.1 Parallel Mounting


Fig. 9.2 Serial Mounting


Fig. 9.3 Minimum Front Clearance


Fig. 9.4 Vertical Mounting (Not allowed)


Fig. 9.5 Horizontal Mounting (Not allowed)

### 9.5 Installation and Removal of Module

This section explains the installation and removal procedures of the power supply module, CPU module, I/O module, special function module, etc. to and from the base module.
(1) Installation of module

The installation procedure is as follows.


Insert the two module fixing projections (two)
into the module fixing hole $(B)$ in the base unit into the module fixing hole ( $B$ ) in the base unit.

Load the module into the base unit by pushing it in the direction of arrow.


## (2) Removal of module

The removal procedure is as follows.


## POINT

To remove the module, be sure to disengage the hook from the module fixing hole ( A ) and then remove the module fixing projections from the module fixing hole (B). If the module is forcibly removed, the hook or module fixing projections will be damaged.

### 9.6 Wiring

### 9.6.1 Wiring instructions

(1) Wiring of power source
(a) When voltage fluctuations are larger than the specified value, use a constant-voltage transformer.

(b) Use a power supply which generates minimal noise across wire and across PC and ground. When excessive noise is generated, connect an insulating transformer.


Insulating transformer


Insulating transformer
(c) When a power transformer or insulating transformer is employed to reduce the voltage from 200 VAC to 100 VAC, use one with a capacity greater than that indicated in the following table.

| Power Supply Module | Transformer Capacity |
| :---: | :---: |
| A61P | 100 VA $\times n$ |
| A62P | 100 VA $\times n$ |
| A65P | 100 VA $\times n$ |
| A66P | 95 VA $\times n$ |
| A68P | 95 VA $\times n$ |

' $n$ ' stands for the number of power supply modules.
(d) When wiring, separate the PC power source from those for $1 / 0$ equipment and power equipment as shown below.


## REMARKS

As safety measures, install a switch for use with "online I/O module change' only to each of the corresponding modules and equipment.
(e) Note on using 24 VDC output of the A66PC power supply modules

To protect the power supply modules, do not supply one I/O module with 24 VDC from several power supply modules connected in parallel.

If 24 VDC output capacity is insufficient for one power supply module, supply 24 VDC from the external 24 VDC power supply as shown below:

(f) Twist the 100 VAC, 200 VAC, and 24 VDC cables as closely as possible. Connect modules with the shortest possible wire lengths.
(g) To minimize voltage drop, use the thickest (max. $2 \mathrm{~mm}^{2}$ (14 AWG)) wires possible for the $100 \mathrm{VAC}, 200 \mathrm{VAC}$, and 24 VDC cables.
(h) Do not bundle the 100 VAC and 24 VDC cables with main-circuit wires or the $1 / O$ signal wires (high-voltage, large-current). Also, do not wire the above indicated cables close to the aforementioned wires. If possible, provide more than 100 mm ( 3.94 in ) distance between the cables and wires.
(i) As a measure against very large surges (e.g. due to lightening), connect a surge absorber as shown below.


## POINT

(1) Ground the surge absorber (E1) and the PC (E2) separately from each other.
(2) Select a surge absorber making allowances for power voltage rises.
(2) Wiring of I/O equipment
(a) Applicable size of the wire which connects to the terminal block connector is 0.75 (18) to $2 \mathrm{~mm}^{2}$ (14 AWG). However, it is recommended to use wires of $0.75 \mathrm{~mm}^{2}$ (18 AWG) for handling convenience.
(b) Separate the input and output lines.
(c) I/O signal wires must be at least 100 mm ( 3.94 in ) away from high-voltage and large-current main circuit wires.
(d) When the I/O signal wires cannot be separated from the main circuit wires and power wires, ground the equipment on the PC side with batch-shielded cables. Under some conditions it may be preferable to ground it on the other side.

(e) If wiring has been done with piping, ground the piping.
(f) Separate the 24 VDC I/O cables from the 100 VAC and 200 VAC cables.
(g) If wiring requires over $200 \mathrm{~mm}(7.87 \mathrm{in}$ ) or longer distance, trouble can be caused by leakage currents due to line capacity. Take corrective action as described in Section 7.4.
(3) Grounding
(a) The A series PC has good noise resistance. Therefore, the PC may be used without grounding except when there is excessive noise.
However, follow (b) to (e) described below.
(b) Ground the PC as independently as possible. Class 3 grounding should be used (grounding resistance $100 \Omega$ or less).
(c) When independent grounding is impossible, use the joint grounding method as shown in the figure below (2).


1) Independent grounding.... Best
2) Joint grounding. $\qquad$ Good
3) Joint grounding $\qquad$ Not allowed
(d) Use $2 \mathrm{~mm}^{2}$ (14 AWG) or thicker grounding wire. Grounding point should be as near to the PC as possible to minimize the distance of the grounding cable.
(e) Should incorrect operation occur due to grounding, disconnect one or both of the LG and FG terminals of the base unit from the ground.

### 9.6.2 Wiring to terminals

This section explains the wiring of power lines and grounding lines to the CPU module and power supply module.



## POINT

(1) Use the thickest possible (max. $2 \mathrm{~mm}^{2}$ (14 AWG)) wires for the 100 V/200 VAC and 24 VDC power cables. Be sure to twist these wires starting at the connection terminais. To prevent shortcircuit should any screws become loose, use solderless terminals with insulation sleeves.
(2) When the LG terminals and FG terminals are connected, be sure to ground the wires. Do not connect the LG terminals and FG terminals to anything other than ground. If LG terminals and FG terminals are connected without grounding the wires, the PC may be susceptible to noise. Also since the LG terminal has haff the potential of the input voltage, the operator may get an electric shock if he touches it.
(3) Make sure that the power to the main base (A74B) and extension base (A65B/A68B) power supply modules the A68P power supply modules, and the A6MD remains the same.
(4) Ground terminal FG of the A6MD.

### 9.7 Grounding the A70SF-MR-SB Cable with Cable Clamps

The cable (MR-BUS[ ]M) connecting the A70SA with the MR-SB must be grounded with the cable clamp (supplied with the ATOSF) provided on the A70SF side.

Using the cable clamp, ground the cable according to the procedure described below.


Fig. 9.6 Grounding of MR-BUS[ ]M Cable with Cable Clamps

M3 $\times 0.5$ screws ( $0.12 \times 0.02$ )


Fig. 9.7 Cable Clamp

## 10. PRE-START-UP AND TEST PROCEDURES

### 10.1 Checks Before Test Start

Checks before testing are listed in Table 10.1.
Table 10.1 Checks Before Test Start

| Check them | Check Point | Refer to |
| :---: | :---: | :---: |
| CPU module | (1) Check that the memory cassette is securely loaded in the CPU. | Section 8.2.1 |
|  | (2) Check that the momory capacity matches the memory cassette capacity. | Section 8.1.1 |
|  | (3) Check that RAM/ROM selection is correct. | Section 8.2.3 |
|  | (4) Check that EP-ROM or IC-RAM is securely loaded in the ROM socket (when the A3NMCA-0 or EP-ROM is used). |  |
|  | (5) Check that the two EP-ROMs and IC-RAMs loaded are of the same type. | Section 8.1.2 |
|  | (6) Check that the memory protect switch is set to OFF. | Section 8.2 .4 |
|  | (7) Check that the lead wire connector of the memory cassette battery (A6BAT) is securely connected to the pin connector on the printed circuit board. | Section 8.2.5 |
|  | (8) Check if the battery is low on voltage (nominal value: 3.16 V ). | Section 11.3 |
| Power supply module | (1) Check that the type of the power supply module installed on the base is correct (only the A61P can be installed on the A74B). | - |
|  | (2) Check for blown fuse. | Section 6.2.2 |
|  | (3) Check that the supply voltage and the power supply module voltage are correct (this check is not applicable to the A63P). |  |
|  | (4) Check that the polarity (+/-) of the power cable is not reversed (this check is applicable only to the A63P). |  |
|  | (5) Check that the FG and LG terminals are correctly wired. | Section 9.6.2 |
|  | (6) Check that the terminals are fully tightened. | Section 6.2.1 |
|  | (7) Check that the size of the cable wire is correct. | Section 6.1.1 |
| Input/output module | (1) Check that the cables connected to the terminals on the terminal blocks match the designations of the signals. | Building 8lock Input/Output Module User's Manual |
|  | (2) Check that the terminal screws are fully tightened. |  |
|  | (3) Check that the size of the cable wire is suitable. |  |
|  | (4) Check that the external supply power line is properly connected 224 VDC, $\pm 15$ VDC, etc.). |  |
| Special function module | (1) Check that switch positions are correct. | User's Manual for the special module used |
|  | (2) Check that the terminals on the terminal blocks match the designations of the signals connected. |  |
|  | (3) Check that the terminal screws are fully tightened. |  |
|  | (4) Check that the size of the cable wire is correct. |  |
|  | (5) Check that the external supply power line is correctly connected ( 24 VDC, $\pm 15$ VDC, etc.). |  |
| Dummy module (AG62) | (1) Check that the point number setting switches are correctly set. | Building Block input/Output Module User's Manual |


| Check ltem | Check Point | Refer to |
| :---: | :---: | :---: |
| Main base unit | (1) Check that the type of the main bace unit used is correct (the A73CPU can only be loaded on the A74B). | Sections 2.1 through 2.3 |
|  | (2) Check that the unit loaded is correct. |  |
|  | (3) Check that the loading order is correct. |  |
|  | (4) Check that the unit is securely loaded. | Section 9.5 |
| Extension base unit | (1) Check that the extension bese unit is correct (only the A65B/A68B can be connected to the A74B). | Sections 2.1 through 2.3 |
|  | (2) Check that the unit loaded is correet. | Section 2.6 |
|  | (3) Check if the total number of input/output points on the input/output module and the special function module exceeds the number of input/output points of the CPU module. | Section 4.1 |
|  | (4) Extension stage number setting <br> (a) Chock that the number is set. <br> (b) Check that the same number is set. <br> (c) Chock that one base unit is used to affect two or more settings. | $\begin{aligned} & \text { Sections 7.2.3 } \\ & \text { and 7.2.4 } \end{aligned}$ |
|  | (5) Check that the unit is securely loaded. |  |
| Extension cable | (1) Check that the extension cable connector is correctly connected to the base unit connector. | Section 7.1.2 |
|  | (2) Check that the extension cable connector is correctly positioned. | Section 7.2.2 |
|  | (3) Check that the total length of extension cables is 6.6 m ( 21.65 ft .) or less. | Sections 2.1 through 2.3 |

### 10.2 Servo Start-up

This section describes the servo start-up procedure to be followed by the A73CPU and peripheral devices.

## POINT

(1) The nameplate cannot be seen when the motor is installed on the machine. Take note of the motor model number on the nameplate before motor installation.
(2) Before initially turning on the servo amplifier or servo motor, check its performance separately. This precaution should be taken to prevent unexpected machine failure or accident.
(3) The confirmation or setting mode for peripheral devices applies when the MR-SB is used.
Confirmation or setting cannot be affected in modes marked with an asterisk (*) when a general-purpose servo amplifier is used.


- For the A74B loading unit and position, see Sections 2.1 through 2.3.
- For installation of the unit, see Section 9.5.



The EMG indication appears when the emergency stop command is given to the emergency stop check screen.
.... Indicate the axis number error definition of the servo amplifier which has been found to be faulty on the initial check screen.
.... Check the preset servo amplifier and servo motor types against the displayed types.

Disengage the motor brake. Relemee the emergency stop status so that the motor can be brought to an emergency stop in the event of an error.


### 10.3 Axis Number Setting

The A73CPU is capable of controlling a total of 8 axes for an MR-SB and a general-purpose servo amplifier.

During positioning control, all the axes are controlled on an axis number basis.

The method of setting the axis number for the MR-SB and the generalpurpose servo amplifier is described below.
(1) MR-SB

Connect 2 MR-SB systems, each consisting of 4 axes, to the A70SF.
When the axis number switch is set from 0 to 3 in either system, the MR-SB switch setting represents axes 1 to 4 or axes 5 to 8 when the A70SF connector is connected. The axis number can be set without regard to the connecting order of servo amplifiers. However, the axis number set for one MR-SB amplifier should not be the same as those set for another MR-SB amplifier connected to the same connector. If the same axis number is set, that axis will not function.


Fig. 10.1 MR-SB AxIs Number Setting
(2) General-purpose Servo Amplifier

The general-purpose servo amplifier is connected to the ATOAF on a one-to-one basis.
When the axis number is set, use slide switches 5 to 7 on the A70AF so that axes 1 to 8 can be set. The axis number can be set without regard to the loading order of the A7OAF.
The axis number set by the general-purpose servo amplifier should not be the same as that set by the general-purpose servo amplifier connected to the same connector. If the same axis number is set, that axis will not function.

(3) When an MR-SB and a general-purpose servo amplifier are used together
When an MR-SB and a general-purpose servo amplifier are used together, a total of 8 axes can be used and axes 1 to 8 can be selected through axis number setting.
The axis number set by the MR-SB should not be the same as that set by the general-purpose servo amplifier.
If the same axis number is set, the following condition will exist:
(a) The axis number which is set at the same time by two MR-SB amplifiers or by two A70AF interfaces will not function.
(b) When the axis number set by the MR-SB is the same as that set by the general-purpose servo amplifier, the servo amplifier that has been set with parameters will function.

### 10.4 Servo Diagnosis

The term "servo diagnosis' means checking, at the peripheral device, whether the velocity and position loop gain values are optimum to the load connected to the servo motor.

Servo diagnosis can only be performed when the MR-SB servo amplifier is in use.
(1) Velocity loop gain check
(a) During velocity loop gain check, the responsibility and stability of the servo motor are diagnosed by checking the settling time (response time) and the amount of overshoot which are measured when the motor rotates 1.6 turns in response to the number of revolutions ( 200 rpm ) command from the A73CPU.

Number of revolutions

(1) The amount of overshoot is determined by the following equation:

$$
\text { (Amount of overshoot) }=\frac{\text { (Meximum number of revolutions) }-200}{200} \times 100[\%]
$$

(2) Here "settling time" means the time taken for the number of revolutions to reach $200 \mathrm{rpm} \pm 10 \mathrm{rpm}$.

Fig. 10.2 Velocity Loop Gain Check
(2) Position loop gain check

During the position loop gain check, the responsibility and stability of the servo motor are diagnosed by checking the amount of undershoot feedback in response to the positioning command from the A73CPU, settling time (response time), and vibration width (number of pulses accumulated in the error counter).

(1) The amount of undershoot is determined by the following equation:
(Amount of undershoot) $=\frac{\text { Max. number of revolutions in reverse from stop }}{100}$
(2) Here 'settling time' means the elapsed time (in ms) from the moment the command value reaches zero to the moment the motor comes to a stop.
(3) The vibration width indicates the number of accumulated pulses.

Fig. 10.3 Position Loop Gain Check

### 10.5 Self-diagnosis

(1) In self-diagnosis, the A73CPUP21/R21 hardware, fiber optic cable, and coaxial cable are checked for broken wires. Any of the following checks can be selected by changing the mode select switch position.

| $\begin{array}{c}\text { Switch } \\ \text { Position }\end{array}$ | Mode Designation | Description |
| :---: | :---: | :--- |
| 3 | Forward loop test mode | $\begin{array}{l}\text { In this mode, the fiber optic cable or } \\ \text { coaxial cable line of the entire data link } \\ \text { system is ehecked. The forward loop } \\ \text { side on which normal linking is per- } \\ \text { formed is checked. }\end{array}$ |
| 4 | $\begin{array}{l}\text { Reverse loop test mode }\end{array}$ | $\begin{array}{l}\text { In this mode, the fiber optic cable or } \\ \text { coaxial cable line of the entire data link } \\ \text { system is checked. The reverse loop } \\ \text { side, on which loopback is performed } \\ \text { in the event of an error, ls checked. }\end{array}$ |
| 5 | $\begin{array}{l}\text { Station-to-station test } \\ \text { mode (main station) }\end{array}$ | $\begin{array}{l}\text { In this mode, the line connecting the } \\ \text { two stations is checked. Before check- } \\ \text { ing, the station with the smaller station }\end{array}$ |
| number is designated as the main sta- |  |  |
| tion; the other is the subordinate station. |  |  |$\}$

(2) For tests other than the self-loopback test, see the MELSECNET Data Link System Reference Manual.

### 10.5.1 Self-loopback test

(1) Self-loopback test
(a) The self-loopback test is intended to check the link unit hardware containing the transmission and receiving circuits (forward and reverse loops) on an individual link unit basis.
(b) A distinction between normal and faulty conditions is made depending on whether the data sent from the send end can be received within the specified duration at the receive end of the forward and reverse loops.

## Fiber Optic Cable



Coaxial Cable


Fig. 10.4 Self-loopback Test
(2) Testing method

The self-loopback test procedure is given below.

.... Self-loopback test is solected.
.. Conduct self-loopback test about 7 sec. fter reset.

(3) Judgment on test results The test results are indicated by the LEDs on the A73CPUP21/R21 front panel.
(a) When the results are normal:

The CRC, OVER, AB.IF, TIME, DATA, and UNDER LEDs successively turn on and off.
(b) When the results are abnormal:

The LED indicating the error in question lights and the test is discontinued.

1) When the F.LOOP, R.LOOP, and TIME LEDs are lit:
i) The forward loop cable is broken.
ii) The forward loop send side and receive side are not connected.
iii) The forward loop send side is connected with the reverse loop send side and the reverse loop receive side is connected with the forward loop receive side.
2) When the F.LOOP, R.LOOP, and DATA LEDs are lit:
i) The reverse loop cable is broken.
ii) The reverse loop send side and receive side are not connected.
3) When the ERROR LEDs other than those mentioned in items (1) and (2) are lit:
i) The hardware is faulty.
ii) The cable was disconnected during testing.
iii) The cable was broken during testing.

## 11. MAINTENANCE AND INSPECTION

This section describes items for daily and periodic maintenance and inspection in order to maintain the programmable controller in the normal and best conditions.

### 11.1 Dally Inspection

Table 11.1 shows the inspection items which are to be checked daily.
Table 11.1 Daily Inspection

| No. | Check Item |  | Check Point | Judgment | Corrective Action |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | Base unit mounting conditions |  | Check for loose mounting screws and cover. | The base unit should be securely mounted. | Retighten screws. |
| 2 | Mounting conditions of 1/O module, etc. |  | Check if the module is disengaged or the hook is securely engaged. | The hook should be securely engaged and the module should be positively mounted. | Securely engage the hook. |
| 3 | Connecting conditions |  | Check for loose terminal screws. | Screws should not be loose. | Retighten terminal screws. |
|  |  |  | Check distance between solderless terminals. | Proper clearance should be provided between solderless terminals. | Correct. |
|  |  |  | Check connectors of extension cable. | Connections should not be loose. | Retighten connector mounting screws. |
| 4 | CPU module. indicator lamps | 'POWER' LED | Check that the LED is on. | On. (Off indicates an error.) | See Section 12.2.2. |
|  |  | "RUN" LED | Check that the LED is on during run. | On. (Off or flicker indicates an error.) | See Section 12.2.3 and 12.2.4. |
|  |  | Input LED | Check that the LED turns on and off. | On when input is on. Off when input is off. (Any discrepancy from the above indicates an error.) | See Section 12.2.5. |
|  |  | Output LED | Check that the LED turns on and off. | On when input is on. Off when output is off. (Any discrepancy from the above indicates an error.) | See Section 12.2.5. |

REMARKS
To change any I/O module during PC operation, see Section 2.2.8 ${ }^{\circ}$ Online I/O module replacement" (see the A73CPU Reference Manual).

### 11.2 Periodic Inspection

This section explains the inspection items which are to be checked every six months to one year. If the equipment have been moved or modified or wiring has been changed, also make the inspection.

Table 11.2 Periodic Inspection

| No. | Check Item |  | Check Method | Judgment | Corrective Action |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | Ambient environment | Ambient temperature | Measure with thermometer and hygrometer. Measure corrosive gas. | 0 to $55^{\circ} \mathrm{C}$ | When PC is used inside a panel, the temperature in the panel is ambient temperature. |
|  |  | Ambient humidity |  | 10 to 90\% RH |  |
|  |  | Atmosphere |  | There should be no corrosive gases. |  |
| 2 | Line voltage check |  | Measure voltage across 100/200 VAC terminal. | $\begin{aligned} & 85 \text { to } 132 \mathrm{~V} \\ & 170 \text { to } 264 \mathrm{VAC} \end{aligned}$ | Change supply power. Change trans. former tap. |
| 3 | Mounting conditions | Looseness, play | Try to move the unit. | The module should be mounted securely and positively. | Retighten screws. For CPU, //O, and power supply modules check all connections. |
|  |  | Adhesion of dust or foreign material | Visual check | There should be no dust or foreign material, in the vicinity of the P.C. | Remove and clean. |
| 4 | Connecting conditions | Loose terminal screws | Retighten. | Connectors should not be loose. | Retighten. |
|  |  | Distances between solderless terminals. | Visual check. | Proper clearance should be provided between solderless terminals. | Correct. |
|  |  | Loose connector | Visual check | Connectors should not be loose. | Retighten connector mounting screws. |
| 5 | Battery |  | Check battery status by monitoring special auxiliary relays M9006 and M9007. Replace battery if necessary. | Preventive maintenance | If battery capacity reduction is not indicated, change the battery when specified service life is exceeded. |
| 6 | Fuse |  | Check fuses. | Preventive maintenance | Change the fuse periodically due to rush current. |

### 11.3 Replacement of Battery

M9006 or M9007 turns on when the voltage of battery for program backup and power failure compensation reduces.

Even if this special relay turns on, the contents of the program and power failure compensation are not lost immediately.

However, if the ON state is overlooked, the PC contents may be lost.
Special auxiliary relays M9006 and M9007 are switched on to indicate that the battery life has reduced to the time (minimum) indicated in Table 11.3 and it must be replaced if continued power failure RAM and/or data back-up is required.

The following sections gives the battery service life and the battery changing procedure.
11.3.1 Service life of battery

This service life of the battery depends on the capacity of the memory.
Table 11.3 shows service life according to memory.
Table 11.3 Battery Life

| Battery Life <br> Memory Cassette Type | Battery Life (Total power failure time) [Hr] |  |  |
| :---: | :---: | :---: | :---: |
|  | Guaranteed Value <br> (Min.) | Actually Applied Value <br> (Typ) | After M9006, M9007 <br> has turned on. |
| A3NMCA-2 | 4100 | 10250 | 168 |
| A3NMCA-4 | 4100 | 10250 | 168 |
| A3NMCA-8 | 3410 | 8525 | 168 |
| A3NMCA-16 | 2600 | 6505 | 168 |
| A3NMCA-24 | 2140 | 5350 | 168 |
| A3NMCA-40 | 1400 | 3500 | 168 |
| A3NMCA-56 | 450 | 125 | 168 |

- The actually applied value indicates a typical value and the guaranteed value indicates the minimum value.

Preventive maintenance is as described below.
(1) Even if the total power failure time is less than the guaranteed value in the above table, change the battery after four to five years.
(2) When the total power failure time has exceeded the guaranteed value in the above table and M9006 has turned on, change the battery.

### 11.3.2 Battery changing procedure

When the service life of the battery has expired, change the battery using the following procedure.

Even if the battery is removed, the memory is backed by a capacitor for some time.

However, if the changing time exceeds the guaranteed value shown in the following table, the contents of the memory may be lost. Therefore, change the battery as fast as possible.

Table 11.4 Backup Time by Capacitor


### 11.4 Replacement of Fuse

Even if the fuse does not blow, its element may be consumed due to rush current. Therefore, it is recommended to change the fuse periodically.
11.4.1 Replacement of fuse for power supply

The fuse changing procedure is explained.

11.4.2 Replacement of fuse for output module


## 12. TROUBLESHOOTING

This section describes various procedures for establishing the nature of any faults, and corrective action.

### 12.1 Basic Troubleshooting

System reliability depends not only on reliable equipment but also on short down-time in the event of faults.

The basic points to be kept in mind in troubleshooting are the following three.
(1) Visual checks

Check the following points.
(a) Machine motions (in stop and operating statuses)
(b) ON or OFF of power
(c) Status of I/O equipment
(d) Conditions of wiring ( $1 / 0$ wires, cables)
(e) Display states of various indicators (such as POWER LED, RUN LED, and I/O LED)
(f) States of various setting switches (such as extension base and power failure compensation)

After checking (a) to (f), connect the peripheral device and check the running status of PC and the contents of program.
(2) Trouble check

Observe any changes in the error condition with the following procedure.
(a) Set the RUN key switch to the "STOP" position.
(b) Perform reset by the RESET key switch.
(c) Turn the power on and off.
(3) Narrow down the possible causes of the trouble

Deduce where the fault lies i.e:
(a) Inside or outside of PC.
(b) $1 / 0$ module or another module.
(c) Sequence program.

### 12.2 Troubleshooting

This section explains the procedure for determining the cause of problems and the errors and corrective actions for error codes.

### 12.2.1 Troubleshooting flow chats

Details for fault finding may be found as follows:

12.2.2 Flow chart used when "POWER" LED has turned off

The flow chart below shows the procedure for when the POWER LED turns off during operation or when the power is turned on.

12.2.3 Flow chart used when "RUN" LED has turned off

The flow chart below shows the procedure for when the RUN LED turns off during operation.


### 12.2.4 Flow chart used when "RUN" LED flickers

The flow chart below shows the procedure for when the RUN LED flickers when the power is turned on, when operation is started or during operation.

12.2.5 Flow chart used when output load of output module does not tum on

The flow chart below shows the procedure for when the output load of an output module does not turn ON during operation.


## POINT

If the input signal or load is not switched off, see Section 12.4.
12.2.6 Malfunction in program down load to PC

The flow chart below shows the procedure for when a program cannot be written to the CPU.


### 12.3 1/O Connection Troubleshooting

This section explains possible problems with I/O circuits.
12.3.1 Input wiring troubleshooting

This section describes possible problems with the input circuit and corrective actions.

Table 12.1 Input Circuit Troubles and Corrective Actions

| $\begin{aligned} & \text { Ex- } \\ & \text { ample } \end{aligned}$ | Condition | Cause | Corrective Action |
| :---: | :---: | :---: | :---: |
| 1 | Input sig. nal does not turn off. | Leakage current of input switch (such as drive by non-contact switch). | Connect an appropriate register which will make the voltage across terminals of input module lower than OFF voltage value. <br> $A C$ input <br> It is recommended to use 0.1 to $0.47 \mu \mathrm{~F}$ +47 to $12 \Omega(1 / 2 \mathrm{~W})$ for the constant of CR. |
| 2 | input signal does not turn off. |  | Same as Example 1. <br> Or make up another independent display circuit. |
| 3 | Input signal does not turn off. | Leakage current due to line capacity of wiring cable. Line capacity C of twisted pair wire is approx. $100 \mathrm{PF} / \mathrm{m}$ ( 39.37 in ). | Same as Example 1. <br> However, leakage current is not generated when power supply is located on the input equipment side as shown below. |
| 4 | Input signal does not turn off. | * Drive by switch with LED indicator. | Connect a resistor which will make the voltage across input unit terminal and common higher than OFF voltage, as shown below. <br> The calculation example of connected resistor value is shown on the following page. |

Table 12.1 Input Circuit Troubles and Corrective Actions (Continued)

| Ex- <br> ample | Condition | Cause | Corrective Action |
| :--- | :--- | :--- | :--- |
| 5 | Sneak path due to the use of two power <br> supplies. <br> nal does <br> not turn off. | Use only one power supply. <br> Connect a sneak path prevention diode. <br> (Figure below) |  |

Calculation example for Example 4


The switch with LED indicator is connected to AX40 and there is 4 mA leakage current.

- The voltage $V_{T B}$ across terminal and common is obtained by the following expression:
$V_{T B}=4[\mathrm{~mA}] \times 2.4[\mathrm{k} \Omega]=9.6[\mathrm{~V}]$
(The voltage drop of LED is ignored.)
Since this voltage does not satisfy the OFF voltage of 6 [V] or lower, the input signal does not turn off. Therefore, connect a resistor as shown below.

- Calculate the resistor value, R, as shown below: For an input voltage 6 V , current I must be:

$$
(24-6[\mathrm{~V}]) \div 3.6[\mathrm{k} \Omega]=5 \mathrm{~mA}
$$

Resistor R must be selected to give a current I 5 mA .

- Hence, for resistor, R

$$
\begin{aligned}
& 6[\mathrm{~V}] \div R>5-2.5[\mathrm{~mA}] \\
& 6[\mathrm{~V}] \div 2.5[\mathrm{~mA}]>R \\
& 2.4[\mathrm{k} \Omega]>R
\end{aligned}
$$

For $R=2 k \Omega$, the power capacity must be:

$$
W=\text { (applied voltage })^{2} / R \text { (or } W=\text { (maximum current) } 2 \times R \text { ) }
$$

Resistor $R$ terminal voltage is:

$$
\begin{aligned}
\frac{2.4 \times 2}{2.4+2}[\mathrm{k} \Omega]: \frac{2.4 \times 2}{2.4+2}+3.6[\mathrm{k} \Omega] & =x: 24[\mathrm{~V}] \\
x & =5.58[\mathrm{~V}]
\end{aligned}
$$

Therefore, the power capacity $W$ or resistor $R$ is

$$
W=(5.58[\mathrm{~V}])^{2} / 2[\mathrm{k} \Omega]=0.015[\mathrm{~W}]
$$

- Use a safety factor of 3 to 5 . Resistor should therefore be rated at 0.5 to 1 W . A $2 \mathrm{k} \Omega, 0.5$ to 1 W resistor should therefore be connected across the relevant input terminal and its COM.


### 12.3.2 Output circuit problems and corrective action

This section described possible problems with the output circuit and corrective actions to be taken.

Table 12.2 Output Circuit Failures and Corrective Actions

| $\begin{aligned} & \text { Ex- } \\ & \text { ample } \end{aligned}$ | Condition | Cause | Corrective Action |
| :---: | :---: | :---: | :---: |
| 1 | When output is off, excessive voltage is applied to load. | Load is half-wave rectified inside (seen in some solenoids). <br> When the polarity of power supply is as shown by (1), C is charged. When the polarity is as shown by (2), voltage charged in C plus line voltage are applied across D1. Max. voltage in approx. 2.2 E. | Connect a resistor of several ten $k \Omega$ to several hundred k\& across the load. Ha resistor is used in this way, it does not pose problem to output element but may sometimes cause the diode, which is built in the load, to deteriorate, resulting in burning etc. |
| 2 | Load does not turn off. (Triac output) | Leakage current due to built-in noise suppression | Connect $C$ and $R$ across the load: When witing distance from output card load is long, there may be a leakage current due to the line capacity. |
| 3 | When load is C-R type timer, time constant fluctuates. (Triac output) |  | After driving the relay, drive the C-R type timer by the same contact. <br> Some timers have half-wave rectified internal circuits. Therefore, take precautions as indicated in Example 1. <br> Calculate the CR constant depending on the load. |
| 4 | Load does not turn off. (For direct current) | - Sneak path due to the use of two power supplies. <br> - When E1 < E2, sneak path occurs. | - Reduce the power supplies from two to one. <br> - Connect a sneak path prevention diode. <br> When the load is a relay or similar device, it is necessary to connect a reverse-voltage absorbing diode to the load. (Shown by the dotted line in the figure on the left) |

Table 12.2 Output Circuit Failures and Corrective Actions (Continued)

| Ex- <br> ample | Condition | Cause | Corrective Action |
| :--- | :--- | :--- | :--- |
|  | External load malfunction or incorrect <br> connection. <br> Load does <br> not <br> operate <br> normally <br> (due to ex. <br> ternal <br> shorting, <br> etc.) <br> AY60EP, <br> AY80EP, <br> AY81EP, <br> AY82EP | Check the external load. <br> Check voltage across the following ter- <br> minals with output (Y) on. If output volt- <br> age exceeds 3 V, check external load <br> and wiring for short circuits. |  |

## APPENDIX 1 Dimensions

### 1.1 CPU Module

(1) A73CPU

(2) A73CPUR21

(3) A73CPUP21


### 1.2 Positioning Module

### 1.2.1 A7OSF



### 1.2.2 A70MDF


1.2.3 A70AF


### 1.3 Power Supply Module

(1) Type A61P, A62P, A63P and A65P power supply module

(2) Type A66P power supply module

(3) Type A68P power supply module


### 1.4 Base Units

### 1.4.1 Main base units



### 1.4.2 Extension base units



### 1.5 Memory Cassette (A3NMCA-[ ])



## IMPORTANT

The components on the printed circuit boards will be damaged by static electricity, so avoid handling them directly. If it is necessary to handle them take the following precautions.
(1) Ground human body and work bench.
(2) Do not touch the conductive areas of the printed circuit board and its electrical parts with any non-grounded tools etc.

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