MITSUBISHI QCPU(Q mode) CPU Module

User's Manual (Hardware)

Q00JCPU Q02CPU Q12PHCPU Q00CPU Q02HCPU Q25PHCPU Q01CPU Q06HCPU Q12HCPU Q25HCPU

Thank you for buying the Mitsubishi programmable logic controller MELSEC-Q Series

Prior to use, please read both this manual and detailed manual thoroughly and familiarize yourself with the product.



Mitsubishi Programmable Logic Controller

MODEL QCPU(Q)-U(H/W)-E MODEL 13JL96 IB(NA)-0800061-J(0209)MEE

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SAFETY PRECAUTIONS

(Read these precautions before using)

When using Mitsubishi equipment, thoroughly read this manual and the related manuals introduced in this manual. Also pay careful attention to safety and handle the module correctly.

These SAFETY PRECAUTIONS Classify the safety precautions into two categories: "DANGER" and "CAUTION".



Depending on circumstances, procedures indicated by **CAUTION** may also cause serious accidents.

In any case, it is important to follow the directions for usage. Store this manual in a safe place so that you can take it out and read it whenever necessary. Always forward it to the end user.

[DESIGN PRECAUTIONS]

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- Install a safety circuit external to the PLC that keeps the entire system safe even when there are problems with the external power supply or the PLC module. Otherwise, trouble could result from erroneous output or erroneous operation.
 (1) Outside the PLC, construct mechanical damage preventing interlock
 - (1) Outside the PLC, construct mechanical damage preventing interlock circuits such as emergency stop, protective circuits, positioning upper and lower limits switches and interlocking forward/reverse operations.

(2) When the PLC detects the following problems, it will stop calculation and turn off all output in the case of (a).

In the case of (b), it will stop calculation and hold or turn off all output according to the parameter setting.

Note that the AnS series module will turn off the output in either of cases (a) and (b).

- (a) The power supply module has over currentprotection equipment and over voltage protection equipment.
- (b) The CPU module self-diagnosis functions, such as the watch dog timer error, detect problems.

In addition, all outputs will be turned on when there are problems that the CPU module cannot detect, such as in the I/O controller. Build a fail-safe circuit or provide a mechanism exterior to the PLC that will make sure the equipment operates safely at such times. For a failsafe circuit example, see "Loading and Installation" in this manual.

- (3) Output could be left on or off when there is trouble in the outputs module relay or transistor. So build an external monitoring circuit that will monitor any single outputs that could cause serious trouble.
- When overcurrent which exceeds the rating or caused by short-circuited load flows in the output module for a long time, it may cause smoke or fire. To prevent this, configure an external safety circuit, such as fuse.
- Build a circuit that turns on the external power supply when the PLC main module power is turned on. If the external power supply is turned on first, it could result in erroneous output or erroneous operation.

[DESIGN PRECAUTIONS]

When there are communication problems with the data link, refer to the corresponding data link manual for the operating status of each station. Not doing so could result in erroneous output or erroneous operation.

When connecting a peripheral device to the CPU module or connecting a personal computer or the like to the intelligent function module to exercise control (data change) on the running PLC, configure up an interlock circuit in the sequence program to ensure that the whole system will always operate safely.

Also before exercising other control (program change, operating status change (status control)) on the running PLC, read the manual carefully and fully confirm safety.

Especially for the above control on the remote PLC from an external device, an immediate action may not be taken for PLC trouble due to a data communication fault.

In addition to configuring up the interlock circuit in the sequence program, corrective and other actions to be taken as a system for the occurrence of a data communication fault should be predetermined between the external device and CPU module.

- Do not bunch the control wires or communication cables with the main circuit or power wires, or install them close to each other. They should be installed 100 mm (3.94 inch) or more from each other. Not doing so could result in noise that would cause erroneous operation.
- When controlling items like lamp load, heater or solenoid valve using an output module, large current (approximately ten times greater than that present in normal circumstances) may flow when the output is turned OFF to ON.

Take measures such as replacing the module with one having sufficient rated current.

[INSTALLATION PRECAUTIONS]

Use the PLC in an environment that meets the general specific	cations
contained in this manual. Using this PLC in an environment ou	utside the
range of the general specifications could result in electric show	x, fire,
erroneous operation, and damage to or deterioration of the pro	oduct.
Hold down the module mounting lever at the module bottom, a	and securely
insert the module fixing latch into the fixing hole in the base ur	nit. Incorrect
mounting of the module can cause a malfunction, failure or dro	эр.
When using the PLC in the environment of much vibration, tig	hten the
module with a screw.	
Tighten the screw in the specified torque range. Undertighteni	ng can cause
a drop, short circuit or malfunction. Overtightening can cause	a drop, short
circuit or malfunction due to damage to the screw or module.	
When installing extension cables, be sure that the connectors	of base unit
are installed correctly. After installation, check them for looser	less. Poor
connections could cause an input or output failure.	
 Securely mount the memory card into the memory card mount 	ting
connector. After mounting, check for lifting. Lifting can cause a	a malfunction
due to a contact fault.	
 Completely turn off the external power supply before mounting 	g or removing
the module. Not doing so could result in electric shock or dam	age to the
product.	
Note that online module change can be made when the QnPF	ICPU is used.
Note that there are restrictions on the modules that can be cha	anged online
and each module has a predetermined changing procedure.	_
For details, refer to the section of online module change in the	Process
CPU User's Manual (Hardware Design, Maintenance and Insp	ection).
Do not directly touch the module's conductive parts or electron	JIC
components. Touching the conductive parts could cause an op	peration
failure or give damage to the module.	

[WIRING PRECAUTIONS]

- Completely turn off the external power supply when installing or placing wiring. Not completely turning off all power could result in electric shock or damage to the product.
- When turning on the power supply or operating the module after installation or wiring work, be sure that the module's terminal covers are correctly attached. Not attaching the terminal cover could result in electric shock.

- Be sure to ground the FG terminals and LG terminals to the protective ground conductor. Not doing so could result in electric shock or erroneous operation.
- When wiring in the PLC, be sure that it is done correctly by checking the product's rated voltage and the terminal layout. Connecting a power supply that is different from the rating or incorrectly wiring the product could result in fire or damage.
- External connections shall be crimped or pressure welded with the specified tools, or correctly soldered. Imperfect connections could result in short circuit, fires, or erroneous operation.
- Tighten the terminal screws with the specified torque. If the terminal screws are loose, it could result in short circuits, fire, or erroneous operation. Tightening the terminal screws too far may cause damages to the screws and/or the module, resulting in fallout, short circuits, or malfunction.
- Be sure there are no foreign substances such as sawdust or wiring debris inside the module. Such debris could cause fires, damage, or erroneous operation.
- The module has an ingress prevention label on its top to prevent foreign matter, such as wire offcuts, from entering the module during wiring.
 Do not peel this label during wiring.

Before starting system operation, be sure to peel this label because of heat dissipation.

[STARTUP AND MAINTENANCE PRECAUTIONS]

- Do not touch the terminals while power is on.
 Doing so could cause shock or erroneous operation.
- Correctly connect the battery. Also, do not charge, disassemble, heat, place in fire, short circuit, or solder the battery. Mishandling of battery can cause overheating or cracks which could result in injury and fires.
- Switch all phases of the external power supply off when cleaning the module or retightening the terminal or module mounting screws. Not doing so could result in electric shock.

Undertightening of terminal screws can cause a short circuit or malfunction. Overtightening of screws can cause damages to the screws and/or the module, resulting in fallout, short circuits, or malfunction.

• The online operations conducted for the CPU module being operated, connecting the peripheral device (especially, when changing data or operation status), shall be conducted after the manual has been carefully read and a sufficient check of safety has been conducted. Operation mistakes could cause damage or problems with of the module. Do not disassemble or modify the modules. Doing so could cause trouble, erroneous operation, injury, or fire. • Use any radio communication device such as a cellular phone or a PHS phone more than 25cm (9.85 inch) away from the PLC. Not doing so can cause a malfunction. • Completely turn off the external power supply before loading or unloading the module. Not doing so could result in electric shock or damage to the product. Note that online module change can be made when the QnPHCPU is used. Note that there are restrictions on the modules that can be changed online and each module has a predetermined changing procedure. For details, refer to the section of online module change in the Process CPU User's Manual (Hardware Design, Maintenance and Inspection). Be sure not make a strong impact on the battery to be mounted into the module by dropping or similar careless action. This could break the battery, causing an internal battery liquid leakage. Do not use the battery which has been exposed to a strong impact, and dispose of it. [DISPOSAL PRECAUTIONS]

When disposing of this product, treat it as industrial waste.

Revisions

* The manual number is described at the lower right of the front cover.

Print Date	*Manual Number	Revision		
Nov.,1999	IB(NA)-0800061-A	First printing		
May.,2000	IB(NA)-0800061-B	Q33B, Q63B, QX70, QX71, QX72,		
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		Correction		
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		Section5.4.1		
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-		and Q55B added.		
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		Chapter 1, Table 4.2, Section 4.5, Section		
		4.5 changed to be Section 4.6.		
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		Correction		
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Sep.,2002	IB(NA)-0800061-J	Completely reviewed and revised.		
		Q32SB, Q33SB, Q35SB, Q61SP added		

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CONTENTS

1.	Overview	1
	1.1 Included Parts	1
	1.2 Confirming the serial number and function version	3
2.	Specifications	4
	2.1 General Specifications	4
	2.2 Performance Specifications of CPU Modules	
	2.3 Power supply module specifications	10
	2.4 Specifications of Base Units	16
3	EMC and Low Voltage Directive	17
0.	3.1 Requirements for conformance to FMC Directive	17
	3.1.1 Standards applicable to the EMC Directive	18
	3.1.2 Installation instructions for FMC Directive	19
	3 1 3 Cables	20
	3 1 4 Power supply module	. 20
	3.1.5 When using 0.01 S6 B type base unit	. 22
	3.1.6 Others	22
	3.2 Requirement to Conform to the Low Voltage Directive	20
	3.2.1 Standard applied for MELSEC-O series PLC	20
	3.2.2 PLC soloction	20
	3.2.2 FLC Selection	.20
	3.2.3 Fower supply	. 21 27
	2.2.5 Grounding	. 21 つの
	3.2.5 Glounding	. 20 20
٨	S.Z.O External willing	. 20
4.	4.1 Module Installation	. 29 20
	4.1 Would Installation	. 29
	4.1.1 Handling instructions	. 29
	4.1.2 Instructions for mounting the base unit	.31
	4.1.3 Mounting and removing of module	.36
	4.1.4 Setting the extension number of the extension base unit	.38
	4.1.5 Connecting and disconnecting extension cable	.39
	4.2 General Safety Requirements	.42
	4.3 Power Supply Wiring	.47
	4.3.1 Precaution when connecting the uninterruptive power supply	.49
	4.4 Part Names and Settings of Q00J/Q00/Q01CPU	. 50
	4.4.1 Part names and settings	. 50
	4.4.2 Switch operation after writing program	. 54
	4.4.3 Reset operation	. 55
	4.4.4 Latch clear operation	. 56
	4.5 Part Names and Settings of	
	Q02 (H) /Q06H/Q12H/Q25H/Q12PH/Q25PHCPU	. 57
	4.5.1 Part names and settings	. 57
	4.5.2 Switch operation after writing program	. 60
	4.5.3 Latch clear operation	.60
	4.5.4 Mounting and removing of memory card during power-on	.61
	4.5.5 Executing automatic write to standard ROM.	.61

63
66
67
67
70
74
74
77
81
81
84
86
86
87
88
88

About the Manuals

The following product manuals are available. Please use this table as a reference to request the appropriate manual as necessary.

Basic Model QCPU (Q00JCPU, Q00CPU, Q01CPU)

Manual name	Manual No. (Model code)
Basic Model QCPU (Q Mode) User's Manual (Hardware Design, Maintenance and Inspection) This manual provides the specifications of the CPU modules, power supply modules, base units, extension cables and others.	SH-080187 (13JR43)
Basic Model QCPU (Q Mode) User's Manual (Function Explanation, Program Fundamentals) This manual explains the functions, programming methods, devices and so on necessary to create programs with the Basic model QCPU (Q mode).	SH-080188 (13JR44)
Relevant Manual	

Manual name	Manual No. (Model code)
QCPU (Q Mode)/QnACPU Programming Manual (Common Instructions) This manual explains how to use the sequence instructions, basic instructions, application instructions and similar instructions. (Sold separately)	SH-080039 (13JF58)
Building Block I/O Module User's Manual This manual provides the specifications and external wiring of I/O modules. (Sold separately)	SH-080042 (13JL99)

High Performance Model QCPU (Q02 (H) CPU, Q06HCPU, Q12HCPU, Q25HCPU)

Detailed Manual

Manual name	Manual No. (Model code)
High Performance Model QCPU (Q Mode) User's Manual (Hardware Design, Maintenance and Inspection) This manual provides the specifications of the CPU modules, power supply modules, base units, extension cables, memory cards and others. (Sold separately)	SH-080037 (13JL97)
High Performance Model QCPU (Q Mode) User's Manual (Function Explanation, Program Fundamentals) This manual explains the functions, programming methods, devices and so on necessary to create programs with the High Performance model QCPU (Q mode). (Sold separately)	SH-080038 (13JL98)

Relevant Manual

Manual name	Manual No. (Model code)
QCPU (Q Mode)/QnACPU Programming Manual (Common Instructions) This manual explains how to use the sequence instructions, basic instructions, application instructions and similar instructions. (Sold separately)	SH-080039 (13JF58)
QCPU (Q Mode)/QnACPU Programming Manual (PID Control) This manual explains the dedicated instructions used to exercise PID control. (Sold separately)	SH-080040 (13JF59)
QCPU (Q Mode)/QnACPU Programming Manual (SFC) This manual explains the system configuration, performance specifications, functions, programming, debugging, error codes and others of MELSAP3. (Sold separately)	SH-080041 (13JF60)
QCPU (Q Mode)/QnACPU Programming Manual (MELSAP-L) This manual explains the programming methods, specifications, functions, and so on that are necessary to create the MELSAP-L type SFC programs. (Sold separately)	SH-080076 (13JF61)
Building Block I/O Module User's Manual This manual provides the specifications and external wiring of I/O modules. (Sold separately)	SH-080042 (13JL99)

Process CPU (Q12PHCPU, Q25PHCPU) Detailed Manual

Manual name	Manual No. (Model code)
Process CPU User's Manual (Hardware Design, Maintenance and Inspection) This manual provides the specifications of the CPU modules, power supply modules, base units, extension cables, memory cards and others. (Sold separately)	SH-080314E (13JR55)
Process CPU User's Manual (Function Explanation, Program Fundamentals) This manual explains the functions, programming methods, devices and so on necessary to create programs with the Process CPU (Sold separately)	SH-080315E (13JR56)
Relevant Manual	
Manual name	Manual No. (Model code)
QCPU (Q Mode)/QnACPU Programming Manual (Common Instructions) This manual explains how to use the sequence instructions, basic instructions and application instructions. (Sold separately)	SH-080039 (13JF58)
QnPHCPU Programming Manual (Process Control Instructions) This manual explains the programming procedures, device names, and other items necessary to implement PID control using process control instructions. (Sold separately)	SH-080316E (13JF67)
QCPU (Q Mode)/QnACPU Programming Manual (SFC) This manual explains the system configuration, performance specifications, functions, programming, debugging, error codes and others of MELSAP3. (Sold separately)	SH-080041 (13JF60)
QCPU (Q Mode)/QnACPU Programming Manual (MELSAP-L) This manual explains the programming methods, specifications, functions, and so on that are necessary to create the MELSAP-L type SFC programs. (Sold separately)	SH-080076 (13JF61)
Building Block I/O Module User's Manual This manual privides the specifications and external wiring of I/O modules. (Sold separately)	SH-080042 (13JL99)

1. Overview

This manual provides the performance specifications, loading and installation, and part names and settings of the MELSEC-Q series CPU modules, the specifications and connection of the I/O modules, and how to read error codes. The CPU type is abbreviated to the QCPU when description is common to the MELSEC-Q series CPU modules.

The usable extension base units and the number of extension bases vary with the CPU module used.

The following table indicates the CPU module types described in this manual and differences between them.

		Extension Base			Number of	Number of
Туре	Main Base	Q5⊡B	Q6□B	QA1S⊡B	Extension Bases	mountable Modules
Q00JCPU	Unnecessary	Usable	Usable	Unusable	2	16
Q00CPU	Nocossan	Usable	Usable	Linucabla	Л	24
Q01CPU	Necessary	*1	*1	Ullusable	4	24
Q02 (H) CPU						
Q06HCPU	Necessary	Usable	Usable	Llooblo *1	7	64
Q12HCPU	necessary	*1	*1	USable I	7	04
Q25HCPU						
Q12PHCPU	Noocoory *2	Llaabla	Llaabla	Linuanhia	7	64
Q25PHCPU	Necessary 2	USable	USable	Unusable	1	04

*1: Extension base unit cannot be connected when the slim type main base unit (Q32SB, Q33SB, Q35SB) is used.

*2: Q12PHCPU and Q25PHCPU are not compatible with the slim type main base unit (Q32SB, Q33SB, Q35SB).

1.1 Included Parts

The following tables list the parts included with the corresponding modules.

- (1) CPU module
 - (a) Q00JCPU

Product Name	Туре	Quantity
CPU module	Q00JCPU	1
Battery	Q6BAT	1
Base unit mounting screw	M4×14 screw	4
This manual		1

(b) Other than Q00JCPU

Product Name	Туре	Quantity
	Q00CPU	
	Q01CPU	
	Q02CPU	
	Q02HCPU	
CPU module	Q06HCPU	1
	Q12HCPU	
	Q25HCPU	
	Q12PHCPU	
	Q25PHCPU	
Battery	Q6BAT	1

(2) Main base unit for Q series modules

Product Name	Туре	Quantity		
	Q33B			
Main bass unit	Q35B	1		
	Q38B	I		
	Q312B			
Base unit mounting screw	M4×14 screw	4		
This manual		1		

(3) Slim type main base unit for Q series module

Product Name	Туре	Quantity		
	Q32SB			
Slim type main base unit	Q33SB	1		
	Q35SB			
Base unit mounting screw	M4×12 screw	4		
This manual		1		

(4) Extension base unit for Q series modules

Product Name	Туре	Quantity			
	Q52B				
	Q55B				
Extension boss unit	Q63B	1			
Extension base unit	Q65B	I			
	Q68B				
	Q612B				
Base unit mounting screw	M4×14 screw	4			

(5) Extension base unit for AnS series modules

(Only Q02CPU, Q02HCPU, Q06HCPU, Q12HCPU or Q25HCPU is usable)

Product Name	Туре	Quantity
Extension base unit	QA1S65B QA1S68B	1
Base unit mounting screw	M5 screw	4

(6) Power supply module

Product Name	Туре	Quantity
	Q61P-A1 Q61P-A2 Q62P Q63P	
Power supply module	Q64P Q61SP	1
	A1S61PN	
	A1S62PN	
	A1S63P	

1.2 Confirming the serial number and function version

The QCPU serial No. and function version can be confirmed on the rating nameplate and GX Developer's system monitor.

(1) Confirmation on the rating nameplate



(2) Confirmation on the system monitor

The QCPU serial No. and function version can be confirmed with Product Information List on the GX Developer's system monitor.* The intelligent function module's serial No. and function version can also

be confirmed.

						Seria	l number	Funct	tion version
roduct	Information	List						×	
Slot	Type	Series	Model name	Points	I/O No.	Control	Serial No	Ver 🔺	
PLC	PLC	Q	QO2HCPU	-	-	_	021120000000000	В	
0-0	Intelli.	Q	QJ71LP21-25	32pt	0000	-	020810000000000	B	
0-1	-	-	None	-	-	-	-	-	
0-2	-	-	None	-	-	-	-	-	
0-3	-	-	None	-	-	-	-	-	
0-4	-	-	None	-	-	-	-	-	
CS	/ file creating						Close		
		_							

* : The version of compatible GX Developer varies depending on the CPU module.

Туре	Version of Compatible GX Developer
Q00JCPU	
Q00CPU	GX Developer Version 7 or later
Q01CPU	
Q02CPU	
Q02HCPU	
Q06HCPU	GX Developer Version 6 or later
Q12HCPU	
Q25HCPU	
Q12PHCPU	CX Developer Version 7 10L or later
Q25PHCPU	GX Developer version 7.10L of later

2. Specifications

2.1 General Specifications

This section provides specifications common to various modules.

Item	Specifications								
Operating ambient temperature	0 to 55° C								
Storage ambient temperature			-25 to 7	75 °C *3					
Operating ambient humidity		5	to 95%RH*4, r	non-condensing	g				
Storage ambient humidity		5	to 95%RH*4, r	non-condensing	g				
			Frequency	Acceleration	Amplitude	Sweep count			
Vibration	Conforming to JIS B 3502, IEC 61131-2	Under intermittent vibration Under continuous vibration	10 to 57Hz		0.075mm (0.003inch)	10 times each in			
resistance			57 to 150Hz	9.8m/s ²		X, Y, Z			
			10 to 57Hz		0.035mm (0.001inch)	directions (for 80			
			57 to 150Hz	4.9m/s ²		minch)			
Shock resistance	Conforming to JIS B 3502, IEC 61131-2 (147 m/s ² , 3 times in each of 3 directions X, Y, Z)								
Operating ambience			No corrosi	ve gases					
Operating altitude	2000m (6562ft.) max.								
Installation position	Inside control panel								
Overvoltage category *1	II max.								
Pollution level *2	2 max.								

*1: This indicates the section of the power supply to which the equipment is assumed to be connected between the public electrical power distribution network and the machinery within premises. Category II applies to equipment for which electrical power is supplied from fixed facilities.

The surge voltage withstand level for up to the rated voltage of 300 V is 2500 V.

- *2: This index indicates the degree to which conductive material is generated in terms of the environment in which the equipment is used. In the environment corresponding to "Pollution level 2", basically only nonconductive pollution occurs, however temporary conductivity may occur due to the occasional condensing.
- *3: The storage ambient temperature is -20 to 75°C if the system includes the AnS series modules.
- *4: The operating ambient humidity and storage ambient humidity are 10 to 90%RH if the system includes the AnS series modules.
- *5: Do not use or store the PLC under pressure higher than the atmospheric pressure of altitude 0m. Doing so can cause a malfunction. When using the PLC under pressure, please contact your sales representative.

2.2 Performance Specifications of CPU Modules

This section provides the performance specifications of the CPU modules. Table 2.2 Q00JCPU, Q00CPU, Q01CPU Performance Specifications

ltore			Domorko			
ILE	em		Q00JCPU	Q00CPU	Q01CPU	Remarks
Control method			Repeated operation using stored program			
I/O control mode			Refresh mode			Direct I/O is available by direct I/O specification (DXI DYI)
Programming lang	juage (Language	Relay symbol la	nguage, logic	symbolic	
dedicated to seque	ence c	ontrol)	lar	nguagee		
Processing speed	L	D X0	0.20µs	0.16µs	0.10µs	
(sequence instruct	tion) N	NOV D0 D1	0.70µs	0.56µs	0.35µs	
Total number of in	struction	ons	249 (except ins intelligent	tructions dedi function modu	cated to ule)	
Constant scan (Fu	Inction	that	1 to	o 2000ms		Parameter setting
uniforms scan time	e)		(can be specifie	ed in 1ms incr	ements)	r arameter setting
Program capacity	*1		8k steps	8k steps	14k steps	
	Progra (Drive	am memory 0)	58kbyte	94ki	oyte	
Memory capacity	Stand (Drive	ard RAM 3)	None	64ki	oyte	
	Stand (Drive	ard ROM 4)	58kbyte	94kbyte		
Number of standa	rd ROI	V write	Max. 100 thousand times			
Number of I/O dev	vice po	ints	2048 points (X/Y0 to 7FF)			Number of points available in programs
Number of occupie	ed I/O	points	256 points (X/Y0 to FF)	1024 points (X/Y0 to 3FF)		Number of points accessible to actual I/O modules
Number of extensi	ion bas	ses	2	4		
Number of loadab	le mod	ules	16	24		<u> </u>
Clock function			Year, month, day, hour, minute, second, day of week (Automatic leap year judgment) Accuracy -3.2 to +5.27s (TYP. +1.98s)/d at 0°C -2.57 to +5.27s (TYP. +2.22s)/d at 25°C -11 68 to +3 65s (TYP2.64s)/d at 55°C			
Allowable momentaly power failure		Within 20ms	depending	on power		
period			(100VAC or more)	supply	module	
5VDC internal current consumption		0.22A *2	0.25A	0.27A		
		Н	98mm (3.86inch)	98mm (3	8.86inch)	
External dimension	ns	W	245mm (9.65inch) *2	27.4mm (1.08inch)	
		D	98mm (3.86inch)	89.3mm (3.52inch)	
Weight			0.66kg *2	0.13	3ka	

*1: The max. number of sequence steps that can be executed is found by the following expression. (Program capacity) - (file header size (default: 34 steps)) Refer to the following manual for details of the program capacity and file. • Basic Model QCPU (Q Mode) User's Manual (Eurotion Explanation, Program Eu

(Function Explanation, Program Fundamentals) *2: These values include those of the CPU module, base unit and power supply module.

Table 2.3 Q02(H)CPU, Q06HCPU, Q12HCPU, Q25HCPU Performance Specifications

		Туре						
Item			Q02	Q02H	Q06H	Q12H	Q25H	Remarks
			CPU	CPU	CPU	CPU	CPU	
Control method			Repeated	operation	on using s	stored pro	ogram	
					Ŭ		Ŭ	Direct I/O is
								available by
I/O contro	ol mode		Refresh r	node				direct I/O
								specification
								(DX□, DY□)
Program	ning language		Relay syr	nbol lang	guage, log	gic symbo	olic	
(Languag	e dedicated to	sequence	language	, MELSA	P3 (SFC), MELSA	\P-L	
control		1	and funct	ion block	ζ.			
Processir	a speed	LD X0	0.079µs		0.03	84µs		
(sequenc	e instruction)	MOV D0 D1	0.237µs		0.10)2µs		
			360 (except in	struction	s dedicat	ed to	
Total num	ber of instruct	ions	i	ntelliaen	t function	module)	04.0	
Constant scan			0.5	to 2000	, ms		Parameter	
(Function	(Function that uniforms scan time)		(can be specified in 0.5ms increments)			nents)	setting	
Program	Drogrom mor			•	COL	1046	2526	
capacity	(Drive 0)		28k step		sten ste	124K	ZOZK	
*2								
	Memory card (RAM)		Capacity of the memory card					
	(Drive 1)		(max. 2Mbyte)					
	Memory card	(ROM)	Capacity of the memory card loaded					
Memory	(Drive 2)	()	(Flash card: max. 4Mbyte,					
capacity			ATA card: max. 32Mbyte)			. *0		
	Standard RA	M (Drive 3)	64kbyte	128kt	byte *5	256Kb	byte *3	
	Standard RO	M (Drive 4)	11	2			1008	
		, , , , , , , , , , , , , , , , , , ,	kbyte kbyte kbyte kbyte			KDyte		
	CPU shared)	okbyle	104	050 *1	
	Program mer		20	5	00	124	252 1	
	Memory card	(RAIVI)			200			
Max.	Nemory card	Flash card			288			
number		ATA caru			512			Only one file
of files								Only one file
stored	Standard RA	M			2			register and local
								device
	Standard RO	М	28	3	60	124	252	
Number	of standard	ROM write						
times			Max. 100 thousand times —					

*1: The CPU module can execute 124 files. It cannot execute 125 or more files.

*2: The maximum number of sequence steps (for one program) for which the parameters are stored in another drive and executed with the CPU module can be calculated with the following expression. (Program size) - (File header size (default : 34 steps))

Refer to the High Performance model QCPU (Q Mode) User's Manual (Function Explanation, Program Fundamentals) for details on the program size and file.

	Туре						
Item	Q02	Q02H	Q06H	Q12H	Q25H	Remarks	
		CPU	CPU	CPU	CPU	CPU	
							Number of points
Number of I/O device point	S		8192 poir	nts (X/Y0	to 1FFF)		available in
							programs
							Number of points
Number of occupied I/O pc	oints		4096 poi	nts (X/Y) to FFF)		accessible to actual
							I/O modules
Number of extension base	S			7			
Number of mountable mod	lules			64			
	Year, month, day, hour, minute, second,						
		day of week (Automatic leap year judgment)					
Clock function		Accuracy					
Clock function		-3.18 to +5.25s (TYP.+2.12s)/d at 0°C					
		-3.93 to +5.25s (TYP.+1.90s)/d at 25°C					
	-14.69 to +3.53s (TYP3.67s)/d at 55°C						
Allowable momentary powe	ər	Depends on power supply module.					
failure period							
5VDC internal current		0.600					
consumption		0.0071		0.0			
	Н	98mm (3.86inch)					
External dimensions	W		27.4r	nm (1.08	inch)		
	D		89.3r	nm (3.52	inch)		
Weight				0.20kg			

*3: The memory capacity of the Q12HCPU or Q25HCPU whose serial No.'s first five digits are "02091" or earlier is 64kbyte. (See to section 1.2 for the way to check the serial No.)

*4: Memory added to the function version B.

The CPU shared memory is not latched.

The CPU shared memory is cleared when the PLC is powered on or the CPU module is reset.

*5: The memory capacity of the Q02HCPU or Q06HCPU whose serial No.'s first five digits are "04011" or earlier is 64kbyte. (See to Section 1.2 for the way to check the serial No.)

Table 2.4 Q12PHCPU, Q25PHCPU Performance Specifications

ltom		Туре		Pomorko	
	Item		Q12PHCPU	Q25PHCPU	Relliaiks
Control me	ethod		Repeated operation u	sing stored program	<u> </u>
I/O control	mode		Refresh mode	Refresh mode	
Programm (Language control)	ing language e dedicated to	e sequence	Relay symbol languag language, MELSAP3 and function block	je, logic symbolic (SFC), MELSAP-L,	
Processing	g speed	LD X0	0.03	4µs	
(sequence	instruction)	MOV D0 D1	0.10	2µs	
Total num	ber of instruc	tions	415 (except instruction 415 (except instruction 415) 415 (except instructi	ctions dedicated to ction module)	
Constant s (Function f	scan that uniforms	scan time)	0.5 to 2 (can be specified in	000 ms 0.5ms increments)	Parameter setting
Program capacity *2	Program memory (Drive 0)		124k step	252k step	
	Memory car (Drive 1)	rd (RAM)	Capacity of moun (max. 2	ited memory card 2Mbyte)	
Memory	Memory cai (Drive 2)	rd (ROM)	Capacity of mounted memory card (Flash card: max. 4Mbyte, ATA card: max. 32Mbyte)		
oupuony	Standard R	AM (Drive 3)	256kbyte		
	Standard R	OM (Drive 4)	496kbyte	1008kbyte	
	CPU shared	d memory*3	8kbyte		
	Program m	emory	124	252 *1	
	Memory car	rd (RAM)	25	56	
Max	Memory	Flash card	28	38	
number	card (ROM)	ATA card	51	12	
stored	Standard RAM		2		Only one file each for file register and local device
	Standard R	OM	124	252	
Number of times	f standard R	OM write	Max. 100 thc	ousand times	

*1: The CPU module can execute 124 files. It cannot execute 125 or more files.

*2: The maximum number of sequence steps (for one program) for which the parameters are stored in another drive and executed with the CPU module can be calculated with the following expression.

(Program size) - (File header size (default : 34 steps))

Refer to the Process CPU User's Manual (Function Explanation, Program Fundamentals) for details on the program size and file.

*3: The CPU shared memory is not latched.

The CPU shared memory is cleared when the PLC is powered on or the CPU module is reset.

ltere		Ту	Domorko	
item		Q12PHCPU	Q25PHCPU	Remarks
Number of I/O device point	S	8192 points ()	Number of points available in programs	
Number of occupied I/O po	ints	4096 points (X/Y0 to FFF)		Number of points accessible to actual I/O modules
Number of extension base	S		7	
Number of mountable mod	lules	6	4	
Clock function		Year, month, day, h day of week (Automat Accuracy -3.18 to +5.25s (TYI -3.93 to +5.25s (TYI -14.69 to +3.53s (TYI		
Allowable momentaly powe failure period	er	Depends on power supply module.		
5VDC internal current consumption		0.64A		
	Н	98mm (3	3.86inch)	
External dimensions	W	27.4mm ((1.08inch)	
	D	89.3mm (3.52inch)		
Weight		0.20kg		

2.3 Power supply module specifications This section provides the specifications of the power supply modules.

Table 2.5 Q61P-A1/A2, Q62P, Q63P Performance Specification List

ltere				Performance Specifications					
	Item		Q61P-A1	Q61P-A2	Q62P	Q63P			
Base mo	Base mounting position		P	ower supply mo	dule mounting sl	ot			
Applicab	le bas	se unit							
1.100.00				100 to 120VAC (- 2	200 to 240VAC	100 to 240VAC	24VDC		
Input power supply		15%/+10%)	(-15%/+10%)	(-15%/+10%)	(-35%/+30%)				
Input power supply		(85V to 132VAC) ((10,0,110,0) 170 to 264VAC)	(85V to 264VAC)	(15.6 to 31.2 VDC)				
Input free	nuenc	:V			50/60Hz $+5%$				
Input vol	tage c	distorti	on factor	Within 5	% (See section	431)			
Max inn	ut anr	arent	nower	Vita in 10	105\/A	1.0.1)			
Max inp	ut nov		power				45\\/		
Inrush ci	irrent	101		2	MA within 8ms		100A within 1ms		
Rated or	itout	5\/DC		<u> </u>		30	6Δ		
	որու	241/0		0A		0.64			
Current		2400							
External	outpu	it volta	ige			(-10%/+10%)			
Overcurr	ont	5\/DC		6 64 or r	more	3 34 or more	6.64 or more		
protectio	om n*1	241/0	, 	0.0/10/1					
Overvolt	200	5VDC	<u> </u>		551				
protectio	ay c n*2	241/0	, C		5.5 t	0.0.0			
Efficience		2400	0	70% or r	moro		70% or more		
	y o mor	nontai	N DOWOR	70/001	nore	05/6 01 11016	Within 10mc		
failuro po	tinol	neniai 2	y power		Within 20ms		(at 24)/DC input)		
Tallule pe	nou .	5		Across inputs/LG a	ad outputs/EG				
Dielectric	c with	stand	voltage	2 220 VAC rms/2 avalas (2 000 m (6562 ft))			primary and 5\/DC		
				Across inputs and	printary and 5VDO				
				across inputs and LG/EG, across outputs and $10M\Omega$ or more 1					
Insulation	n resis	stance	;	FG/LG 10MO or mo	bre by insulation	resistance	insulation		
				tester		resistance	resistance tester		
							By noise simulator		
							of 500Vp-p noise		
				• By noise simulator of 1,500Vp-p noise voltage, voltage, 1μ s noise					
Noise du	rabilit	у		1 μ s noise width and 25 to 60Hz noise frequency width and 25 to					
				• Noise voltage IECo 1000-4-4, 2KV 60Hz noise					
							frequency		
Operatio	n disp	lay			LED display (lit	at 5VDC output)			
Fuse					Built-in (Uncha	ngeable by user)			
	A			ERR contact (conta	act switched off	(opened: normally	v closed contact) at		
	Арріі	cation		an error stop of CF	U module), for	ÈPU module opei	rating status output		
	Rate	d swite	ching						
	volta	ge, cu	rrent	24VDU, U.ƏA					
Contract	Minir	num s	witching	5\/DC 1m/					
Contact	Jontact load								
soction	Response time		time	OFF to ON: 10ms max. ON to OFF: 12ms max.					
3601011	Section		Mechanical: More than 2 million times						
Life		Electrical : More than 100 thousand times at rated switching voltage.							
				current					
	Surge suppressor		oressor	None					
	Fuse			None					
Terminal screw size		M3.5 × 7							
Applicable wire size		0.75 to 2mm ²							
Applicable solderless terminal		RAV1.25-3.5, RAV2-3.5							
Applicable tightening torque		g torque	66 to 89N•cm						
	<u> </u>		Ĥ		98mm ((3.86inch)			
External	000		W	55.2mm (2.17inch)					
uinensio	115		D		90mm ((3.55inch)			
Weight				0.31k	g	0.39kg	0.33kg		

Table 2.6 Q64P Performance Specifications

ltem			Performance Specifications					
	iter	111		Q64P				
Base mo	ounting	g positio	on	Power supply module mounting slot				
Applicab	le bas	e unit		Q3□B, Q6□B				
Input pov	ver su	ipply		100 to 120VAC/200 to 240VAC (-15%/+10%) (85\/ to 132\/AC/170 to 264\/AC)				
Input free	nuenci	v		50/60Hz +5%				
Input vol	tane d	<u>y</u> listartia	n	50/00112 ±570				
factor	lage a			Within 5% (See section 4.3.1)				
Max. inp	ut app	arent p	ower	160VA				
Inrush cu	urrent			20A within 8ms				
Rated ou	utput	5VDC		8.5A				
current		24VDC	;					
Overcurr	ent	5VDC		9.9A or more				
protectio	n*1	24VDC)					
Overvolt	age	5VDC		5.5 to 6.5V				
protectio	n*2	24VDC	;					
Efficienc	у			70% or more				
Allowable power fa	e morr ilure p	nentary eriod*3	3	Within 20ms				
Dielectric	c withs	stand v	oltage	Across inputs/LG and outputs/FG				
				2,830VAC rms/3 cycles (2,000 m (6562 ft.))				
Insulation	n resis	stance		LG/FG, across outputs and FG/LG $10M\Omega$ or more by insulation resistance tester				
Noise du	irability	у		 By noise simulator of 1,500Vp-p noise voltage, 1^µs noise width and 25 to 60Hz noise frequency Noise voltage IEC61000-4-4, 2kV 				
Operatio	n disp	lav		LED display (lit at 5VDC output)				
Fuse				Built-in (Unchangeable by user)				
	Appli	cation		ERR contact (contact switched off (opened: normally closed contact) at an error stop of CPU module), for CPU module operating status output				
	Rateo voltao	d switcl ge, cur	ning rent	24VDC, 0.5A				
Contact	Minin switc	num hina lo:	ad	5VDC, 1mA				
output	Resp	onse ti	me	OFF to ON: 10ms max. ON to OFF: 12ms max.				
Section	Life			Mechanical : More than 2 million times Electrical : More than 100 thousand times at rated switching voltage, current				
Surge suppressor		essor	None					
Fuse			None					
Terminal screw size			M3.5 × 7					
Applicable wire size			0.75 to 2mm ²					
Applicable solderless			RAV1.25-3.5, RAV2-3.5					
Applicable tightening torque		toraue	66 to 89N∙cm					
	9-1		H	98mm (3.86inch)				
Extornal	dimon	sione	11	55 2mm (2.17inch)				
LAIGINA	unel	1310113		$\frac{115}{115}$				
Woight			U					
vveigni				U.4UNY				

Table 2.7 Q61SP Performance/Specifications

				Performance/Specifications		
Item			Q61SP			
Base mo	untina pa	osition		Slim type power supply module mounting slot		
Applicab	e base u	init				
Input pov	ver supp	lv		100 to 240V AC +10% -15% (85 to 264V AC)		
Input free	auencv	.,		50/60Hz +-5%		
Input volt	age dist	ortion	rate	Within 5% (See section 4.3.1)		
Maximun	n input a	pparer	nt	10)//		
power	• •	•		40VA		
Inrush cu	irrent			Within 20A for 8 sec.		
Rated inp	out	5V D	С	2.0A		
current		24V I	DC	-		
Overcurr	ent	5V D	С	2.2A or more		
protection	n *1	24V I	DC	-		
Overvolta	age	5V D	С	5.5 to 6.5V		
protection	n *2	24V I	DC	-		
Efficiency	/			70% or more		
Allowable	e momen eriod	itary p	ower	Within 20ms		
Dielectric	withstar	nd volt	age	Across inputs/LG and outputs/FG		
				2,830V AC crims/3 cycles (2,000m)		
Insulatior	n resistar	nce *3		inputs and LG/FG, across outputs and FG/LG, $10M\Omega$ or more by 500V DC insulation resistance tester		
Noise durability			 By noise simulator of 1500Vp-p noise voltage, 1µs noise width and 25 to 60HZ noise frequency 			
				Noise voltage IEC61000-4-4, 2kV		
Operation	n display			LED display (lit at 5V DC output)		
Fuse				Built-in (Unchangeable by user)		
	Applica	tion		ERR contact (contact switched off (opened: B-contact) at error stop of CPU module) for CPU module operating status output		
	Rated s voltage	switchi , curre	ng ent	24V DC, 0.5A		
Contact	Minimu	Ainimum switching		5V DC, 1mA		
output	Respon	nse tim	ne	OFF to ON: 10ms max. ON to OFF: 12ms max.		
				Mechanic: More than 2 million times		
Life			Electrical: More than 100 thousand times at rated switching voltage, current			
Surge suppresser		sser	None			
Fuse			None			
Terminal screw size			M3.5×7			
Applicable wire size			0.75 to 2mm ²			
Applicable solderless terminal		rminal	RAV1.25-3.5, RAV2-3.5			
Applicable tightening torque		rque	66 to 89N•cm			
			Н	98mm (3.86inch)		
External of	dimensio	ns	W	55.2mm (2.17inch)		
			D	104mm (4.09inch)		
Weight			0.18kg			

Table 2.8 Q00JCPU (Power Supply Section) Performance Specifications

ltom		Performance Specifications			
I	tem	Q00JCPU (power supply section)			
lenut neuron europhi		100 to 240VAC (-15%/+10%)			
input power st	yppiy	(85 to 264VAC)			
Input frequence	у	50/60Hz ±5%			
Input voltage of	distortion factor	Within 5% (See section 4.3.1)			
Max. input app	parent power	105VA			
Inrush current		40A within 8ms			
Rated output	DC5V	3A			
current	DC24V				
Overcurrent	DC5V	3.3A or more			
protection*1	DC24V				
Overvoltage	DC5V	5.5 to 6.5V			
protection*2	DC24V				
Efficiency		65% or more			
Allowable insta	antaneous	Within 20ms $(100)/AC$ or more)			
power failure t	ime*3				
Dielectric with	stand voltage	Across inputs/LG and outputs/FG			
		2,830VAC rms/3 cycles (altitude 2,000 m (6,562 ft.))			
		Across inputs and outputs (LG and FG separated),			
Insulation resi	stance	across inputs and LG/FG, across outputs and FG/LG			
		10MΩ or more by 500VDC insulation resistance tester			
		• By noise simulator of 1,500Vp-p noise voltage, 1µs noise width			
Noise durabilit	У	and 25 to 60Hz noise frequency			
0		Noise voltage IEC61000-4-4, 2kV			
Operation disp	olay	LED display (lit at 5VDC output)			
Fuse		Built-in (Unchangeable by user)			
Contact outpu	t section	None			
Terminal screv	N SİZE	M3.5×7			
Applicable wire size		0.75 to 2mm ²			
Applicable sol	derless terminal	RAV1.25-3.5, RAV2-3.5			
Applicable tigh	ntening torque	66 to 89N•cm			
External dimer	nsions	Refer to Table 2.2			
Weight					

Table 2.9 A1S61PN, A1S62PN, A1S63P Performance Specifications

Item A1S61PN A1S62PN A1S63P Base mounting position Power supply module mounting slot Applicable base unit QA1S65B, QA1S68B Input power supply 100 to 240VAC (-15%/+10%) (85 to 264VAC) 24VDC (-35%/+30%) (15.6 to 31.2VDC) Input frequency 50/60Hz ±5% — Input voltage distortion factor Within 5% (See section 4.3.1) — Max. input apparent power — 41W Inrush current 20A within 8ms 81A within 1ms Rated output SVDC 5A 3A Overcurent SVDC 5.5A or more 3.3A or more Overcurent SVDC 5.5A or more — Overcurent SVDC 5.5K or more 3.4 or more Protection*2 24VDC — _ Overcurent SVDC 5.5K or more _ _ Protection*2 24VDC — _ _ Bilectric withstand voltage Across inputs/LG and outputs/FG _ _ _ Dielectric withstand voltage Across inputs and LG/FG, across
Base mounting position Power supply module mounting slot Applicable base unit QA1S65B, QA1S68B Input power supply 100 to 240VAC (-15%/+10%) (85 to 264VAC) 24VDC (-35%/+30%) (15.6 to 31.2VDC) Input frequency 50/60Hz ±5% — Input voltage distortion factor Within 5% (See section 4.3.1) — Max. input apparent power 105VA — Max. input apparent power 20A within 8ms 81A within 1ms Rated output SVDC 5A 3A Current 24VDC — — Overcurrent 5VDC 5.5A or more 3.3A or more Overcurrent 5VDC 5.5A or more 3.3A or more Overcurrent 5VDC 5.5A or more . Overcurrent 5VDC 5.5 to 6.5V — protection*1 24VDC — . Efficiency 65% or more . . Allowable momentary prover failure period*3 Within 20ms fat 24VDC input) . Noise durability Noise sinputs and outputs (LG and FG 2,830VAC ms/3 cycles (
Applicable base unit QA1\$665B, QA1\$68B Input power supply 100 to 240VAC (-15%/+10%) (85 to 264VAC) 24VDC (-35%/+30%) (15.6 to 31.2VDC) Input frequency 50/60Hz ±5% Input voltage distortion factor Within 5% (See section 4.3.1) Max. input apparent power 41W Inrush current 20A within 8ms 81A within 1ms Rated output 5VDC 5A 3A Current 5VDC 5A 3A External output voltage 24VDC (-10%/+10%) Overcurrent 5VDC 5.5A or more 3.3A or more Overcurrent 5VDC 5.5A or more 3.3A or more Overvoltage 5VDC 5.5A or more 5.5V Protection*1 24VDC 24VDC (-10%/+10%) Efficiency 65% or more 500VAC across 0180VAC ms/3 cycles (2,000 m (6562 ft.)) primary and 5VDC Insulation resistance 2,830VAC ms/3 cycles (2,000 m (6562 ft.)) primary and 5VDC 5MQ or more by insulation resistance tester Noise durability •Nise simulator of 1,500Vp-p noi
Input power supply 100 to 240VAC (-15%/+10%) (85 to 264VAC) 24VDC (-35%/+30%) (15 et o 31.2VDC) Input frequency 50/60Hz ±5% Input voltage distortion factor Within 5% (See section 4.3.1) Max. input apparent power 41W Inrush current 20A within 8ms 81A within 1ms Rated output 5VDC 5A 3A Current 24VDC 41W Covercurrent 5VDC 5A 3A Overcurrent 5VDC 5.5A or more 5.5A or more Overcurrent 5VDC 5.5A or more 5.5A or more Overvoltage 5VDC 5.5A or more 5.5A or more Overcurrent 5VDC 5.5A or more 5.50 or more Allowable momentary protection*1 24VDC Allowable momentary power failure period*3 Across inputs/LG and outputs/FG 500VAC across outputs and FGLAC 10MX or more by insulation resistance 5MQ or more by insulation resistance tester SM or more by insulation resistance tester Noise durability • By noise simulator of 1,500Vp
Input power supply 100 to 240/VAC (*15%)*10%) (*35%/+30%/+30%) (*35%/+30%) (*35%/+30%/+30%) (*35%/+30%/+30%) (*35%/+30%/+30%) (*35%/+30%/+30%) (*35%/+30%/+30%) (*35%/+30%/+30%) (*35%/+30%/+30%) (*35%/+30%/+30%) (*35%/+30%/+30%) (*35%/+30%/+30%) (*35%/+30%/+30%) (*35%/+30%/+30%/+30%/+30%/+30%) (*35%/+30%/+30%/+30%/+30%/+30%/+30%) (*35%/+30%/+30%/+30%/+30%/+30%/+30%/+30%/+30
Input frequency (15.6 to 31.2VDC) Input voltage distortion factor Within 5% (See section 4.3.1) Max. input apparent power 105VA Max. input apparent power Max. input apparent power Inrush current 20A within 8ms 81A within 1ms Rated output 5VDC 5A 3A Current 24VDC External output voltage 24VDC (-10%/+10%) Overcurrent 5VDC 5.5A or more 3.3A or more 5.5A or more Protection*1 24VDC 0.66A or more Overcurrent 5VDC 5.5A or more 3.3A or more 5.5A or more Protection*2 24VDC Efficiency Allowable momentary Within 20ms (at 24VDC input) 500VAc across primary and 5VDC Insulation resistance Separated), across inputs and LG/FG, across 5MQ or more by insulation resistance tester 5MQ or more by insulation resistance 5MQ or more by insulation resistance Noise durability • By noise simulator of 1,500Vp-p noise voltage, 1, µs noi
Input frequency 50/60Hz ±5% —— Input voltage distortion factor Within 5% (See section 4.3.1) —— Max. input apparent power 105VA —— Max. input power —— 41W Inrush current 20A within 8ms 81A within 1ms Rated output 5VDC 5A 3A 5A current 24VDC — 0.6A — Protection*1 24VDC — 0.6A — Overcurrent 5VDC 5.5A or more 3.3A or more 5.5A or more Overcurrent 24VDC — 0.66A or more — Overcurrent 24VDC — — 0.66A or more Protection*1 24VDC — — 0.66A or more — Overvoltage 5VDC 5.5A or more 3.3A or more 5.6A or more _ Insulation resitance Across inputs/LG and outputs/FG 500VAC across f(at 24VDC input) f(at 24VDC input) _ Insulation resistance Across inputs/LG and VD or more by in
Input voltage distortion factor Within 5% (See section 4.3.1) Max. input apparent power 105VA Max. input power Max. input power Max. input power Max. input power Rated output 5VDC 5A 3A Current 24VDC 0.6A
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Applicable tightening torque 66 to 89N•cm
H 130mm (5.12inch)
External W 55mm (2.17inch)
D 93.6 (3.69inch)
Weight 0.60kg 0.50kg

POINT

*1 : Overcurrent protection

The overcurrent protection device shuts off the 5 V, 24 VDC circuit and stops the system if the current flowing in the circuit exceeds the specified value. If this device is activated, switch the input power supply off and eliminate the cause such as insufficient current capacity or short. Then, a few minutes later, switch it on to restart the system.

The initial start for the system takes place when the current value becomes normal.

*2 : Overvoltage protection

The overvoltage protection device shuts off the 5 VDC circuit and stops the system if a voltage of 5.5 to 6.5 VDC is applied to the circuit. When this device is activated, the power supply module LED is switched OFF. If this happens, switch the input power OFF, then a few minutes later ON. This causes the initial start for the system to take place. The power supply module must be changed if the system is not booted and the LED remains OFF.

- *3 : Allowable momentary power failure period
 - (1) For AC input power supply
 - An instantaneous power failure lasting less than 20ms will cause AC down to be detected, but operation will continue.
 - An instantaneous power failure lasting more than 20ms may cause the operation to continue or initial start to take place depending on the power supply load.

Furthermore, by using the same AC power supply for the AC input module and the power supply module, it is possible to prevent the sensor, to which the AC input module is connected, from turning OFF when it is ON when the power is turned OFF.

However, if only the AC input module is connected to the AC line, which is connected to the power suppludetection of the AC down for the power supply module may be delayed by the capacitor in the AC input module. Thus, connect a load of approx. 30mA per QX10 unit to the AC line.

- (2) For DC input power supply
 - An instantaneous power failure lasting less than 10ms (*4) will cause 24VDC down to be detected, but operation will continue.
 - An instantaneous power failure lasting more than 10ms (*4) may cause the operation to continue or initial start to take place depending on the power supply load.
 - (*4: This is for a 24VDC input. This is 10ms or less for 24VDC or less.)

2.4 Specifications of Base Units (1) Main base units

Туре	Item	Q00JCPU	Q33B	Q35B	Q38B	Q312B	
Number of mountable I/O modules		2	3	5	8	12	
Applicable mod	lules		Q	series module	s		
5VDC internal Current consumption		0.220A	0.105A	0.110A	0.114A	0.121A	
Weight		0.66kg	0.21kg	0.25kg	0.35kg	0.45kg	
	Н	98mm (3.86inch)					
External	W	245mm (9.65inch)	189mm (7.44inch)	245mm (9.65inch)	328mm (12.92inch)	439mm (17.30inch)	
aimensions	D	97mm (3.82inch)	44.1mm (1.74inch)				

(2) Slim type main base units

Type Item		Q32SB	Q33SB	Q35SB			
Number of mountable I/O modules		2 3		5			
Applicable modules		Q series modules					
5VDC internal Current consumption		0.086A 0.086A		0.091A			
Weight		0.12kg 0.15kg		0.21kg			
Extornal	Н		98mm (3.86inch)				
dimensions	W	114mm (4.49inch)	142mm (5.59inch)	197.5mm (7.78inch)			
	D						

(3) Extension base units

Туре	Item	Q52B	Q55B	Q63B	Q65B	Q68B	Q612B	
Number of mountable I/O modules		2	5	3	5	8	12	
Applicable mod	dules		Q series modules					
5VDC internal Current consumption		0.080A	0.100A	0.105A	0.110A	0.114A	0.121A	
Weight		0.14kg	0.23kg	0.23kg	0.25kg	0.35kg	0.45kg	
	Н	98mm (3.86inch)						
External	۱۸/	106mm	189mm	189mm	245mm	328mm	439mm	
dimensions	vv	(4.17inch)	(7.44inch)	(7.44inch)	(9.65inch)	(12.92inch)	(17.30inch)	
	D			44.1mm (1.74inch)			

Туре	Item	QA1S65B	QA1S68B	
Number of mountable I/O modules		5	8	
Applicable mod	dules	AnS series modules		
5VDC internal current consumption		0.117A	0.118A	
Weight		0.75kg	1.00kg	
External dimensions	Н	130mm (5.12inch)	
	W	315mm (12.41inch)	420mm (16.55inch)	
	D	51.2mm (2.02inch)	

3. EMC and Low Voltage Directive

For the products sold in European countries, the conformance to the EMC Directive, which is one of the European Directives, has been a legal obligation since 1996. Also, conformance to the Low Voltage Directive, another European Directives, has been a legal obligation since 1997.

Manufacturers who recognize their products must conform to the EMC and Low Voltage Directive are required to declare that their products conform to these Directives and put a "CE mark" on their products.

3.1 Requirements for conformance to EMC Directive

The EMC Directive specifies that products placed on the market must "be so constructed that they do not cause excessive electromagnetic interference (emissions) and are not unduly affected by electromagnetic interference (immunity) ". The applicable products are requested to meet these requirements. The sections 3.1.1 through 3.1.6 summarize the precautions on conformance to the EMC Directive of the machinery constructed using the MELSEC-Q series PLCs.

The details of these precautions has been prepared based on the control requirements and the applicable standards . However, we will not assure that the overall machinery manufactured according to these details conforms to the above-mentioned directives. The method of conformance to the EMC Directive and the judgment on whether or not the machinery conforms to the EMC Directive must be determined finally by the manufacturer of the machinery.

3.1.1 Standards applicable to the EMC Directive

Specification Test item Test details Standard value 30M-230MHz QP : 30dB ^{*µ*} V/m Electromagnetic (30 m in measurement range) *1 emissions from the EN55011 230M-1000MHz QP : 37 dB μ Radiated noise *2 product are V/m EN50081-2: measured. (30 m in measurement range) 1995 150k-500kHz QP: 79 dB, Electromagnetic EN55011 emissions from the Mean: 66 dB *1 Conducted noise product to the power 500k-30MHz QP: 73 dB, line is measured. Mean: 60 dB Immunity test in which static EN61000-4-2 Electrostatic electricity is applied 15kV Aerial discharge to the cabinet of the immunity *2 equipment. Immunity test in Power line: 2kV EN61000-4-4 Digital I/O (24V or higher): 1kV which burst noise is Fast transient burst applied to the power (Digital I/O (24V or less)) > 250V noise *2 EN61131-2: line and signal lines. (Analog I/O, signal lines) > 250V 1996 Immunity test in EN61000-4-3 which field is 10V/m, 26-1000MHz, Radiated field AM irradiated to the 80%AM modulation@1kHz modulation *2 product. Immunity test in EN61000-4-12 which a damped Power line: 1kV Damped oscillatory oscillatory wave is Digital I/O (24V or higher): 1kV wave immunity superimposed on the power line.

The standards applicable to the EMC Directive are listed below.

*1: QP : Quasi-peak value, Mean : Average value

*2: The PLC is an open type device (device installed to another device) and must be installed in a conductive control panel.

The tests for the corresponding items were performed while the PLC was installed inside a control panel.

3.1.2 Installation instructions for EMC Directive

The PLC is open equipment and must be installed within a control cabinet for use. This not only ensures safety but also ensures effective shielding of PLC-generated electromagnetic noise.

- (1) Control cabinet
 - (a) Use a conductive control cabinet.
 - (b) When attaching the control cabinet's top plate or base plate, mask painting and weld so that good surface contact can be made between the cabinet and plate.
 - (c) To ensure good electrical contact with the control cabinet, mask the paint on the installation bolts of the inner plate in the control cabinet so that contact between surfaces can be ensured over the widest possible area.
 - (d) Earth the control cabinet with a thick wire so that a low impedance connection to ground can be ensured even at high frequencies.
 - (e) Holes made in the control cabinet must be 10 cm (3.94 inch) diameter or less. If the holes are 10 cm (3.94 inch) or larger, radio frequency noise may be emitted.

In addition, because radio waves leak through a clearance between the control panel door and the main unit, reduce the clearance as much as practicable.

The leakage of radio waves can be suppressed by the direct application of an EMI gasket on the paint surface.

Our tests have been carried out on a panel having the damping characteristics of 37 dB max. and 30 dB mean (measured by 3 m method with 30 to 300MHz).

(2) Connection of power and earth wires

Earthing and power supply wires for the PLC system must be connected as described below.

- (a) Provide an earthing point near the power supply module. Earth the power supply's LG and FG terminals (LG : Line Ground, FG : Frame Ground) with the thickest and shortest wire possible. (The wire length must be 30 cm (11.18 inch) or shorter.) The LG and FG terminals function is to pass the noise generated in the PLC system to the ground, so an impedance that is as low as possible must be ensured. As the wires are used to relieve the noise, the wire itself carries a large noise content and thus short wiring means that the wire is prevented from acting as an antenna.
- (b) The earth wire led from the earthing point must be twisted with the power supply wires. By twisting with the earthing wire, noise flowing from the power supply wires can be relieved to the earthing. However, if a filter is installed on the power supply wires, the wires and the earthing wire may not need to be twisted.

3.1.3 Cables

The cables extracted from the control panel contain a high frequency noise component. On the outside of the control panel, therefore, they serve as antennas to emit noise. To prevent noise emission, use shielded cable for the cables which are connected to the I/O units and intelligent function units and may be extracted to the outside of the control panel.

The use of a shielded cable also increases noise resistance. The signal lines connected to the PLC I/O units and intelligent function units use shielded cables to assure noise resistance under the conditions where the shield is earthed. If a shielded cable is not used or not earthed correctly, the noise resistance will not meet the specified requirements.

When the shield of a shielded cable is earthed to the cabinet body, please ensure that the shield contact with the body is over a large surface area. If the cabinet body is painted it will be necessary to remove paint from the contact area. All fastenings must be metallic and the shield and earthing contact must be made over the largest available surface area. If the contact surfaces are too uneven for optimal contact to be made either use washers to correct for surface inconsistencies or use an abrasive to level the surfaces. The following diagrams show examples of how to provide good surface contact of shield earthing by use of a cable clamp.

- (1) Earthing of shielded of shield cable
 - (a) Earth the shield of the shielded cable as near the unit as possible taking care so that the earthed cables are not induced electromagnetically by the cable to be earthed.
 - (b) Take appropriate measures so that the shield section of the shielded cable from which the outer cover was partly removed for exposure is earthed to the control panel on an increased contact surface. A clamp may also be used as shown in the figure below. In this case, however, apply a cover to the painted inner wall surface of the control panel which comes in contact with the clamp.



Note) The method of earthing by soldering a wire onto the shield section of the shielded cable as shown below is not recommended. The high frequency impedance will increase and the shield will be ineffective.



(2) MELSECNET/H module

Always use a double-shielded coaxial cable (MITSUBISHI CABLE INDUSTRIES, LED.: 5C-2V-CCY) for the coaxial cables MELSECNET/H module. Radiated noise in the range of 30MHz or higher can be suppressed by use of the double-shielded coaxial cables. Earth the double-shielded coaxial cable by connecting its outer shield to the ground.



See to 1) for the earthing of the shield.

- (3) Ethernet module, FL-net module, Web server module Precautions for using AUI cables, twisted pair cables and coaxial cables are described below.
 - (a) Always earth the AUI cables connected to the 10BASE5 connectors. Because the AUI cable is of the shielded type, strip part of the outer cover and earth the exposed shield section to the ground on the widest contact surface as shown below.



See to 1) for the earthing of the shield.

(b) Use shielded twisted pair cables as the twisted pair cables connected to the 10BASE-T/100BASE-TX connectors. For the shielded twisted pair cables, strip part of the outer cover and earth the exposed shield section to the ground on the widest contact surface as shown below.



See to 1) for the earthing of the shield.

(c) Always use double-shielded coaxial cables as the coaxial cables connected to the 10BASE2 connectors. Earth the double-shielded coaxial cable by connecting its outer shield to the ground.



See to 1) for the earthing of the shield.

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(4) I/O signal lines and other communication cables
 For the I/O signal lines and other communication lines (RS-232, RS-422, CC-Link, etc.), if extracted to the outside of the control panel, also ensure to earth the shield section of these lines and cables in the same manner as in item (1) above.

3.1.4 Power supply module

Always ground the LG and FG terminals after short-circuiting them.

3.1.5 When using QA1S6 B type base unit

- (1) Cable
 - (a) Earthing of shielded cables
 - Earth the shield of the shielded cable as near the unit as possible taking care so that the earthed cables are not induced electromagnetically by the cable to be earthed.
 - Take appropriate measures so the shield that was partly removed for exposure is earthed to the control panel over a large contact surface area. A clamp may also be used as shown in the figure below. In this case, however, apply a cover to the painted inner wall surface of the control panel which comes in contact with the clamp.



Note) The method of earthing by soldering a wire onto the shield section of the shielded cable as shown below is not recommended. The high frequency impedance will increase and the shield will be ineffective.



(b) Positioning modules

Precautions to be followed when the machinery conforming to the EMC Directive is configured using the A1SD75P1-S3/A1SD75P2-S3/A1SD75P3-S3 (hereafter referred to as the A1SD75) are described below.

- 1) When wiring with a 2 m (6.56 ft.) or less cable
 - Ground the shield section of the external wiring cable with the cable clamp. (Ground the shield at the closest location to the A1SD75 external wiring connector.)
 - Wire the external wiring cable to the drive unit and external device with the shortest practicable length of cable.
 - Install the drive unit in the same panel.



- 2) When wiring with cable that exceeds 2 m (6.56 ft.), but is 10 m (32.79 ft.) or less
 - Ground the shield section of the external wiring cable with the cable clamp. (Ground the shield at the closest location to the A1SD75 external wiring connector.)
 - Install a ferrite core.
 - Wire the external wiring cable to the drive unit and external device with the shortest practicable length of cable.



- 3) Ferrite core and cable clamp types and required quantities
 - Cable clamp
 - Type : AD75CK (Mitsubishi Electric)
 - Ferrite core
 - Type : ZCAT3035-1330 (TDK ferrite core)
 - Required quantity

Cable length	Prepared part	Required Qty		
		1 axis	2 axis	3 axis
Within 2 m (6.56 ft.)	AD75CK	1	1	1
2 m (6.56 ft.) to 10m (32.79 ft.)	AD75CK	1	1	1
	ZCAT3035-1330	1	2	3
(c) CC-Link module

 Be sure to ground the cable shield that is connected to the CC-Link module close to the exit of control panel or to any of the CC-Link stations within 30 cm (11.8 inch) from the module or stations. The CC-Link dedicated cable is a shielded cable. As shown in the illustration below, remove a portion of the outer covering and ground as large a surface area of the exposed shield part as possible.



- 2) Always use the specified CC-Link dedicated cable.
- 3) Do not use a ferrite core for the CC-Link module or CC-Link stations.
- 4) The CC-Link module, the CC-Link stations and the FG line inside the control panel should be connected at both the FG terminal and the SLD terminal as shown in the diagram below.

[Simplified diagram]



(d) I/O signal lines and other communication cables

For the I/O signal lines extracted to the outside of the control panel, also ensure to earth the shield section of these lines and cables in the same manner as in item (1) above.

(2) Power supply module

The precautions required for each power supply module are described below. Always observe the items noted as precautions.

Model	Precautions
A1S61P, A1S62P	Not usable
A1S63P*1	Use the 24VDC panel power equipment conforming to the CE Directives.
A1S61PEU, A1S62PEU, A1S61PN, A1S62PN	Always ground the LG and FG terminals after short- circuiting them.

*1: If sufficient filter circuitry is built into the 24 VDC external power supply module, the noise generated by A1S63P will be absorbed by that filter circuit, so a line filter may not be required.

Filtering circuitry of version F or later of A1S63P is improved so that a external line filter is not required.

3.1.6 Others

(1) Ferrite core

A ferrite core has the effect of reducing radiated noise in the 30MHz to 100MHz band.

It is not required to fit ferrite cores to cables, but it is recommended to fit ferrite cores if shield cables pulled out of the enclosure do not provide sufficient shielding effects.

It should be noted that the ferrite cores should be fitted to the cables in the position immediately before they are pulled out of the enclosure. If the fitting position is improper, the ferrite will not produce any effect. In the CC-Link system, however, ferrite cores cannot be fitted to cables.

(2) Noise filter (power supply line filter)

A noise filter is a component which has an effect on conducted noise. It is not required to fit the noise filter to the power supply line, but fitting it can further suppress noise.

(The noise filter has the effect of reducing conducted noise of 10MHz or less.)

The precautions required when installing a noise filter are described below.

1) Do not bundle the wires on the input side and output side of the noise filter. When they are bundled, the output side noise will induct into the input side wires.



when the input and output wires are bundled.



Separate the input and output wires.

2) Earth the noise filter earthing terminal to the control cabinet with the short wire as possible (less than approx. 10 cm (3.94 inch)).

Noise Filter Model Name	FN343-3/01	FN660-6/06	ZHC2203-11
Manufacturer	SCHAFFNER	SCHAFFNER	TDK
Rated current	3 A	6 A	3 A
Rated voltage		250 V	

Reference

3.2 Requirement to Conform to the Low Voltage Directive

The Low Voltage Directive requires each device that operates with the power supply ranging from 50 to 1000VAC and 75 to 1500VDC to satisfy the safety requirements.

In Sections 3.2.1 to 3.2.6, cautions on installation and wiring of the MELSEC-Q series PLC to conform to the Low Voltage Directive are described.

We have put the maximum effort to develop this material based on the requirements and standards of the regulation that we have collected. However, compatibility of the devices which are fabricated according to the contents of this manual to the above regulation is not guaranteed. Each manufacturer who fabricates such device should make the final judgement about the application method of the Low Voltage Directive and the product compatibility.

3.2.1 Standard applied for MELSEC-Q series PLC

The standard applied for MELSEC-Q series PLC is EN61010-1 safety of devices used in measurement rooms, control rooms, or laboratories.

The MELSEC-Q series modules which operate at the rated voltage of 50VAC/75VDC or above are also developed to conform to the above standard. The modules which operate at the rated voltage of less than 50VAC/75VDC are out of the Low Voltage Directive application range.

3.2.2 PLC selection

(1) Power supply module

There are dangerous voltages (voltages higher than 42.4V peak) inside the power supply modules of the 100/200VAC rated I/O voltages. Therefore, the CE marked models are enhanced in insulation internally between the primary and secondary.

(2) I/O module

There are dangerous voltages in the I/O modules of the 100/200VAC rated I/O voltages. Therefore, the CE marked models are enhanced in insulation internally between the primary and secondary.

The I/O modules of 24VDC or less rating are out of Low Voltage Directive application range.

- (3) CPU module, memory card, base unit Using 5VDC circuits inside, the above modules are out of the Low Voltage Directive application range.
- (4) Intelligent function modules (special function modules)

The intelligent function modules (special function modules) such as the analog, network and positioning modules are 24VDC or less in rated voltage and are therefore out of the Low Voltage Directive application range.

(5) Display device

Use the CE marked display device.

3.2.3 Power supply

The insulation specification of the power supply module was designed assuming installation category II. Be sure to use the installation category II power supply to the PLC. The installation category indicates the durability level against surge voltage generated by a thunderbolt. Category I has the lowest durability; category IV has the highest durability.



Category II indicates a power supply whose voltage has been reduced by two or more levels of isolating transformers from the public power distribution.

3.2.4 Control panel

Because the PLC is an open device (a device designed to be stored within another module), be sure to use it after storing in the control panel.

(1) Electrical shock prevention

In order to prevent persons who are not familiar with the electric facility such as the operators from electric shocks, the control panel must have the following functions :

- (a) The control panel must be equipped with a lock so that only the personnel who has studied about the electric facility and have enough knowledge can open it.
- (b) The control panel must have a structure which automatically stops the power supply when the panel is opened.
- (2) Dustproof and waterproof features

The control panel also has the dustproof and waterproof functions. Insufficient dustproof and waterproof features lower the insulation withstand voltage, resulting in insulation destruction. The insulation in our PLC is designed to cope with the pollution level 2, so use in an environment with pollustion level 2 or below. Pollution level 1: An environment where the air is dry and conductive dust

Pollution level 1: An environment where the air is dry and conductive dust does not exist.

- Pollution level 2: An environment where conductive dust does not usually exist, but occasional temporary conductivity occurs due to the accumulated dust. Generally, this is the level for inside the control panel equivalent to IP54 in a control room or on the floor of a typical factory.
- Pollution level 3: An environment where conductive dust exits and conductivity may be generated due to the accumulated dust. An environment for a typical factory floor.
- Pollution level 4: Continuous conductivity may occur due to rain, snow, etc. An outdoor environment.

As shown above, the PLC can realize the pollution level 2 when stored in a control panel equivalent to IP54.

3.2.5 Grounding

There are the following two different grounding terminals. Use either grounding terminal in an earthed status.

Protective grounding (): Maintains the safety of the PLC and improves the noise resistance.

Functional grounding \bigcirc : Improves the noise resistance.

3.2.6 External wiring

(1) 24 VDC external power supply

For the MELSEC-Q series 24VDC I/O modules and the intelligent function modules (special function modules) which require external supply power, (2) External devices

- When a device with a hazardous voltage circuit is externally connected to the PLC, use a model whose circuit section of the interface to the PLC is intensively insulated from the hazardous voltage circuit.
- (3) Intensive insulation

Intensive insulation refers to the insulation with the dielectric withstand voltage shown in Table 1.

Table 1 :Intensive Insulation Withstand Voltage (Installation Category II, source : IEC664)

Rated voltage of hazardous voltage area	Surge withstand voltage (1.2/50 μ s)
150 VAC or below	2500 V
300 VAC or below	4000 V

4. Mounting and Installation

4.1 Module Installation

4.1.1 Handling instructions

CAUTION	 Use the PLC in an environment that meets the general specifications contained in this manual. Using this PLC in an environment outside the range of the general specifications could result in electric shock, fire, erroneous operation, and damage to or deterioration of the product. Hold down the module mounting lever at the module bottom, and securely insert the module fixing latch into the fixing hole in the base unit. Incorrect mounting of the module can cause a malfunction, failure or drop. When using the PLC in the environment of much vibration, tighten the module with a screw. Tighten the screw in the specified torque range. Undertightening can cause a drop, short circuit or malfunction. Overtightening can cause a drop, short circuit or malfunction due to damage to the screw or module. When installing extension cables, be sure that the connectors of base unit are installed correctly. After installation, check them for looseness. Poor connections could cause an input or output failure. Securely mount the memory card by pushing it into the memory card mounting slot. Then, confirm that it has been securely mounted. Not doing so cause a malfunction due to a contact fault. Completely turn off the external power supply before mounting or removing the module. Not doing so could result in electric shock or damage to the product. Note that online module change can be made when the QnPHCPU is used. Note that there are restrictions on the modules that can be changed online and each module has a predetermined changing procedure. For details, refer to the section of online module change in the Process CPU User's Manual (Hardware Design, Maintenance and Inspection). Do not directly touch the module's conductive parts or electronic components. Touching the conductive parts could cause an operation failure or give damage to the
	module.

This section provides instructions for handling the CPU, I/O, intelligent function and power supply modules, base units and so on.

- (1) Module enclosure, memory card, terminal block connectors and pin connectors are made of resin; do not drop them or subject them to strong impact.
- (2) Do not remove modules' printed circuit boards from the enclosure in order to avoid changes in operation.
- (3) Tighten the module mounting and fixing screws as specified below.

Screw	Tightening Torque Range
Module fixing screw (M3 $ imes$ 12 screw)	36 to 48 N·cm
I/O module terminal block screw (M3 screw)	42 to 58 N·cm
I/O module terminal block mounting screw (M3.5 screw)	66 to 89 N·cm
Power supply module terminal screw (M3.5 screw)	66 to 89 N·cm

- (4) Make sure to install the power supply module on the extension base units Q6□B, QA1S6□B. Even if the power supply module is not installed, when the I/O modules and intelligent function module installed on the base units are of light load type, the modules may be operated. In the case, because a voltage becomes unstable, we cannot guarantee the operation.
- (5) When using an extension cable, do not bundle it with nor run it close to the main circuit (high voltage, large current) cables.

4.1.2 Instructions for mounting the base unit

When mounting the PLC to an enclosure or the like, fully consider its operability, maintainability and environmental resistance.

- (1) Mounting dimensions
 - Mounting dimensions of each base unit are as follows:
 - (a) Main base unit (Q3□B), extension base unit (Q5□B, Q6□B, QA1S6□B)



$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\overline{\ }$	Q00J CPU	Q33B	Q35B	Q38B	Q312B	Q52B	Q55B	Q63B	Q65B	Q68B	Q612B	QA1S 65B	QA1S 68B
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	W	245	189	245	328	439	106	189	189	245	328	439	315	420
Ws 224.5 169 224.5 308 419 83.5 167 167 222.5 306 417 295 400 (8.85) (6.61) (8.85) (12.14) (16.51) (3.29) (6.57) (6.57) (8.77) (12.06) (16.43) (11.62) (15.76) WL	••	(9.65)	(7.44)	(9.65)	(12.92)	(17.30)	(4.17)	(7.44)	(7.44)	(9.65)	(12.92)	(17.30)	(12.41)	(16.55)
WS (8.85) (6.61) (8.85) (12.14) (16.51) (3.29) (6.57) (6.57) (8.77) (12.06) (16.43) (11.62) (15.76) WL 15.5 0.61) 10 0.39) 10 0.39) H 98 3.86) 130 (5.12) Hs 80 (3.15) 110 (4.33)	Wo	224.5	169	224.5	308	419	83.5	167	167	222.5	306	417	295	400
WL 15.5 (0.61) 10 (0.39) H 98 (3.86) 130 (5.12) Hs 80 (3.15) 110 (4.33)	vv5	(8.85)	(6.61)	(8.85)	(12.14)	(16.51)	(3.29)	(6.57)	(6.57)	(8.77)	(12.06)	(16.43)	(11.62)	(15.76)
H 98 (3.86) 130 (5.12) Hs 80 (3.15) 110 (4.33)	W∟	15.5 (0.61) 10						10 (0).39)					
Hs 80 (3.15) 110 (4.33)	Н	98 (3.86) 130 (5.12)												
	Hs	80 (3.15)						110 (4.33)					

Unit: mm (inch)

(b) Slim type main base unit (Q3 SB)



/	Q32SB	Q33SB	Q35SB
W	114 (4.49)	142 (5.59)	197.5 (7.78)
Ws	101 (3.98)	129 (5.08)	184.5 (7.26)
W∟		8.5 (0.33)	
H		98 (3.86)	
Hs		80 (3.15)	

Unit: mm (inch)

- (2) Unit mounting position For enhanced ventilation and ease of module replacement, leave the following clearances between the unit top/bottom and structure/parts. (a) Main base unit (Q3 \square B), extension base unit (Q5 \square B, Q6 \square B, $QA1S6\squareB)$ Indicates the panel top, wiring duct or any part position. PLC 30mm (1.18 inch) or more*1 Panel Door 20mm (0.79 inch) 30mm or more*3 (1.18 inch) or more 5mm (0.20 inch) or more*2 5mm (0.20 inch) or more
- *1: For wiring duct (50mm (1.97 inch) or less height). 40mm (1.58 inch) or more for other cases.
- *2: 5mm (0.20 inch) or more when the adjacent module is removed and the extension cable is connected.
- *3: 80mm (3.15 inch) or more for the connector type.
- (b) Slim type main base unit (Q3 SB)



- *1: For wiring duct (50mm or less) or other, 40mm or more.
- *2: When wiring the slim type main base unit and power supply module, the cable will run off the left edge of the module. Separate the system and wall by 17mm or more to secure the installation space. If the materials, devices or parts on right side of the module may damage the cable coating, attach an insulating material such as spiral tube to it for protection.
- *3: For connector type, 80mm or more.

- (3) Unit mounting orientation
 - (a) Since the PLC generates heat, it should be mounted on a well ventilated location in the orientation shown below.



(b) Do not mount it in either of the orientations shown below.



- (4) Mount the base unit on a flat surface. If the mounting surface is not even, this may strain the printed circuit boards and cause malfunctions.
- (5) Avoid mounting base unit in proximity to vibration sources such as large magnetic contractors and no-fuse circuit breakers; mount these on a separate panel or at a distance.
- (6) In order to avoid the effects of radiated noise and heat, provide the clearances indicated below between the PLC and devices that generate noise or heat (contactors and relays).
 - Required clearance in front of PLC: at least 100 mm (3.94 inch)
 - Required clearance on the right and left of PLC: at least 50 mm (1.97 inch)



(7) Install the main base unit/Q00JCPU (by screwing) in the following procedure.

(a) Fit the two base unit top mounting screws into the enclosure.



(b) Place the right-hand side notch of the base unit onto the right-hand side screw.



(c) Place the left-hand side pear-shaped hole onto the left-hand side screw.



- (d) Fit the mounting screws into the mounting screw holes in the base unit bottom and retighten the four fixing screws.
- Note1) : Install the main base unit to a panel, with no module mounted in the rightmost slot.

Remove the base unit after removing the module from the rightmost slot.

- Note2): The mounting screws included with the slim type main base unit differ from those included with other main base units. Please specify "Cross-recessed binding head machine screw M4×12(black) when ordering the mounting screws for the slim type main base unit.
- (8) Note the following points when mounting a DIN rail.

Mounting a DIN rail needs special adaptors (optional), which are to be user-prepared.

DIN rail adapter	Applicable main base unit
Q6DIN1	Q38B, Q312B, Q68B, Q612B
Q6DIN2	Q35B, Q65B, Q00JCPU
Q6DIN3	Q33B, Q52B, Q55B, Q63B, Q32SB, Q33SB, Q35SB

(a) Applicable adapter types

(b) Adaptor installation method

The way to install the adaptors for mounting a DIN rail to the base unit is given below.



Insert the adaptor (large) into the grooves of the base unit from below.

Push the bottom of the adaptor (large) far enough until it "clicks".

- (c) Applicable DIN rail types (JIS C 2812) TH35-7.5Fe TH35-7.5AI TH35-15Fe
- (d) DIN rail mounting screw intervals

When using a TH35-7.5Fe or TH35-7.5Al DIN rail, rail mounting screws should be tightened at a pitch of 200 mm (7.88 inch) or less in order to ensure that the rail has sufficient strength.



4.1.3 Mounting and removing of module

This section explains how to mount and remove a power supply, CPU, I/O, intelligent function or another module to and from the base unit.

(1) Mounting of module



POINT

- (1) Always insert the module fixing latch of the module into the module fixing hole. Forcing the hook into the hole will damage the module connector and module.
- (2) When using the PLC in a place where there is large vibration or impact, screw the CPU module to the base unit. Module fixing screw : M3 \times 12 (user-prepared)

(2) Removing of module



POINT

When the module fixing screw is used, always remove the module by removing the module fixing screw and then taking the module fixing latch off the module fixing hole of the base unit.

Forcibly removing the module will damage the module.

4.1.4 Setting the extension stage number of the extension base unit

When using two or more extension base units, their extension stage numbers must be set with the extension stage number setting connectors. Extension 1 need not be set since the extension stage number is factory-set to 1. Make this setting in the following procedure.

(1) The extension stage number setting connector of the extension base unit is located under the IN side base cover.

First, loosen the upper and lower screws in the IN side base cover and remove the base cover from the extension base unit.



(2) Insert the connector pin in the required extension number location of the connector (PIN1) existing between the IN and OUT sides of the extension cable connector.



Setting Extension Stage Number of Extension Base Units

	Extension Stage Number Setting							
	1 st stage	2 nd stage	3 rd stage	4 th stage	5 th stage	6 th stage	7 th stage	
Extension stage No. setting							0000000	
Q00JCPU	0	0	×	×	×	×	×	
Q00CPU, Q01CPU	0	0	0	0	×	×	×	
Q02 (H) CPU, Q06HCPU Q12HCPU, Q25HCPU	0	0	0	0	0	0	0	
Q12PHCPU, Q25PHCPU	0	0	0	0	0	0	0	
				O: Can b	be set	×:Can	not be set	

POINT

(1) Set the extension stage number setting connector to any of numbers 1 to 7 in ascending order.

If two or more modules have the same number or any module has no setting, for example, false input and output will occur.

(2) If there are Q5□B/Q6□B and QA1S6□B extension base units, first set the extension stage numbers of the Q5□B/Q6□B's. (The Q00J/Q00/Q01CPU or Q12PH/Q25PHCPU cannot use the QA1S6□B.)

Example: When there are a total of seven extension base units, four Q68B's and three QA1S68B's, set numbers 1 to 4 to the Q68B's and numbers 5 to 7 to the QA1S68B's.

For the extension base connection sequence, connect the $Q5\Box B/Q6\Box B$'s, then connect the $QA1S6\Box B$'s.

(3) Mount the base cover to the extension base unit and tighten the base cover screw.

(Tightening torque: 36 to 48N·cm)



4.1.5 Connecting and disconnecting extension cable

- (1) Instructions for handling an extension cable
 - Do not stamp an extension cable.
 - An extension cable must be connected to the base unit when the base cover has been installed. (After you have set the extension number to the extension base unit,

(After you have set the extension number to the extension base unit, reinstall and screw the base cover.)

- When running an extension cable, the minimum bending radius of the cable should be 55mm (2.17 inch) or more.
 If it is less than 55mm (2.17 inch), a malfunction may occur due to characteristic deterioration, open cable or the like.
- When connecting or disconnecting an extension cable, do not hold the ferrite cores mounted at both ends of the cable.

Hold the connector part of the cable for connection or disconnection.



Holding the ferrite core may cause the cable to open within the connector. Also, if the ferrite core is shifted, the characteristic will change. When handling the cable, take care not to shift the ferrite core positions.

(2) Connecting of extension cable

POINT

When connecting an extension base unit to the main base unit with an extension cable, always plug the OUT side connector of the main base unit and the IN side connector of the extension base unit with an extension cable. The system will not operate properly if the extension cable is connected in the form of IN to IN, OUT to OUT or IN to OUT. When connecting two or more extension base units, plug the OUT side connector of the first extension base unit and the IN side connector of the second extension base unit with an extension cable.

(a) To connect an extension cable to the main base unit, remove the portion under the OUT characters on the base cover with a tool such as a flat-blade screwdriver (5.5×75 , 6×100). This also applies to a case where an extension cable is connected to the OUT side connector of the extension base unit.

When plugging the extension cable to the Q00JCPU, remove the cover of the extension cable connector by hand.



(b) To connect the extension cable to the next extension base unit, remove the seal applied under the IN characters on the base cover.



(c) When plugging the extension cable to any base unit, hold the connector part of the extension cable.



(d) After fitting the extension cable, always tighten the extension cable connector fixing screws.

(Tightening torque: 20N·cm)



(3) Disconnecting extension cable

When unplugging the extension cable, hold and pull the connector part of the extension cable after making sure that the fixing screws have been removed completely.

4.2 General Safety Requirements

	 Install a safety circuit external to the PLC that keeps
VDANOLI	the entire system safe even when there are problems
	with the external power supply or the PLC module.
	Otherwise, trouble could result from erroneous output
	or erroneous operation.
	(1) Outside the PLC, construct mechanical damage
	preventing interlock circuits such as emergency stop,
	protective circuits, positioning upper and lower limits
	switches and interlocking forward/reverse operations.
	(2) When the PLC detects the following problems, it will stop
	calculation and turn off all output in the case of (a).
	In the case of (b), it will stop calculation and hold or turn
	off all output according to the parameter setting.
	Note that the AnS series module will turn off the output in
	either of cases (a) and (b).
	(a) The power supply module has over current protection
	equipment and over voltage protection equipment.
	(b) The CPU module self-diagnostic functions, such as the
	watch dog timer error, detect problems.
	In addition, all output will be turned on when there are
	problems that the CPU module cannot detect, such as
	in the I/O controller. Build a fail safe circuit exterior to
	the PLC that will make sure the equipment operates
	safely at such times.
	(3) Output could be left on or off when there is trouble in the
	outputs module relay or transistor. So build an external
	monitoring circuit that will monitor any single outputs that
	could cause serious trouble.
	 When overcurrent which exceeds the rating or caused
	by short-circuited load flows in the output module for a
	long time, it may cause smoke or fire. To prevent this,
	configure an external safety circuit, such as fuse.
	 Build a circuit that turns on the external power supply
	when the PLC main module power is turned on. If the
	external power supply is turned on first, it could result
	in erroneous output or erroneous operation.
	 When there are communication problems with the data
	link, refer to the corresponding data link manual for the
	operating status of each station. Not doing so could
	result in erroneous output or erroneous operation.
	operating status of each station. Not doing so could
	result in erroneous output or erroneous operation.

DANGER	 When connecting a peripheral device to the CPU module or connecting a personal computer or the like to the special function module to exercise control (data change) on the running PLC, configure up an interlock circuit in the sequence program to ensure that the whole system will always operate safely. Also before exercising other control (program change, operating status change (status control)) on the running PLC, read the manual carefully and fully confirm safety. Especially for the above control on the remote PLC from an external device, an immediate action may not be taken for PLC trouble due to a data communication fault. In addition to configuring up the interlock circuit in the sequence program, corrective and other actions to be taken as a system for the occurrence of a data communication fault should be predetermined between the external device and CPU module.
≜ CAUTION	 Do not bunch the control wires or communication cables with the main circuit or power wires, or install them close to each other. They should be installed 100 mm (3.94 inch) or more from each other. Not doing so could result in noise that would cause erroneous operation. When controlling items like lamp load, heater or solenoid valve using an output module, large current (approximately ten times greater than that present in normal circumstances) may flow when the output is

When the PLC power supply is switched ON to OFF, proper output of the control target may not be provided temporarily due to differences in delay time and starting time between the PLC power supply and the external power supply for control target (especially DC).

turned OFF to ON. Take measures such as replacing the module with one having sufficient rated current.

For example, if the external power supply for control target is switched on in a DC output module and the PLC power supply is then switched on, the DC output module may provide false output instantaneously at power-on of the PLC. Therefore, it is necessary to make up a circuit that can switch on the PLC power supply first.

Also, an abnormal operation may be performed if an external power supply fault or PLC failure takes place.

To prevent any of these abnormal operations from leading to the abnormal operation of the whole system and in a fail-safe viewpoint, areas which can result in machine breakdown and accidents due to abnormal operations (e.g. emergency stop, protective and interlock circuits) should be constructed outside the PLC. The following page gives examples of system designing in the above viewpoint.

(1) System design circuit example (when not using ERR contact of power supply module/for Q00JCPU)



The power-ON procedure is as follows:

For AC

- 1) Switch power ON.
- 2) Set CPU to RUN.
- 3) Turn ON the start switch.
- 4) When the magnetic contactor (MC) comes on, the output equipment is powered and may be driven by the program.

For AC/DC

- 1) Switch power ON.
- 2) Set CPU to RUN.
- 3) When DC power is established, RA2 goes ON.
- 4) Timer (TM) times out after the DC power reaches 100%. (The TM set value should be the period of time from when RA2 goes ON to the establishment of 100% DC voltage. Set this value to approximately 0.5 seconds.)
- 5) Turn ON the start switch.
- 6) When the magnetic contactor (MC) comes on, the output equipment is powered and may be driven by the program. (If a voltage relay is used at RA2, no timer (TM) is required in the program.)

(2) System design circuit example (when using ERR contact of power supply module)



The power-ON procedure is as follows:

For AC/DC

- 1) Switch power ON.
- 2) Set CPU to RUN.
- 3) When DC power is established, RA2 goes ON.
- 4) Timer (TM) times out after the DC power reaches 100%. (The TM set value should be the period of time from when RA2 goes ON to the establishment of 100% DC voltage. Set this value to approximately 0.5 seconds.)
- 5) Turn ON the start switch.
- 6) When the magnetic contactor (MC) comes on, the output equipment is powered and may be driven by the program. (If a voltage relay is used at RA2, no timer (TM) is required in the program.)

(3) Fail-safe measures against failure of the PLC

Failure of a CPU module or memory can be detected by the self-diagnosis function. However, failure of I/O control area may not be detected by the CPU module.

In such cases, all I/O points turn ON or OFF depending on a condition of problem, and normal operating conditions and operating safety cannot sometimes be maintained.

Though Mitsubishi PLCs are manufactured under strict quality control, they may cause failure or abnormal operations due to unspecific reasons. To prevent the abnormal operation of the whole system, machine breakdown, and accidents, fail-safe circuitry against failure of the PLC must be constructed outside the PLC.

Examples of a system and its fail-safe circuitry are described below: <System example>



*1: The output module for fail-safe purpose should be loaded in the last slot of the system. (Y80 to Y8F in the above system.)

<Fail-safe circuit example>



*2: Y80 repeats turning ON and then OFF at 0.5 second intervals. Use a no-contact output module (transistor in the example shown above).

4.3 Power Supply Wiring This section provides instructions for wiring the power supply.

DANGER	 Completely turn off the external power supply when installing or placing wiring. Not completely turning off all power could result in electric shock or damage to the product.
	 When turning on the power supply or operating the module after installation or wiring work, be sure that the module's terminal covers are correctly attached. Not attaching the terminal cover could result in electric shock.

CAUTION	 Be sure to ground the FG terminals and LG terminals to the protective ground conductor. Not doing so could result in electric shock or erroneous operation. When wiring in the PLC, be sure that it is done correctly by checking the product's rated voltage and the terminal layout. Connecting a power supply that is different from the rating or incorrectly wiring the product could result in fire or damage. External connections shall be crimped or pressure welded with the specified tools, or correctly soldered. Imperfect connections could result in short circuit, fires, or erroneous operation. Tighten the terminal screws with the specified torque. If the terminal screws are loose, it could result in short circuits, fire, or erroneous operation. Tightening the terminal screws too far may cause damages to the screws and/or the module, resulting in fallout, short circuits, or malfunction. Be sure there are no foreign substances such as sawdust or wiring debris inside the module. Such debris could cause fires, damage, or erroneous operation. The module has an ingress prevention label on its top to prevent foreign matter, such as wire offcuts, from entering the module during wiring. Before starting system operation, be sure to peel this label because of heat dissipation.

(1) Separate the PLC's power supply line from the lines for I/O devices and power devices as shown below.
When there is much poise, connect an insulation transformer.

When there is much noise, connect an insulation transformer.



(2) Do not connect the 24VDC outputs of two or more power supply modules in parallel and supply power to one I/O module. Parallel connection will damage the power supply modules.



- (3) 100VAC, 200VAC and 24VDC wires should be twisted as dense as possible. Connect the modules with the shortest distance. Also, to reduce the voltage drop to the minimum, use the thickest wires possible (maximum 2mm²).
- (4) Do not bundle the 100VAC and 24VDC wires with, or run them close to, the main circuit (high voltage, large current) and I/O signal lines. If possible, run them more than 100mm (3.94 inch) away.
- (5) As a countermeasure to power surge due to lightening, connect a surge absorber for lightening as shown below.



POINT

- (1) Separate the ground of the surge absorber for lightening (E1) from that of the PLC (E2).
- (2) Select a surge absorber for lightening whose power supply voltage does no exceed the maximum allowable circuit voltage even at the time of maximum power supply voltage elevation.

(5) The following diagram shows the wiring example of power lines, grounding lines, etc. to the main and extension base units.



POINT

- (1) Use the thickest possible (max. 2 mm² (14 AWG)) wires for the 100/200 VAC and 24 VDC power cables. Be sure to twist these wires starting at the connection terminals. To prevent a short-circuit should any screws loosen, use solderless terminals with insulation sleeves.
- (2) When the LG terminals and FG terminals are connected, be sure to ground the wires. Do not connect the LG terminals and FG terminals to anything other than ground. If LG terminals and FG terminals are connected without grounding the wires, the PLC may be susceptible to noise. In addition, since the LG terminals have potential, the operator may receive an electric shock when touching metal parts.
- (3) *1 Error output through the ERR terminal of the power supply module on the extension base is not available. For error output, use the ERR terminal of the power supply module on the main base.

4.3.1 Precaution when connecting the uninterruptive power supply

Be sure of the following terms when connecting the QCPU system to the uninterruptive power supply (abbreviated as UPS hereafter):

Use a UPS which employs the constant inverter power supply method with 5 % or less voltage fluctuation.

Do not use a UPS with the constant commercial power supply method.

4.4 Part Names and Settings of Q00J/Q00/Q01CPU 4.4.1 Part names and settings

(1) Q00JCPU



Front face

With front cover open



No.	Name	Application
1)	Base mounting hole	Pear-shaped hole for mounting the unit to a panel such as a control box. (For M4 screw)
2)	Cover	Protective cover for extension cable connector. Remove this cover when connecting an extension base.
3)	Extension cable connector	Connector for transfer of signals to/from the extension base unit. Connect an extension cable.
4)	POWER LED	LED for indicating 5VDC power. Lit when 5VDC is output normally.

No.	Name	Application
5)	RUN LED	Displays the operating status of the CPU module.
		ON :During operation in "RUN" mode.
		OFF :During a stop in "STOP" mode or when the errorwhose
		occurrence stops operation is detected.
		Flicker : When parameters/program is written during STOP and the
		RUN/STOP/RESET switch is moved from "STOP" to "RUN".
		following stops
		Move the RUN/STOP/RESET switch from "RUN" to "STOP" to
		"RUN".
		Perform reset with the RUN/STOP/RESET switch.
		 Power ON the PLC again.
		To turn ON the RUN LED after writing the parameters, carry out
		the following steps.
		 Perform reset with the RUN/STOP/RESET switch.
		• Power ON the PLC again.
		(If the RUN/STOP/RESET switch is moved from "STOP" to
		RUN to STOP to RUN after changing the parameter values,
		intelligent function module, such as the network parameters)
6)	FRR LED	ON When the self-diagnostic error that will not stop operation is
0)		detected.
		 When continuation of operation at error detection is set in the
		parameter.
		 When the annunciator (F) is turned ON by the SET/OUT
		instruction.
		OFF :Normal
		Flicker : When the error whose occurrence stops operation is detected.
		switch
7)	Module	Connector used for loading an I/O or intelligent function module.
/	connector	To the connector of the spare space where no module is mounted, fit the
		accessory connector cover or QG60 blank cover module to prevent dust
		from entering.
8)	DIN rail adaptor	Holes for mounting a DIN rail adaptor.
0)	mounting holes	One und terresingly appresented with the chiefd methods of the printed singuit
9)	FG terminal	Ground terminal connected with the shield pattern of the printed circuit
10)	I G terminal	Power filter ground having a half potential of the input voltage
11)	Power input	Power input terminals for connection of a 100VAC to 200VAC power
• ,	terminals	supply.
12)	Battery	Backup battery for use of the program memory, standard RAM and
	-	power failure compensation function.
13)	Battery fixing	Hook for holding the battery.
	hook	
14)	Battery	For connection of the battery lead wires.
	connector pin	(When shipped from the factory, the lead wires are disconnected from
15)	RS-2320	Connector for connection of a peripheral device
13)	connector *1	Can be connected by the RS-232 connection cable (OC30R2)
16)	RUN/STOP/RES	RUN :Executes sequence program operation.
	ET switch	STOP :Stops sequence program operation.
		RESET : Performs hardware reset, operation error reset, operation
		initialization or like. (Refer to Section 4.4.3 for details of reset
		operation.)
*4 . \//	han normally again	a acting a apple to the DS 222 connector, clown the apple to provent it

1 : When normally connecting a cable to the RS-232 connector, clamp the cable to prevent it from coming off due to the dangling, moving or careless pulling of the cable. Q6HLD-R2 type RS-232 Connector Disconnection Prevention Holder is available as a clamp for RS-232 connector.

(2) Q00CPU, Q01CPU



When opening the front cover, put your finger here.



No.	Name	Application
1)	Module fixing hook	Hook used to fix the module to the base unit.
		(Single-motion installation)
2)	RUN LED	Displays the operating status of the CPU module.
		ON :During operation in "RUN" mode.
		OFF :During a stop in "STOP" mode or when the errorwhose
		occurrence stops operation is detected.
		Flicker : When parameters/program is written during STOP and the
		RUN/STOP/RESET switch is moved from "STOP" to "RUN".
		To turn ON the RON LED after writing the program, carry out
		Move the PLIN/STOP/PESET switch from "PLIN" to "STOP" to
		"RUN".
		 Perform reset with the RUN/STOP/RESET switch.
		 Power ON the PLC again.
		To turn ON the RUN LED after writing the parameters, carry out
		the following steps.
		Perform reset with the RUN/STOP/RESET switch. Perform reset with the RUN/STOP/RESET switch.
		 Power ON the PLC again. (If the PLN/STOP/PESET switch is moved from "STOP" to
		"RUN" to "STOP" to "RUN" after changing the parameter values
		the new values are not reflected on the parameters related to
		the intelligent function module, such as the network parameters.)
3)	ERR. LED	ON :When the self-diagnostic error that will not stop operation is
,		detected.
		 When continuation of operation at error detection is set in the
		parameter.
		 When the annunciator (F) is turned ON by the SET/OUT
		instruction.
		OFF :Normal
		Flicker :When the error whose occurrence stops operation is detected.
		switch
4)	Module loading	Used to mount the module to the base unit
/	lever	
5)	Battery	Backup battery for use of the program memory, standard RAM and
		power failure compensation function.
6)	Battery fixing hook	Hook for holding the battery.
7)	Battery connector	For connection of the battery lead wires.
	pin	(When shipped from the factory, the lead wires are disconnected from
- 0)		the connector to prevent the battery from consuming.)
8)	RUN/STOP/RESET	RUN : Executes sequence program operation.
	Switch	RESET : Performs bardware reset operation error reset operation
		initialization or like (Refer to Section 4.5.3 for details of reset
		operation.)
9)	RS-232C	Connector for connection of a peripheral device.
-,	connector *1	Can be connected by the RS-232 connection cable (QC30R2).
10)	Module fixing	Hole for the screw used to fix to the base unit. (M3×12 screw)
, 	screw hole	
11)	Module fixing latch	Latch used to fix to the base unit.

 *1) Twoddo many latent reaction doed to fix to the base diff.
 *1 : When normally connecting a cable to the RS-232 connector, clamp the cable to prevent it from coming off due to the dangling, moving or careless pulling of the cable. Q6HLD-R2 type RS-232 Connector Disconnection Prevention Holder is available as a clamp for RS-232 connector.

4.4.2 Switch operation after writing program

Program can be written to the CPU module during STOP or RUN.

- (1) When you write a program during STOP of the CPU module When you write a program during STOP of the CPU module, operate the switch in the following procedure.
 - 1) RUN/STOP/RESET switch: STOP
 - RUN LED: OFF CPU module STOP status to Program write
 - 2) RUN/STOP/RESET switch: Perform reset. (Refer to Section 4.4.3)
 - 3) RUN/STOP/RESET switch: STOP to RUN
 - RUN LED: ON CPU module RUN status
- (2) When you write a program during RUN When you write a program during RUN of the CPU module, you need not operate the switch.

POINT

(1) The CPU module will not go into the RUN status if you move the RUN/STOP/RESET switch to RUN immediately after writing a program in the STOP status of the CPU module. After writing a program, perform reset operation with the RUN/STOP/RESET switch, then move the RUN/STOP/RESET switch to RUN to set the CPU module into the RUN status.

(Refer to Section 4.4.3 for reset operation.)

- (2) To place the CPU module in the RUN status without resetting, move the RUN/STOP/RESET switch from STOP to RUN to STOP to RUN. After the second motion of STOP to RUN, the CPU module goes into the RUN status.
- (3) The program written by online program correction during boot operation is written to the program memory

After making online program correction, also write the program to the standard ROM of the boot source memory. If you forget to write the program to the standard ROM, the old program will be executed at the next boot operation.

(For details of boot operation, refer to the Basic Model QCPU (Q Mode) User's Manual (Function Explanation, Program Fundamentals).)

4.4.3 Reset operation

For the Q00J/Q00/Q01CPU, the RUN/STOP/RESET switch of the CPU module is used to switch between the "RUN status" and "STOP status" and to perform "RESET operation".

When using the RUN/STOP/RESET switch to reset the CPU module, moving the RUN/STOP/RESET switch to the reset position will not reset it immediately.

POINT

Hold the RUN/STOP/RESET switch in the RESET position until reset processing is complete (the flickering ERR. LED goes off).

If you release your hand from the RUN/STOP/RESET switch during reset processing (while the ERR. LED is flickering), the switch will return to the STOP position and reset processing cannot be completed.

Perform reset operation with the RUN/STOP/RESET switch in the following procedure.



4.4.4 Latch clear operation

To execute latch clear, perform remote latch clear operation using GX Developer. Latch clear cannot be executed by operating the switches of the CPU module.

POINT

- (1) The invalid range of latch clear can be set device-by-device by making device setting in the parameter mode.
- (2) For remote latch clear operation using GX Developer, refer to the operating manual of GX Developer.

4.5 Part Names and Settings of Q02 (H) /Q06H/Q12H/Q25H/Q12PH/Q25PHCPU 4.5.1 Part names and settings

This section explains the names and settings of the module.



When opening the front cover, put your finger here.



*1: Not provided for Q02CPU.

No.	Name	Application
1)	Module fixing hook	Hook used to fix the module to the base unit. (Single-motion installation)
2)	Mode LED	Displays the mode of the CPU module. ON (green) : Q mode ON (orange) : A mode Flicker (green) : Mandatory ON/OFF of external input/output
3)	RUN LED	 Displays the operating status of the CPU module. ON : During operation in "RUN" mode. OFF : During a stop in "STOP" mode or detection of error whose occurrence stops operation. Flicker : When parameter/program is written at STOP and RUN/STOP switch is changed from "STOP" to "RUN". To turn ON the RUN LED after writing the program, carry out the following steps. Set the RUN/STOP switch from "RUN" → "STOP" → "RUN". Reset with the RESET/L.CLR switch. Reset represent the PLC power. (If the RUN/STOP switch is set from "RUN" → "STOP" → "RUN" after changing the parameters, the parameters related to the intelligent function module, such as the network parameters, will not be reflected.)
4)	ERR. LED	 ON : Self-diagnostic error (except battery error) is detected and operation will not stop. (When operation continued at error detection is set in the parameter) OFF : Normal Flicker: Detection of error whose occurrence stops operation. When automatic writing to standard ROM is completed normally. (BOOT LED also flickers.)
5)	USER LED	ON : Error detected by CHK instruction or annunciator ON OFF : Normal Flicker: Execution of latch clear
6)	BAT. LED	ON : Battery error occurs due to reduction in battery voltages of CPU module and memory card. OFF : Normal
7)	BOOT LED	 ON : Start of boot operation OFF : Non-execution of boot operation Flicker: When automatic writing to standard ROM is completed normally. (ERR. LED also flickers.)
8)	Module loading lever	Used to mount the module to the base unit.
9)	Memory card EJECT button	Used to eject the memory card from the CPU module.
10)	Memory card loading connector	Connector used to mount the memory card to the CPU module.

No.	Name	Application
11)		Connector for connection with USB-compatible peripheral
	USB connector*2	(Connector type B)
,		Can be connected by USB-dedicated cable.
		Not available for Q02CPU.
12)	RS-232 connector*2	Connector for connection with a peripheral device.
12)		Can be connected by RS-232 connection cable (QC30R2).
		Used to set the items for operation of the CPU module.
		For system protection and parameter-valid drive functions, refer
		Fundamentals) of the CPU module used
		SW1 : Used to set system protection. Batch-inhibits write and
	DID owitch as	control directives to the CPU module. (Shipped in OFF
	DIP Switches	position)
		OFF : No protection
		ON : Protection
		SW2, SW3: Used to specify parameter-valid drive. (Both SW2
13)	2	SW2 SW3 Parameter Drive
	3	OFE OFE Program memory (Drive 0)
	4	ON OFF SRAM card (Drive 1)
	5	OFF ON Flash card/ATA card (Drive 2)
		ON ON Standard ROM (Drive 4)
		Note: Parameters cannot be stored in standard RAM (Drive 3).
		SW4: Must not be used. Normally OFF.
		(Shipped in OFF position)
		SW5: Must not be used. Normally OFF.
		(Shipped in OFF position)
14)	RUN/STOP switch	STOP: Stops sequence program operation.
		RESET: Used to perform hardware reset, operation fault rest,
		operation initialization, etc.
		If this switch is left in the RESET position, the whole
		system will be reset and the system will not operate
15)	RESET/L.CLR	properly. After performing reset, always return this
	SWIICH	L CI R · Used to turn "OFF" or "zero" all data in the parameter-
		set latch area.
		Used to clear the sampling trace and status latch
		registration.
16)	Module fixing screw	Hole for the screw used to fix to the base unit. (M3 $ imes$ 12 screw)
, 17)	Module fixing latch	Latch used to fix to the base unit.
18)		For connection of battery lead wires.
	Battery connector	(When shipped from the factory, the lead wires are
	pin	disconnected from the connector to prevent the battery from
		consuming.)
19) B	Battery	Backup battery for use of program memory, standard RAM and
-		

*2 : When normally connecting a cable to the USB connector or RS-232 connector, clamp the cable to prevent it from coming off due to the dangling, moving or carelessly pulling of the cable.

The Q6HLD-R2 type Connector Disconnection Prevention Holder is available as a clamp for RS232 connector.
4.5.2 Switch operation after writing program

- (1) When you write a program during STOP of CPU module
 When you write a program during STOP of the CPU module, operate the switches in the following procedure.
 (a) DUN(CTOP switche CTOP
 - (a) RUN/STOP switch: STOP
 - RUN LED : OFF CPU module STOP status to Program write
 - (b) RESET/L.CLR switch: Move to RESET once and return to the original neutral position. RUN/STOP switch : STOP to RUN

RUN LED : ON CPU module RUN status

(2) When you write a program during RUN When you write a program during RUN of the CPU module, do not operate the switch.

POINT

(1) The CPU module will not go into the RUN status if you move the RUN/STOP switch to RUN immediately after writing a program in the STOP status of the CPU module.
After writing a program, perform rest with the RESET/L CLR switch, the

After writing a program, perform rest with the RESET/L.CLR switch, then move the RUN/STOP switch to RUN to set the CPU module to the RUN status.

(2) If you want to set the CPU module into the RUN status without resetting, move the RUN/STOP switch from STOP to RUN to STOP to RUN. After the second motion of STOP to RUN, the CPU module goes into the RUN status.

4.5.3 Latch clear operation

To perform latch clear, operate the RESET/L.CLR switch in the following procedure.

- (a) RUN/STOP switch : STOP
- (b) RESET/L.CLR switch : Move the switch to L.CLR several times until the USER LED flickers.

USER LED : Flicker Ready for latch clear.

(c) RESET/L.CLR switch : Move the switch to L.CLR once more.

USER LED: OFF Latch clear complete.

POINT

- (1) Latch clear can be set to be valid or invalid device-by-device by making device setting in the parameter mode.
- (2) In addition to the way of using the RESET/L.CLR switch for latch clear, remote latch clear may be performed from GX Developer. For remote latch clear, refer to the User's Manual (Function Explanation, Program Fundamentals) of the CPU module used.

4.5.4 Mounting and removing memory card during power-on

- (1) When removing the memory card while power is on
 - (a) Using the sequence program or testing of the GX Developer, turn on the special relay "SM609".
 - (b) Make sure that the special relays "SM604", "SM605" turned off by monitoring the GX Developer.
 - (c) Remove the memory card.

POINT

Note that if you do not follow the above procedure, the data within the memory card may be corrupted.

If the CPU module operation at parameter error occurrence has been set to a stop, the CPU module stops operation due to the occurrence of "ICM. OPE. ERROR".

(2) When installing the memory card while power is on(a) Mounting memory card

POINT

Note that after mounting the memory card, remounting processing is performed, increasing the scan time to a maximum of several 10 ms.

4.5.5 Executing automatic write to standard ROM.

Automatic write to the standard ROM is carried out with the following procedures.

- (1) Operation with GX Developer (setting automatic write to standard ROM)
 - (a) Check the "Auto Download all Data from Memory card to standard ROM" item in the PLC parameter boot file setting.



(b) Store the set parameters and programs to be booted in the memory card.

- (2) Operation with CPU module (automatic write to standard ROM)
 - (a) Turn the PLC power OFF.
 - (b) Mount the memory card containing the parameters and program to be booted into the CPU module.
 - (c) Set the parameter valid drive to the mounted memory card using the CPU module DIP switches.
 - When SRAM card is mountedSW2: ON, SW3: OFF
 - When Flash/ATA card is mountedSW2: OFF, SW3: ON
 - (d) Turn the PLC power ON. Boot the file designated in the memory card into the program memory, and after booting is completed, write the contents of the program memory into the standard ROM.
 - (e) When automatic write to standard ROM is completed, the "BOOT LED" will flicker, and the CPU module will stop with an error.
 - (f) Turn the PLC power OFF.
 - (g) Remove the memory card, and set the parameter valid drive to the standard ROM with the CPU module DIP switches.
 - Standard ROMSW2: ON, SW3: ON
- (3) When the PLC power is turned ON, the data will be booted from the standard ROM to the program memory, and actual operation will be possible.

4.6 Part Names of Power Supply Modules

(1) Q61P-A1, Q61P-A2, Q62P, Q63P, Q64P, Q61SP (See section 4.4.1 for part names of power supply of Q00JCPU.)



No.	Name	Application
1)	POWER LED	5VDC power indicator LED
2)	ERR terminals	Turned OFF (opened) when a stop error occurs in the CPU module. Normally off when mounted in an extension base unit.
3)	FG terminal	Ground terminal connected to the shield pattern of the printed circuit board.
4)	LG terminal	Grounding for the power supply filter. The potential of Q61P- A1, Q61P-A2, Q62P, Q64P and Q61SP terminal is 1/2 of the input voltage
5)	Power input terminals	Used to connect a 100VAC power supply.
6)	Power input terminals	Used to connect a 200VAC power supply.
7)	Power input terminals	Used to connect a 100VAC to 200VAC power supply.
8)	Power input terminals	Used to connect a 24VDC power supply.
9)	Power input terminals	Used to connect a 100VAC/200VAC power supply.
10)	+24V, 24G terminals	Used to supply 24VDC power to inside the output module (using external wiring).
11)	Terminal screw	M3.5 × 7
12)	Terminal cover	Protective cover of the terminal block
13)	Module fixing screw	Used to fix the module to the base unit. M3 \times 12 screw (user- prepared) (Tightening torque : 36 to 48N·cm)
14)	Module loading lever	Used to mount the module into the base unit.

POINTS

(1) The Q61P-A1 is dedicated for inputting a voltage of 100 VAC. Do not input a voltage of 200 VAC into it or trouble may occur on the Q61P-A1.

Power	Supply power voltage				
module type	100VAC	200VAC			
Q61P-A1	Operates normally.	Power supply module causes trouble.			
Q61P-A2	Power supply module does not cause trouble. CPU module cannot be operated.	Operates normally.			

(2) Q64P automatically switches the input range 100VAC/200VAC. Therefore, it is not compatible with the intermediate voltage (133VAC to 169VAC). The CPU module may not work normally if the above intermediate voltage is applied.
(3) Ensure that the earth terminals LG and FG are grounded.

(2) A1S61PN, A1S62PN, A1S63P



A1S63P

4)

6)

7)

No.	Name	Application
1)	POWER LED	5VDC power indicator LED
2)	+24V, 24G terminals	Used to supply 24VDC power to inside of the output module (using external wiring).
3)	FG terminals	Ground terminal connected to the shield pattern of the printed circuit board.
4)	LG terminals Grounding for the power supply filter. The potent A1S61PN or A1S62PN terminal is 1/2 of the input	
5)	Power input terminals	Used to connect a 100VAC to 200VAC power supply.
6)	Power input terminals	Used to connect a 24VDC power supply.
7)	Terminal screw	Protective cover of the terminal block
8)	Terminal cover	Protective cover of the terminal block
9)	Module fixing screw	Used to fix the module to the base unit. (M4 screw, tightening torque : 59 to 88N·cm)

POINT

(1) Do not wire the terminal which is printed NC on the terminal block.

(2) Be sure to ground the terminals LG and FG to the protective ground

5. Specifications and Connections of I/O Modules

DANGER	 Completely turn off the external power supply when installing or placing wiring. Not completely turning off all power could result in electric shock or damage to the product. When turning on the power supply or operating the module after installation or wiring work, be sure that the module's terminal covers are correctly attached. Not attaching the terminal cover could result in electric shock.
CAUTION	 Be sure to ground the FG terminals and LG terminals to the protective ground conductor. Not doing so could result in electric shock or erroneous operation. When wiring in the PLC, be sure that it is done correctly by checking the product's rated voltage and the terminal layout. Connecting a power supply that is different from the rating or incorrectly wiring the product could result in fire or damage. External connections shall be crimped or pressure welded with the specified tools, or correctly soldered. Imperfect connections could result in short circuit, fires, or erroneous operation. Tighten the terminal screws with the specified torque. If the terminal screws are loose, it could result in short circuits, fire, or erroneous operation. Tightening the terminal screws too far may cause damages to the screws and/or the module, resulting in fallout, short circuits, or malfunction. Be sure there are no foreign substances such as sawdust or wiring debris inside the module. Such debris could cause fires, damage, or erroneous operation. The module has an ingress prevention label on its top to prevent foreign matter, such as wire offcuts, from entering the module during wiring. Do not peel this label during wiring. Before starting system operation, be sure to peel this label because of heat dissipation.

This chapter provides the specifications and connection diagrams of the Q series I/O modules.

5.1 Input Modules 5.1.1 Specifications of input modules

Type Item	QX10	QX28			
Input form	AC input				
Number of input points	16 points	8 points			
Rated input voltage,	100 to120V AC (+10%/-15%)	100 to 240V AC (+10%/-15%)			
frequency	50/60Hz (±3Hz)	50/60Hz (±3Hz)			
Rated input current	Approx. 8mA (100V AC, 60Hz) Approx. 7mA (100V AC, 50Hz)	Approx. 17mA (200V AC, 60Hz), Approx. 14mA (200V AC, 50Hz), Approx. 8mA (100V AC, 60Hz), Approx. 7mA (100V AC, 50Hz)			
ON voltage/ON current	80V AC or more/5mA or more (50Hz, 60Hz)				
OFF voltage/OFF current	30V AC or less/1.7mA or less (50Hz, 60Hz)				
Number of simultaneously input points (Simultaneous ON ratio)	100% (110V AC) 60% (132V AC)	100% (240V AC) 87.5% (264V AC)			
Response OFF to ON	15ms or less (100V AC, 60Hz)	10ms or less (100V AC, 60Hz)			
time ON to OFF	20ms or less (1	00V AC, 60Hz)			
Writing method for common	16 points/common (Common terminal: TB17)	8 points/common (Common terminal: TB17)			
Number of occupied I/O points	16 points/common (Common terminal:TB17)				
External connection	18-point terminal b	lock (M3 x 6 screw)			
5 V DC internal current consumption	50mA (TYP.	all points ON)			

For all modules, the following is applied. Isolation method: Photocoupler isolation Input display: LED display

Type Item		QX40	QX40-S1	
Input form		DC input (positive common type)		
Number of in	out points	16 p	oints	
Rated input v	oltage	26V DC (+ 20%/-15%,	ripple ratio within 5%)	
Rated input c	urrent	Approx. 4mA	Approx. 6mA	
ON voltage/C	N current	19V or more/3mA or more	19V or more/4mA or more	
OFF voltage/	OFF current	11V or less/1	.7mA or less	
Number of simultaneous input points (Simultaneous ON ratio)		100%		
Response	OFF to ON	1ms/5ms/10ms/20ms/70ms or less	*1	
time	ON to OFF	Initial setting is 10ms.	-	
Writing metho common	od for	16 points/common (Common terminal:TB17)		
Number of occupied I/O		16 points (I/O assignment is set as	16 points (I/O assignment is set as	
points		16-point input module)	16-point high-speed input module)	
External conr	nection	18-point terminal bl	ock (M3 $ imes$ 6 screw)	
5V DC internation	al current	50mA (TYP. all points ON)	60mA (TYP. all points ON)	

For all modules, the following is applied. Isolation method: Photocoupler isolation

Input display: LED display *1: The response time of QX40-S1 is shown below. (Set with parameters of the CPU module. The initial setting is 0.2ms.)

Setting value		0.1	0.2	0.4	0.6	1
OFF→ON	TYP.	0.05ms	0.15ms	0.30ms	0.55ms	1.05ms
	MAX.	0.10ms	0.20ms	0.40ms	0.60ms	1.20ms
ON→OFF	TYP.	0.15ms	0.20ms	0.35ms	0.60ms	1.10ms
	MAX	0.20ms	0.30ms	0.50ms	0.70ms	1.30ms

Type Item		QX41	QX41-S1	
Input form		DC input (positive common type)		
Number of in	out points	32 pc	pints	
Rated input v	oltage	24V DC (+ 20%/-15%,	ripple ratio within 5%)	
Rated input c	urrent	Approx. 4mA	Approx. 6mA	
ON voltage/C	N current	19V or more/3	BmA or more	
OFF voltage/	OFF current	11V or less/1.7mA or less	9.5V or less/1.5mA or less	
Number of simultaneous input points (Simultaneous ON ratio)		60% (20 points/common)		
Response	OFF to ON	1ms/5ms/10ms/20ms/70ms or less	*1	
time	ON to OFF	Initial setting is 10ms.	ľ	
Writing method for common		32 points/common (Common terminal:B01, B02)		
Number of occupied I/O		32 points (I/O assignment is set as	32 points (I/O assignment is set as	
points		32-point input module)	32-point high-speed input module)	
External conr	nection	40-pin connector		
5V DC internation	al current	75mA (TYP. all points ON)		

For all modules, the following is applied. Isolation method: Photocoupler isolation

Input display: LED display *1: The response time of QX41-S1 is shown below. (Set with parameters of the CPU module. The initial setting is 0.2ms.)

Setting value		0.1	0.2	0.4	0.6	1
OFF→ON	TYP.	0.05ms	0.15ms	0.30ms	0.55ms	1.05ms
	MAX.	0.12ms	0.20ms	0.40ms	0.60ms	1.20ms
ON→OFF	TYP.	0.15ms	0.20ms	0.35ms	0.60ms	1.10ms
	MAX.	0.20ms	0.30ms	0.50ms	0.70ms	1.30ms

Type Item		QX42	QX42-S1	
Input form		DC input (positive common type)		
Number of input	ut points	64 p	oints	
Rated input vo	ltage	24V DC (+ 20%/-15%,	ripple ratio within 5%)	
Rated input cu	rrent	Approx	k. 4mA	
ON voltage/ON	l current	19V or more/	3mA or more	
OFF voltage/O	FF current	11V or less/1.7mA or less	9.5V or less/1.5mA or less	
Number of simultaneous input points (Simultaneous ON ratio)		50% (16 points/common)		
Response time	OFF to ON ON to OFF	1ms/5ms/10ms/20ms/70ms or less (CPU module parameter setting) Initial setting is 10ms.	*1	
Writing method for common		32 points/common (Common terminal: 1B01, 1B02, 2B01, 2B02)		
Number of occupied I/O points		64 points (I/O assignment is set as 64-point input module)	64 points (I/O assignment is set as 64-point high-speed input module)	
External connection		40-pin connector × 2		
5V DC internal consumption	current	90mA (TYP. all points ON)		

For all modules, the following is applied. Isolation method: Photocoupler isolation Input display: LED display

*1: The response time of QX42-S1 is shown below. (Set with parameters of the CPU module. The initial setting is 0.2ms.)

Setting value		0.1	0.2	0.4	0.6	1
OFF→ON	TYP.	0.05ms	0.15ms	0.30ms	0.55ms	1.05ms
	MAX.	0.12ms	0.20ms	0.40ms	0.60ms	1.20ms
ON→OFF	TYP.	0.15ms	0.20ms	0.35ms	0.60ms	1.10ms
	MAX.	0.20ms	0.30ms	0.50ms	0.70ms	1.30ms

QX70		QX71		QX72			
16 n	oints	32 n	oints	64 n	oints		
5VDC		5VDC		5\/DC			
(-15%/	(-15%/	(-15%/	(-15%/	(-15%/	(-15%/		
+20%	+20%	+20%	+20%	+20%	+20%		
ripple ratio	ripple ratio	ripple ratio	ripple ratio	ripple ratio	ripple ratio		
within 5%)	within 5%)	within 5%)	within 5%)	within 5%)	within 5%)		
Approx.	Approx.	Approx.	Approx.	Approx.	Approx.		
1.2mA	3.3mA	1.2mA	3.3mA	1.2mA	3.3mA		
		.5V or more	/1mA or mor	e	•		
		1V or less/0	.1mA or less				
100%							
							1ms/5ms/10ms/20ms/70ms or less (CPU module parameter setting)
		Initial settir	ng is 10ms.				
16 points (Con termina	16 points/common (Common terminal:TB17)32 points/common (Common terminal: B01, B02)32 points/co (Common ter 1B01, 1B02, 			/common h terminal: 02, 2B01, 02)			
16 points (I/O assignment is set as 16-point input module)		32 poir assignmer 32-point inp	nts (I/O nt is set as out module)	64 points (I/O assignment is set as 64-point input module)			
18-point ter (M3 × 6	18-point terminal block (M3 \times 6 screw)		onnector	40-pin connector \times 2			
55	mA	70	mA	85	mA		
(TYP. all p	points ON)	(TYP. all p	points ON)	(TYP. all points ON)			
	QX 16 p 5VDC (-15%/ +20%, ripple ratio within 5%) Approx. 1.2mA	QX70 DC input 16 points 5VDC 12VDC (-15%/ (-15%/ +20%, +20%, ripple ratio ripple ratio within 5%) within 5%) Approx. Approx. 1.2mA 3.3mA 3.3mA 1.2mA 3.3mA 1.2mA 3.3mA 3.3mA 16 points/common (Common terminal:TB17) 16 points (I/O assignment is set as 16-point input module) 18-point terminal block (M3 × 6 screw) 55mA (TYP. all points ON)	QX70QXImage: DC input (positive/net)16 points32 p5VDC12VDC5VDC(-15%/(-15%/+20%,+20%,+20%,+20%,+20%,+20%,+20%,+20%,ripple ratioripple ratiowithin 5%)within 5%)Approx.Approx.1.2mA3.3mA3.5V or more.1.2mA3.3mA1.2mA3.3mA1.2mA3.3mA1.2mA10m10m10ms/20ms/70ms or les10m10m11ms/5ms/10ms/20ms/70ms or les11ms/5ms/10ms/20ms/70ms 1ms/5ms/10ms/20ms/70ms1	2QX70QX71DC input (positive/negative comm16 points32 points5VDC12VDC5VDC(-15%/(-15%/(-15%/(-15%/+20%,Approx.Approx.Approx.Approx.Approx.Approx.Approx.1.2mA3.3mA1.2mA3.5V or more/1mA or more100%1ms/5ms/10ms/20ms/70ms or less (CPU mod Initial setting is 10ms.16 points/common (Common (Common (Common terminal: B01, B02)16 points (I/O assignment is set as 16-point input module)18-point terminal block (M3 × 6 screw)55mA70mA (TYP. all points ON)100ma100ma100ma <td>$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$</td>	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$		

For all modules, the following is applied. Isolation method: Photocoupler isolation Input display: LED display

Type Item		QX80	QX81
Input form		DC input (negativ	ve common type)
Number of in	put points	16 points	32 points
Rated input v	oltage	24V DC (+ 20%/-15%,	ripple ratio within 5%)
Rated input of	urrent	Approx	x. 4mA
ON voltage/C	N current	19V or more/	3mA or more
OFF voltage/	OFF current	11V or less/1	.7mA or less
Number of si	multaneous		
input points		100%	60% (20 points/common)
(Simultaneou	s ON ratio)		
Response	OFF to ON	1ms/5ms/10ms/20ms/70ms or les	s (CPU module parameter setting)
time	ON to OFF	Initial setting is 10ms.	
Writing metho	od for	16 points/common	32 points/common
common		(Common terminal:TB18)	(Common terminal: 17,18, 36)
Number of oc	cupied I/O	16 points (I/O assignment is set as	32 points (I/O assignment is set as
points		16-point input module)	32-point input module)
External connection		18-point terminal block (M3 $ imes$ 6 screw)	37-pin D sub-connector
5V DC internation	al current	50mA (TYP. all points ON)	75 (TYP. all points ON)

5.1.2 Input module connection diagram

(1) Type Rated input voltage QX10 100 to 200VAC	(2) Type Rated input voltage QX28 100 to 240VAC
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$Empty \begin{array}{c} 2 \\ Empty \begin{array}{c} 2 \\ 3 \\ Empty \end{array} \begin{array}{c} 1 \\ 3 \\ X01 \\ 5 \\ X02 \\ \hline 0 \\ 0 \\ \hline
 Do not touch the terminals during energization. DANGER Doing so can cause an electric shock or malfunction. 	• Do not touch the terminals during energization. Doing so can cause an electric shock or malfunction.
(3) $\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$(4) \begin{array}{ c c c c }\hline Type & Rated input voltage \\ \hline QX80 & 24VDC \\ \hline \hline QX80 & 24VDC \\ \hline \hline & & & & & & & & & & & & & & & & &$
*1: Negative and positive commons are bo applicable to QX70.	th

	Туре	Rated inp	ut voltage			Туре	Rated i	nput voltage	
	QX41				(6)	0.001	2		
	QX41-S1	24VDC				QADI	2	4000	
(5)	QX42 *1								
	QX42-S1								
	QX71	5/12	VDC						
	QX72 *1	0/12	VDO						
	X00		X10				Module	e bottom	
	0 0		0 0 X11			X	01 20		<u> </u>
+		B19 A19		-		X	(03	2 X02	
│	X02	-B18 A18	<u>X12</u>	-			21	X04	
+	X03	B17 A17	X13	_ •	│	X	22	X06	
	X04		X14		│	X	23		
	X05		X15			<mark> X</mark>	24	5 700 0	•
		B15 A15	0 0 X16 I	-		X	0B 25	6 X0A	
+		-B14 A14		-		X	0D [20]	7 X0C	
+	X07	B13 A13		-			26 0E	X0E	
	X08	-B12 A12	<u> </u>	-			27	X10	
+	<u> </u>	B11 A11	X19	_ •	│	X	28	×12 –	
	X0A	B10 A10	X1A	_ \	+	X	29	10 ×14	
	X0B		<u>X1B</u>		+	X	30		
	X0C		X1C	Ι	+	<mark> X</mark>	17 31		
II	X0D		X1D	Ι		X	19 32		
	X0E		0 X1E	Ţ		X	(1B 33		
	X0F		0 X1F	1		<mark> X</mark>	(1D 34	15 <u>X1C</u>	
		B2 A2	0	•		X	(1F 35		<u> </u>
	Empty	B4 A4	Empty			C	OM 36	17 COM -	┿
	Empty	B3 A3	Empty			Em	pty oz	18 COM	
+		B2 A2	Empty				· · [37]	19 Empty	
+-		B1 A1	Empty						
	+ -						Modu	le top	
	L L								
*1: F	For the QX42/C	X42-S1/QX	72, the above	Э	* 17 ,	18 and	36 are o	connected inte	rnally.
diagram shows the first half 32 points (F).									
	The connections of the latter half 32 points								
(L) are the same as those of the first half 32									
points (F).									
Regard X00 to X1F as X20 to X3F.									
*2: B1 and B2 are connected internally.									
*3: 1	*3: Negative and positive commons are both								
a	applicable to QX71 and QX72.								

5.2 Output Modules 5.2.1 Specifications of output modules

Type Item		QY10	QY18A	
Output form		Contact output		
Number of ou	tput points	16 points	8 points	
Rated switching voltage, current		24V DC, 2A (resistance load)/point; 240V AC, 2A ($COS\phi = 1$)/point; 8A/common	24V DC, 2A (resistance load)/point; 240V AC, 2A ($COS\phi = 1$)/point	
Response	OFF to ON	10ms	or less	
time	ON to OFF	12ms	or less	
Surge suppre	ssor	None		
Fuse		None		
Fuse blown di	isplay	None		
External	Voltage			
power supply	Current	-		
Writing metho	od for	16 points/common	_	
common		(Common terminal:TB17)	_	
Number of oc	cupied I/O	16 points (I/O assignment is set as 16-point output module)		
points				
Protective function		None		
External connection		18-point terminal block (M3 $ imes$ 6 screw)		
5V DC internation	al current	430mA (TYP. all points ON)	240mA (TYP. all points ON)	

For QY10 and QY18A, the following is applied. Isolation method: Relay isolation

Output display: LED display

Type		QY22	QY40P	
Output form		TRIAC output	Transistor output (sink type)	
Number of ou	tput points	16 p	oints	
Rated load vo	ltage	100 – 240V AC (+20%/-15%)	12/24V DC (+20%/-15%)	
Max. load cur	rent	0.6A/point, 4.8A/common	0.1A/point, 1.6A/common	
Bosponso	OFF to ON	1ms + 0.5Hz or less	1ms or less	
time	ON to OFF	1ms + 0.5Hz or less (rated load, resistance load)	1ms or less (rated load, resistance load)	
Surge suppres	ssor	CR absorber	Zener diode	
Fuse		None		
Fuse blown di	splay	None		
External	Voltage		12/24V DC (+20%/-15%, ripple ratio within 5%)	
power supply	Current	-	10mA (at 24V DC, MAX all points ON)	
Writing method for common		16 points/common (Common terminal:TB17)	16 points/common (Common terminal:TB18)	
Number of occupied I/O points		16 points (I/O assignment is set as 16-point output module)		
Protective function		None	Provided	
External connection		18-point terminal bl	ock (M3 screw $ imes$ 6)	
5V DC internation	al current	250mA (MAX. all points ON)	65mA (TYP. all points ON)	

For QY22 and QY40P, the following is applied. Isolation method: Photocoupler isolation

Output display: LED display

Type Item		QY41P	QY42P	
Output form		Transistor out	put (sink type)	
Number of ou	tput points	32 points	64 points	
Rated load vo	ltage	12/24V DC (+20%/-15%)	
Max. load cur	rent	0.1A/point, 2	2A/common	
Response	OFF to ON	1ms c	or less	
time	ON to OFF	1ms or less (rated lo	oad, resistance load)	
Surge suppre	ssor	Zener	diode	
Fuse		None		
Fuse blown di	isplay	None		
External	Voltage	12/24V DC (+20%/-15%	6, ripple ratio within 5%)	
power supply	Current	20mA (at 24V DC)	10mA (at 24V DC)/common	
Writing method for common		32 points/common (Common terminal:A01, A02)	1632 points/common (Common terminal:1A01, 1A02, 2A01, 2A02)	
Number of occupied I/O points		32 points (I/O assignment is set as 32-point output module)	64 points (I/O assignment is set as 64-point output module)	
Protective function		Provided		
External connection		40-pin connector	40-pin connector $ imes$ 2	
5V DC internal current consumption		105mA (TYP. all points ON)	150mA (TYP. all points ON)	

For QY41P and QY42P, the following is applied. Isolation method: Photocoupler isolation Output display: LED display

Type Item		QY50	QY68A	
Output form		Transistor output (sink type)	Transistor output (all points independent, sink/source type)	
Number of ou	Itput points	16 points	8 points	
Rated load vo	oltage	12/24V DC (+20%/-15%)	5/12/24V DC (+20%/-15%)	
Max. load cur	rent	0.5A/point, 4A/common	2A/point, 8A/module	
Bosponso	OFF to ON	1ms or less	3ms or less	
time	ON to OFF	1ms or less (rated load, resistance load)	10ms or less (resistance load)	
Surge suppre	ssor	Zener	diode	
Fuse		6.7A (Not replaceable, blown fuse capacity: 50A)	None	
Fuse blown display		Provided (LED display for fuse blown, signal output to CPU module)	None	
External power	Voltage	12/24V DC (+20%/-15%, ripple ratio within 5%)	-	
supply	Current	20mA (at 24V DC)		
Writing method for common		16 points/common (Common terminal:TB18)	All points independent	
Number of occupied I/O points		16 points (I/O assignment is set as 16-point output module)		
Protective function		None		
External connection		18-point terminal bl	ock (M3 screw $ imes$ 6)	
5V DC internation	al current	80mA (TYP. all points ON) 110mA (TYP. all points		

For QY50 and QY68A, the following is applied. Isolation method: Photocoupler isolation Output display: LED display

Type		QY70	QY71	
Output form		Transistor output (sink type)		
Number of ou	Itput points	16 points	32 points	
Rated load vo	oltage	5/12V DC (+20%/-10%)	5/12V DC (+25%/-10%)	
Max. load cur	rent	16mA/point, 256mA/common	16mA/point, 512mA/common	
Response	OFF to ON	0.5ms	or less	
time	ON to OFF	0.5ms or less (I	esistance load)	
Surge suppre	essor	No	ne	
Fuse		1.6A (Not replaceable, b	lown fuse capacity: 50A)	
Fuse blown d	isplay	Provided (LED display for fuse blown, signal output to CPU module)		
Extornal	Voltage	5/12V DC	5/12V DC	
External		(+20%/-10%, ripple ratio within 5%)	(+25%/-10%, ripple ratio within 5%)	
supply	Current	90mA	170mA	
Supply		(at 24V DC, MAX all points ON)	(at 24V DC)	
Writing metho	od for	16 points/common	32 points/common	
common		(Common terminal:TB18)	(Common terminal: A01, A02)	
Number of oc	cupied I/O	16 points (I/O assignment is set as	32 points (I/O assignment is set as	
points		16-point output module)	32-point output module)	
Protective function		None		
External connection		18-point terminal block (M3 $ imes$ 6 screw)	40-pin connector	
5V DC internation	al current	95mA (TYP. all points ON)	150mA (TYP. all points ON)	

For QY70 and QY71, the following is applied. Isolation method: Photocoupler isolation

Output display: LED display

Type		QY80	QY81P	
Output form		Transistor output (source type)		
Number of ou	tput points	16 points	32 points	
Rated load vo	oltage	12/24V DC (+20%/-15%)	
Max. load cur	rent	0.5A/point, 4A/common	0.1A/point, 2A/common	
Response	OFF to ON	1ms c	or less	
time	ON to OFF	1ms or less (resistance	e load, resistance load)	
Surge suppre	ssor	Zenor	diode	
Fuse		6.7A (Not replaceable, blown fuse capacity: 50A)	None	
Fuse blown display		Provided (LED display for fuse blown, signal output to CPU module)	None	
External Voltage		12/24V DC (+20%/-15%, ripple ratio within 5%)		
power supply Current		20mA (at 24V DC)	40mA (at 24V DC)	
Writing method for common		16 points/common (Common terminal:TB17)	32 points/common (Common terminal: 17,18, 36)	
Number of oc	cupied I/O	16 points (I/O assignment is set as	32 points (I/O assignment is set as	
points		16-point output module)	32-point output module)	
Protective function		None	Provided	
External connection		18-point terminal block (M3 $ imes$ 6 screw)	37-pin D sub-connector	
5V DC internation	al current	80mA (TYP. all points ON)	95mA (TYP. all points ON)	

For QY80 and QY81P, the following is applied. Isolation method: Photocoupler isolation

Output display: LED display

5.2.2 Output module connection diagram



(5)	Type	Rated load voltage	(6) Type Rated load voltage
	QY50	12/24VDC	QY68A 5/12/24VDC
		Y01 1 Y00 Y03 2 3 Y02 Y03 4 5 Y04 L Y05 6 7 Y06 L Y07 7 Y08 L Y07 Y09 10 Y0A L Y07 Y09 10 Y0A L Y00 Y0B 12 Y0C L Y0E Y0F 16 17 12/24VDC L Y08 18 Y0E L Y0E	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$
(7)	Type	Rated load voltage	(8) Type Rated load voltage
	QY70	5/12VDC	QY80 12/24VDC
		Y01 1 Y00 Y03 1 Y02 L Y05 6 7 Y06 L Y07 7 Y08 L Y09 Y09 10 Y0A L Y0A Y00 11 Y0C L Y0D Y0D 14 Y0E L Y0E Y0F 16 17 5/12VDC 18	Y01 1 Y00 Y03 2 Y02 Y03 4 Y02 Y05 6 Y04 Y07 8 Y08 Y09 10 11 Y08 11 Y06 Y09 10 11 Y09 12 Y06 Y00 14 Y06 Y07 16 TOC Y07 18 Y0E

Type Rated load voltage	(10) Type Rated load voltage
(9) QY41P 12/24VDC	(10) QY71 5/12VDC
(9) QY41P QY42P 12/24VDC L Y00 B20 A20 Y10 L Y01 B19 A19 Y11 L Y02 B18 A18 Y12 L Y03 B17 A17 Y13 L Y04 B16 A16 Y15 L	(10) QY71 5/12VDC L Y00 B20 A20 Y10 L L Y01 B19 A19 Y11 L L Y02 B18 A18 Y12 L V03 B17 A17 Y13 L L Y04 B16 A16 Y14 L V05 B15 A15 Y15 L
← L Y06 B14 A14 Y16 L ←	+ <u>L</u> <u>Y06</u> <u>B14</u> <u>A14</u> <u>Y16</u> <u>L</u> +
Y07 B13 A13 Y17 L	Y07 B13 A13 Y17 L
Y08 B12 A12 L	← <u>L</u> <u>Y08</u> <u>B12</u> <u>A12</u> <u>L</u> ←
► <u>Y09</u> B11 A11 Y19 L	• <u>L</u> <u>Y09</u> <u>B11</u> <u>A11</u> <u>Y19</u> <u>L</u> •
Empty B4 A4 Empty	Empty B4 A4 Empty
Empty B3 A3 Empty	Empty B3 A3 Empty
$\begin{array}{c c} 12/24 \\ \hline 12/24 \\ \hline \end{array} \\ \hline \end{array} \\ \begin{array}{c c} B2 \\ B2 \\ \hline \end{array} \\ \hline \end{array} \\ \hline \end{array} \\ \begin{array}{c c} COM \\ COM \\ \hline \end{array} \\ \hline \end{array}$	
B1 A1 COM	B1 A1 CONVI
* For OY42P, the above diagram shows the first half	* B1 and B2 and A1 and A2 are internally
32 points (F).	connected.
The connections of the latter half 32 points (L) are	
the same as those of the first half 32 points (F).	
Regard Y00 to Y1F as Y20 to Y3F.	
connected.	



5.3 Input/output Composite Module

5.3.1 QH42P type input/output composite module

(1) Specifications

(a) Input section specifications

Type		DC Input (positive common)
Number of ir	nput points	32 points
Isolation me	thod	Photocoupler isolation
Rated input	voltage	24V DC (+20%/-15%, ripple ratio within 5%)
Rated input	current	Approx. 4mA
ON voltage/	ON current	19V DC or more/3mA or more
OFF voltage	/OFF current	11V DC or less/1.7mA or less
Number of s	imultaneously input	100% (50 ° C)
points (Simu	Itaneous ON ratio)	75% (55 °C)
Response	OFF to ON	1ms/5ms/10ms/20ms/70ms or less
time		(CPU module parameter setting)
ume		*1 Initial setting is 10ms.
Writing meth	od for common	32 points/common (Common terminal:1B01, 1B02)
Number of occupied I/O points		32 points (I/O assignment is set as 32-point I/O composite module.)
Operation display		32-point changeover indication with ON indication (LED) switch
External wire connection method		40-pin connector
5V DC interr consumptior	nal current	130mA (TYP. all points ON)

*1: The response time of the input section can be changed using the following GX developer.

CPU module	GX Developer
Q00JCPU, Q00CPU, Q01CPU	Version 7 or later
Q02 (H) CPU, Q06HCPU, Q12HCPU, Q25HCPU	Version 5 or later
Q12PHCPU, Q25PHCPU	Version 7.10L or later

Notice that the set response time of the input section becomes valid when using the CPU module with the first 5 digits of the serial number, "01112" or later. (To check the serial number, see Section 1.2.)

(b) Output section specifications

Item	Туре	Transistor output (sink type)
Number of ou	tput points	32 points
Isolation meth	od	Photocoupler isolation
Rated load vo	ltage	12/24V DC (+20%/-15%)
Max. load cur	rent	0.1A/point, 2A/common
Response	OFF to ON	1ms or less
Time	ON to OFF	1ms or less (rated load, resistance load)
Surge suppres	ssor	Zenor diode
Fuse		None
Fuse blown di	isplay	None
External	Voltage	12/24V DC (+20%/-15%, ripple ratio within 5%)
power supply	power supply Current 15mA (at 24V DC)/common (MAX. all points Of	
Writing method for common		32 points/common (Common terminal: 2A01, 2A02))
Protective fun	ction	Provided

(2) Connection diagram

Туре	Rated load voltage	Туре	Rated load voltage
QH42P	24VDC	QH42P	12/24VDC
(input section)		(output section)	,
QH42P (input section) - $ -$	24VDC $B20$ $A20$ $X10$ $B19$ $A19$ $X11$ $B19$ $A19$ $X12$ $B19$ $A19$ $X12$ $B18$ $A18$ $X12$ $B17$ $A17$ $X13$ $B17$ $A17$ $X13$ $B16$ $A16$ $X14$ $B16$ $A16$ $X14$ $B13$ $A13$ $X17$ $B14$ $A14$ $X19$ $B11$ $A11$ $X19$ $B10$ $A10$ $X18$ $B10$ $A10$ $X1B$ $B10$ $A10$ $X10$ $B10$	QH42P (output section)	12/24VDC B20 A20 Y10 B19 A19 Y11 B19 A19 Y11 B18 A18 Y12 B17 A17 Y13 B17 A17 Y13 B16 A16 Y14 B15 A15 Y15 B14 A14 Y16 B13 A13 Y17 B13 A13 Y17 B13 A13 Y18 B12 A12 Y18 B11 A11 Y19 B10 A10 Y1A B10 A10 Y1B
Empty	/ B3 A3 Empty	Empty	B3 A3 Empty
	B2 A2 Empty	DC12/24V	
CON	A B1 A1 Empty	DC12/24V	B1 A1 COM
*B1 and B2, and	d A1 and A2 are connected	*B1 and B2, and	A1 and A2 are connected
internally.		internally.	

5.3.2 QX48Y57 type input/output composite module

(1) Specifications

(a) Input section specifications

Specificatio	Type	DC input (positive common)
Number of i	nput points	8 points
Isolation me	ethod	Photo coupler isolation
Rated input	voltage	24VDC (-15%/+20%, ripple ratio within 5%)
Rated input	current	Approx. 4mA
ON voltage	ON current	19VDC or more/3mA or more
OFF voltage	e/OFF current	11VDC or less/1.7mA or less
Number of simultaneou points (Simu ratio)	usly input ultaneous ON	100%
Response	$OFF \to ON$	1ms/5ms/10ms/20ms/70ms or less
time	$ON \rightarrow OFF$	(CPU module parameter setting) *1 Initial setting is 10ms
Writing met common	hod for	8 points/common (Common terminal:TB09)
Number of o	occupied	16 points (The I/O assignment is set as the 16-point
input/output points		input/output composite module)
Operation indication		ON indication (LED)
External wire connection method		18-point terminal block (M3 $ imes$ 6 screw)
5VDC interr consumptio	nal current	80mA (TYP. all points ON)

*1: The response time of the input section can be changed with the following GX Developer.

CPU Module	GX Developer
Q00JCPU, Q00CPU, Q01CPU	Version 7 or later
Q02 (H) CPU, Q06HCPU, Q12HCPU, Q25HCPU	Version 5 or later
Q12PHCPU, Q25PHCPU	Version 7.10L or later

Notice that the set response time of the input section becomes valid when using the CPU module with the first 5 digits of the serial number, "01112" or later. (To check the serial number, see Section 1.2.)

(b) Output section specifications

Specification	Туре	Transistor output (sink type)	
Specification			
Number of c	output points	7 points	
Isolation me	thod	Photo coupler isolation	
Rated load v	/oltage	12/24VDC (-15%/+20%)	
Max. load cu	urrent	0.5A/point, 2A/common	
Response	$OFF \to ON$	0.1ms or less	
time	$ON \rightarrow OFF$	1ms or less (rated load, resistance load)	
Surge suppr	essor	Zener diode	
Fuse		4A (not replaceable) (Fuse blowing capacity: 50A)	
	diaction	Provided (When the fuse blows, it is indicated with the	
	luication	LED and a signal is output to the CPU.)	
External	Voltage	12/24VDC(+20%/-15%) (ripple ratio within 5%)	
supply	Current	$10m\Lambda$ (at 24λ /DC)	
power	Current	1011A (al 24VDC)	
Writing method for		Z nainte (common (Common terminal) (D40)	
common		r points/common (Common terminal: TBT8)	
Protective fu	Inction	None	

(2) Connection diagram

	0
Rated input voltage	Rated load voltage
24VDC	12/24VDC
	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

5.4 Interrupt Module 5.4.1 QI60 type interrupt module

Туре	Q160	
	DC input (positive common type)	
nput points	16 points	
voltage	24V DC (+20%/-15%, ripple ratio within 5%)	
current	Approx. 6mA	
ON current	19V DC or more/4.0mA or more	
OFF current	11V DC or less/1.7mA or less	
imultaneously input	100%	
Itaneous ON ratio)	100 /0	
OFF to ON	*2	
ON to OFF	2	
nod for common	16 points/common (Common terminal:TB17)	
equaled I/O points	16 points	
	(I/O assignment is set as 16-point I/O composite module.)	
e connection method	18-point terminal block (M3 $ imes$ 6 screw)	
nal current	60mA (TVP all points ON)	
۱		
	Type Ty	

For QI60, the following is applied. Isolation method: Photocoupler isolation Iutput display: LED display

*1: The response time of QI60 can be changed according to the following.

When using other products, the response time will not changed. (Fixed at 0.2ms)

Product name	Description
CPU module	Product with first 5 digits of serial number, "02092" or later.
GX Developer	Version 6 or later

*2 The response time of QI60 is shown below. (Set with CPU module parameters. Initial setting is 0.2ms.)

Setting valu	le	0.1	0.2	0.4	0.6	1
	TYP.	0.05ms	0.15ms	0.30ms	0.55ms	1.05ms
	MAX.	0.12ms	0.20ms	0.40ms	0.60ms	1.20ms
	TYP.	0.15ms	0.20ms	0.35ms	0.60ms	1.10ms
	MAX.	0.20ms	0.30ms	0.50ms	0.70ms	1.30ms

Туре	Rated load voltage	
Q160	24VDC	
	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

5.5 Wiring of I/O Equipment

- (1) The wires used for connection to the terminal block should be 0.3 to 0.75mm² in core and 2.8mm (0.11 inch) max. in outside diameter.
- (2) Insulation-sleeved crimping terminals cannot be used with the terminal block.

It is recommended to cover the wire connections of the crimping terminals with mark or insulation tubes.

- (3) Run the input and output lines away from each other.
- (4) When the wiring cannot be run away from the main circuit and power lines, use a batch-shielded cable and ground it on the PLC side. In some cases, ground it in the opposite side.



- (5) Where wiring runs through piping, ground the piping without fail.
- (6) Run the 24VDC input line away from the 100VAC and 200VAC lines.
- (7) Wiring of 200m (686.67 ft.) or longer distance will give rise to leakage currents due to the line capacity, resulting in a fault.

Refer to the troubleshooting chapter of the I/O Module User's Manual.

6. Error Codes

If a fault occurs when the CPU module is switched from STOP to RUN or during running (while the PLC system is ON), the CPU module will output the error (LED indication) by the self-diagnosis function and store the error information in the special relay (SM) and special register (SD).

REMARK

For the error code (4000H to 4FFFH) of the errors occurred on GX Developer, intelligent function module or network system at request for general data processing, refer to the user's manual of the CPU module (Hardware Design, Maintenance and Inspection).

6.1 How to Read the Error Code

If an error occurs, read the error code, error message and so on using the following GX Developer.

CPU Module	GX Developer
Q00JCPU, Q00CPU, Q01CPU	Version 7 or later
Q02 (H) CPU, Q06HCPU, Q12HCPU, Q25HCPU	Version 5 or later
Q12PHCPU, Q25PHCPU	Version 7.10L or later

Use the following procedure to read the error code with GX Developer.

- (1) Start GX Developer.
- (2) Connect the PLC and peripheral device.
- (3) On GX Developer, choose the [Online]-[Transfer setup] menu and specify the port and the CPU module from which the error code will be read.
- (4) Choose [Diagnostics]-[PLC diagnostics] menu.
- (5) Clicking the Current error button in the PLC diagnostics dialog box shows the error code and error message.
- (6) Choose the [Help]-[PLC error] menu and confirm the definition of the corresponding error code.

For details of how to operate GX Developer, refer to the operating manual of your GX Developer.

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- Before using the product for special purposes such as nuclear power, electric power, aerospace, medicine or passenger movement vehicles, consult with Mitsubishi.
- This product has been manufactured under strict quality control. However, when installing the product where major accidents or losses could occur if the product fails, install appropriate backup or failsafe functions in the system.

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