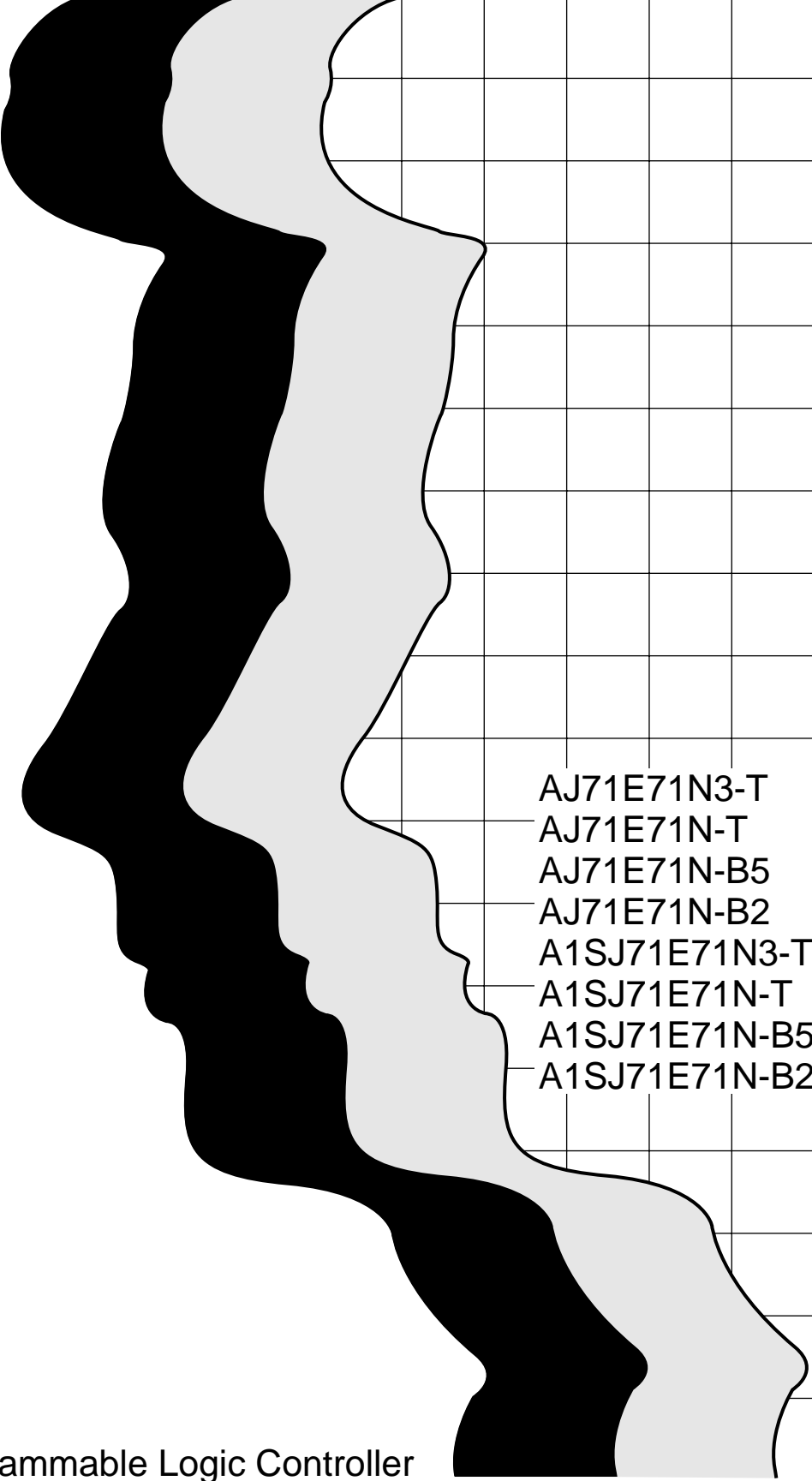


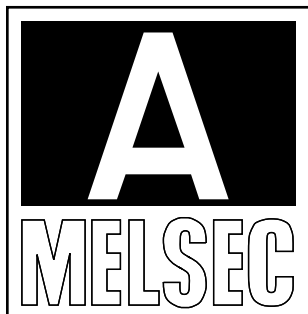
mitsubishi

For A Ethernet Interface Module

User's Manual



AJ71E71N3-T
AJ71E71N-T
AJ71E71N-B5
AJ71E71N-B2
A1SJ71E71N3-T
A1SJ71E71N-T
A1SJ71E71N-B5
A1SJ71E71N-B2



Mitsubishi Programmable Logic Controller

● SAFETY PRECAUTIONS ●

(Read these precautions before using.)

When using Mitsubishi equipment, thoroughly read this manual and the associated manuals introduced in this manual. Also pay careful attention to safety and handle the module properly.

These precautions apply only to Mitsubishi equipment. Refer to the user's manual of the CPU module to use for a description of the PLC system safety precautions.

These ● SAFETY PRECAUTIONS ● classify the safety precautions into two categories: "DANGER" and "CAUTION".



DANGER

Procedures which may lead to a dangerous condition and cause death or serious injury if not carried out properly.



CAUTION

Procedures which may lead to a dangerous condition and cause superficial to medium injury, or physical damage only, if not carried out properly.

Depending on circumstances, procedures indicated by  CAUTION may also be linked to serious results.

In any case, it is important to follow the directions for usage.

Store this manual in a safe place so that you can take it out and read it whenever necessary. Always forward it to the end user.

[DESIGN PRECAUTIONS]

DANGER

- When controlling (changing data, program or operation status (remote RUN/STOP) in particular) a PLC while it is running via a device such as a personal computer connected to the special function module, configure an interlock circuit in the sequence program so that the safety of the overall system is always maintained.

Especially, when performing the above control for a remote PLC from an external device, troubles occurring on the PLC side due to data communication error may not be handled immediately.

Determine error handling methods between the external device and the PLC CPU for when data communication errors occur, in addition to configuring a interlock circuit in the sequence program.

CAUTION

- Do not bundle the control wires and the communication cables with the main circuit and the power wires, and do not install them close to each other.

They should be installed 100mm (3.9 inch) or more from each other.

Not doing so could result in noise that would cause erroneous operation.

[INSTALLATION PRECAUTIONS]

CAUTION

- Use the PLC in the environment given in the general specifications section of this manual. Using the PLC outside the range of the general specifications may result in electric shock, fire, or erroneous operation or may damage or degrade the product.
- Make sure to switch all phases of the external power supply off when installing or placing wiring. If you do not switch off the external power supply, it will cause electric shock or damage to the product.
- Make sure to switch all phases of the external power supply off before mounting or removing the module. If you do not switch off the external power supply, it may result in electric shock, or may damage the product.
- Insert the fixing latch on the bottom of the module into the fixing hole in the base unit and install the module using the hole point as a fulcrum. (Modules in AnS series, make sure screws are securely tightened to base unit with specified torques.)
Improper installation may cause erroneous operation, failure, or the module to fall out.
- Tighten the screw within the range of specified torque.
If the screws are loose, it may result in fallout, short circuits, or malfunction.
Tightening the screws too far may cause damage to the screw and/or the module, resulting in fallout, short circuits, or erroneous operation.
- Do not touch the electronic parts or the module conducting area.
It may cause erroneous operation or failure.

[WIRING PRECAUTIONS]

CAUTION

- Do not connect the AUI cable when the module installation station's power is turned on.
- Be sure to fix communication cables and power cables leading from the module by placing them in the duct or clamping them. Cables not placed in the duct or without clamping may hang or shift, allowing them to be accidentally pulled, which may result in a module malfunction and cable damage.
- Perform correct pressure-displacement, crimp-contact or soldering for wire connections using the tools specified by the manufacturers. Attach connectors to the module securely.
- Tighten the terminal screws within the range of specified torque.
If the terminal screws are loose, it may result in short circuits or malfunction.
Tightening the screws too far may cause damage to the screw and/or the module, resulting in fallout, short circuits, or erroneous operation.
- When detaching the communication cable or power cable from the module, do not pull the cable portion. For cables with connectors, hold the connector at the junction to the module, then detach it. For connectors without connectors, first loosen the screw at the junction, then detach the cable.
Pulling the cable portion while it is connected to the module may cause a malfunction or damage to the module and cable.

CAUTION

- Be sure that cuttings, wire chips, or other foreign matter do not enter the module.
Foreign matter may start a fire or cause an accident or erroneous operation.

[STARTING AND MAINTENANCE PRECAUTIONS]

DANGER

- Do not touch the terminals while the electricity is on.
Doing so could cause erroneous operation.
- Make sure to switch all phases of the external power supply off before cleaning or re-tightening screws.
If you do not switch off the external power supply, it will cause failure or erroneous operation of the module.
If the screws are loose, it may result in fallout, short circuits, or erroneous operation.
Tightening the screws too far may cause damage to the screws and/or the module, resulting in fallout, short circuits, or erroneous operation.

CAUTION

- Do not disassemble or rebuild the module.
It may cause failure, erroneous operation, injury, or fire.
- Make sure to switch all phases of the external power supply off before mounting or removing the module.
If you do not switch off the external power supply, it will cause failure or erroneous operation of the module.
- Always make sure to touch the grounded metal to discharge the electricity charged in the body, etc., before touching the module.
Failure to do so may cause a failure or malfunctions of the module.

[OPERATING PRECAUTIONS]

DANGER

- Do not write data in the "system area" in the buffer memory of the special function module.
Also, of the output signals directed to the special function module from the PLC CPU, do not output (switch on) the signals that are "use-prohibited".
If data is written to the "system area" or output is performed with respect to a "use-prohibited" signal, it may result in the malfunction of the PLC system.

CAUTION

- Before performing the control of the PLC in operation (especially changing data, program, and operation status (remote RUN/STOP)) by connecting a personal computer, etc. to the special function module, read the manual carefully and confirm if the overall safety is maintained. Failure to perform correct operations to change data, program, or the operation status may result in system malfunction, machine damage, or an accident.
- Remote RUN/STOP for the module installation station's PLC CPU is recommended to use the "Data Exchange during PLC CPU STOP" function after thoroughly reading the manual. If the remote RUN/STOP is executed without using the "Data Exchange during PLC CPU STOP" function, the output signal from the PLC CPU to the module goes OFF and the communication line is disconnected (close processing). As a result, all data transmission from other nodes, including status control of the PLC CPU, becomes impossible.

[DISPOSAL PRECAUTIONS]

CAUTION

- When disposing of this product, handle it as industrial waste.

REVISIONS

* The manual number is given on the bottom left of the back cover.

Print Date	*Manual Number	Revision								
Jan., 2002	SH(NA)-080192-A	First edition								
May, 2003	SH(NA)-080192-B	<div><div>Additional model</div><div>AJ71E71N-T, AJ71E71N-B5, A1SJ71E71N-T, A1SJ71E71N-B5</div><div>Deleted model</div><div>AJ71E71N-B5T, A1SJ71E71N-B5T</div><div>Correction</div><div>SAFETY PRECAUTIONS, About the Generic Terms and Abbreviations, Product Configuration, Section 1.1 (1), 1.3, Section 2.1, 2.2, 2.3, 2.4, Section 3.1, 3.2, 3.6.2 (11), 3.7.2, Section 4.2, 4.4, 4.5.1, 4.7, Section 5.3.1, 5.4.3, Section 6.3.3, Section 7.4.3, Section10.1, Section 13.1.1, 13.4.3, Appendix 1.1, Appendix 6, Appendix 7, Appendix 9</div><div>Term change</div><table><tr><td>Before change</td><td>After change</td></tr><tr><td>Routing information parameter</td><td>Router relay parameter</td></tr></table></div>	Before change	After change	Routing information parameter	Router relay parameter				
Before change	After change									
Routing information parameter	Router relay parameter									
Dec., 2004	SH(NA)-080192-C	<div><div>Additional model</div><div>AJ71E71N3-T, A1SJ71E71N3-T</div><div>Correction</div><div>SAFETY PRECAUTIONS, About the Generic Terms and Abbreviations, Product Configuration, Section 2.1, 2.2, 2.3 (3), Section 3.2, 3.7.2, Section 4.2, 4.3.2, 4.7.1 (3), 4.7.4, Section 5.3.1, 5.4.3 Point (4), 5.4.4, 5.4.5, Section 7.3.3 Remark, Section 10.1.3 (4). Chapter 11, Chapter 12, Appendix 1.1, Appendix 6, Appendix 7, Appendix 9</div><div>Term change</div><table><tr><td>Before change</td><td>After change</td></tr><tr><td>Net ID, Net address</td><td>Network address</td></tr><tr><td>Subnet ID</td><td>Subnet address</td></tr><tr><td>Host ID</td><td>Host address</td></tr></table></div>	Before change	After change	Net ID, Net address	Network address	Subnet ID	Subnet address	Host ID	Host address
Before change	After change									
Net ID, Net address	Network address									
Subnet ID	Subnet address									
Host ID	Host address									
Jun., 2006	SH(NA)-080192-D	<div><div>Correction</div><div>Compliance with the EMC and Low Voltage Directives, Section 5.4.3, Appendix 3</div></div>								

Japanese Manual Version SH-080191-E

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INTRODUCTION

Thank you for purchasing the Mitsubishi programmable controller MELSEC-A Series.

Before using your MELSEC-A Series, please read this manual thoroughly to gain an understanding of the functions and performances of the A Series PLC so that the equipment is used to its optimum.

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(RANDOM ACCESS BUFFER EXCHANGE SECTION)

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MANUALS

Following is a list of manuals related to the Ethernet Interface Module.

Related Manuals

Manual Name	Manual No. (Model Code)		
MELSECNET and MELSECNET/B Data Link System Reference Manual This manual gives an overview and the specifications for MELSECNET (II) and MELSECNET/B and the procedures for setting the link parameters and operation and troubleshooting. Please read this manual when accessing other stations via data link systems. (Sold separately)	IB-66350 (13JF70)		
MELSECNET/10 Network System Reference Manual This system gives an overview of and the specifications for the MELSECNET/10 and the procedures for setting and operating the parameters, and explains about programming and troubleshooting. Please read this manual when accessing remote stations via the MELSECNET/10 network system or when accessing another station using data link instructions. (Sold separately)	AnU	PLC to PLC	IB-66440 (13JE33)
		Remote I/O	SH-3509 (13JE72)
	QnA		IB-66620 (13JF77)
	QnA/Q4AR		IB-66690 (13JF78)

Compliance with the EMC and Low Voltage Directives

When incorporating the Mitsubishi PLC into other industrial machinery or equipment and keeping compliance with the EMC and low voltage directives, refer to Chapter 3 "EMC Directive and Low Voltage Instruction" of the User's Manual (Hardware) for the CPU module used or the PLC CPU supplied with the base unit.

The CE logo is printed on the rating plate of the PLC, indicating compliance with the EMC and low voltage directives.

For making this product compliant with the EMC and low voltage directives, please refer to Section 3.1.3 "Cable" in Chapter 3 of the above-mentioned user's manual.

About the Generic Terms and Abbreviations

1

Module terms and abbreviations

This manual uses the following terms and abbreviations for the Ethernet interface module and the PLC CPU module. When display of the model name is required, the module name will be returned.

Abbreviations/Terms		Descriptions/Pertinent Module
A1SJ71E71N3-T		A1SJ71E71N3-T Ethernet Interface Module
A1SJ71E71N-T		A1SJ71E71N-T Ethernet Interface Module
A1SJ71E71N-B5		A1SJ71E71N-B5 Ethernet Interface Module
A1SJ71E71N-B2		A1SJ71E71N-B2 Ethernet Interface Module
ACPU PLC CPU		The appropriate CPU module shown in Item 2.2. Sometimes shown as CPU in diagrams. (Including PLC CPUs with MELSECNET data link functions)
AJ71E71 (Previous products)		AJ71E71, A1SJ71E71-B2, A1SJ71E71-B5
AJ71E71-S3 (Previous products)		AJ71E71-S3, A1SJ71E71-B2-S3, A1SJ71E71-B5-S3
AJ71E71N3-T		AJ71E71N3-T Ethernet Interface Module
AJ71E71N-T		AJ71E71N-T Ethernet Interface Module
AJ71E71N-B5		AJ71E71N-B5 Ethernet Interface Module
AJ71E71N-B2		AJ71E71N-B2 Ethernet Interface Module
AnA/AnU/QnACPU AnU/QnACPU		AnACPU, AnUCPU, QnACPU AnUCPU, QnACPU
AnACPU		A2ACPU, A2ACPU-S1, A2ACPUP21/R21, A2ACPUP21/R21-S1, A3ACPU and A3ACPUP21/R21
AnNCPUP		A1NCPUP, A1NCPUP21/R21, A2NCPUP, A2NCPUP-S1, A2NCPUP21/R21, A2NCPUP21/R21-S1, A3NCPUP, A3NCPUP21/R21
AnUCPU		A2UCPU, A2UCPU-S1, A3UCPU, A4UCPU, A2ASCPU, and A2ASCPU-S1
E71		AJ71E71N3-T, AJ71E71N-T, AJ71E71N-B5, AJ71E71N-B2, AJ71E71N-B5T, A1SJ71E71N3-T, A1SJ71E71N-T, A1SJ71E71N-B5, A1SJ71E71N-B2, A1SJ71E71N-B5T
E71 (Previous products)		AJ71E71(-S3), A1SJ71E71-B2(-S3), A1SJ71E71-B5(-S3)
LP21/BR11		AJ71LP21 (G), AJ71BR11, AJ71LR21, A1SJ71LP21, A1SJ71BR11, A1SJ71LR21
LP25/BR15		AJ72LP25 (G), AJ72BR15, AJ72LR25
QCPU	Q mode	Q00JCPU, Q00CPU, Q01CPU, Q02CPU, Q02HCPU, Q06HCPU, Q12HCPU, Q25HCPU, Q12PHCPU, Q25PHCPU, Q12PRHCPU, Q25PRHCPU
	A mode	Q02CPU-A, Q02HCPU-A, Q06HCPU-A
QE71		AJ71QE71N3-T, AJ71QE71N-T, AJ71QE71N-B5, AJ71QE71N-B2, AJ71QE71N-B5T, A1SJ71QE71N3-T, A1SJ71QE71N-T, A1SJ71QE71N-B5, A1SJ71QE71N-B2, A1SJ71QE71N-B5T
QLP21/QBR11		AJ71QLP21 (S/G), AJ71QBR11, AJ71QLR21, A1SJ71QLP21, A1SJ71QBR11, A1SJ71QLR21
QLP25/QBR15		AJ72QLP25 (G), AJ72QBR15, AJ72QLR25, A1SJ72QLP25, A1SJ72QBR15, A1SJ72QLR25
QnACPU		Q2ACPU, Q2ACPU-S1, Q3ACPU, Q4ACPU, Q4ARCPU, Q2ASCPU, Q2ASCPU-S1, Q2ASHCPU and Q2ASHCPU-S1

2

Other terms and abbreviations

This manual uses the following terms and abbreviations to explain the E71 data exchange functions. When it is necessary to clearly show what is being explained the name or model name will be written.

Abbreviation/Terms	Description
External device	Remote node personal computers, computers, workstations (WS), and other E71/QE71s, etc. connected to the Ethernet to exchange data.
Data link system	MELSECNET (II), MELSECNET/B data link systems
Data link module	MELSECNET (II), MELSECNET/B modules
Network	10BASE5, 10BASE2, 10BASE-T network system, data link system
Network system	MELSECNET/10 network system
Network module	MELSECNET/10 module
Ethernet	10BASE5, 10BASE2, 10BASE-T network system
Ethernet address	A machine-specific address that is also referred to as the MAC (Media Access Control) address. This is used to identify the addresses of external devices over a network. The Ethernet address of the E71 can be verified on the MAC ADD column of the rating plate.
GX Developer	GX Developer (SW2D5C/F-GPPW-E or later)
I/F	Interface
MELSECNET	Network system, data link system

3

Terminology

For information on terminology, please use the index provided at the end of the appendixes in this manual.

Product Configuration

The following lists the product configuration of the Ethernet interface modules.

Model	Item name (*1)	Quantity
AJ71E71N3-T	AJ71E71N3-T Ethernet Interface Module	1
AJ71E71N-T	AJ71E71N-T Ethernet Interface Module	1
AJ71E71N-B5	AJ71E71N-B5 Ethernet Interface Module	1
AJ71E71N-B2	AJ71E71N-B2 Ethernet Interface Module	1
	F-type connector (A6RCON-F)	1
A1SJ71E71N3-T	A1SJ71E71N3-T Ethernet Interface Module	1
A1SJ71E71N-T	A1SJ71E71N-T Ethernet Interface Module	1
A1SJ71E71N-B5	A1SJ71E71N-B5 Ethernet Interface Module	1
A1SJ71E71N-B2	A1SJ71E71N-B2 Ethernet Interface Module	1
	F-type connector (A6RCON-F)	1

*1 For parts and materials not listed above, please contact the respective manufacturers or network vendors for purchase.

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COMMON SECTION

The common edition gives a summary of the functions and explains the features and system configuration, module specifications, and data exchange when exchanging data with the PLC CPU using a node external device via the Ethernet Interface Module.

Before using the Ethernet Interface Module, please read Chapters 1 through 5 once.

When booting up the system please follow the explanation in Chapter 4 to set the unit switch, connect with external devices, and check operations.

Abbreviated procedures for booting up the unit are given in Item 4.1.

1. GENERAL DESCRIPTION

This manual explains the specifications, functions and usage, handling, and remote node connection methods of the following Ethernet interface modules in order to send and receive data using the TCP/IP or UDP/IP communication protocol by connecting the A Series PLC with a computer via Ethernet. When applying the following program examples to the actual system, make sure to examine the applicability and confirm that it will not cause system control problems.

	Model	Product name (*1)	Interface (*2)		
			T	B5	B2
1	AJ71E71N3-T	AJ71E71N3-T Ethernet Interface Module	○	—	—
2	AJ71E71N-T	AJ71E71N-T Ethernet Interface Module	○	—	—
3	AJ71E71N-B5	AJ71E71N-B5 Ethernet Interface Module	—	○	—
4	AJ71E71N-B2	AJ71E71N-B2 Ethernet Interface Module	—	—	○
5	A1SJ71E71N3-T	A1SJ71E71N3-T Ethernet Interface Module	○	—	—
6	A1SJ71E71N-T	A1SJ71E71N-T Ethernet Interface Module	○	—	—
7	A1SJ71E71N-B5	A1SJ71E71N-B5 Ethernet Interface Module	—	○	—
8	A1SJ71E71N-B2	A1SJ71E71N-B2 Ethernet Interface Module	—	—	○

*1 For the package contents of each product, refer to the product configuration (previous page in the Common Edition).

*2 Indicates the communication interface of the applicable module (○: available, —: not available).

T: 10BASE-T, B5: 10BASE5, B2: 10BASE2

Including these modules in Ethernet makes it possible to exchange data between the A series PLC and the computer and between QnA and A-series PLC.

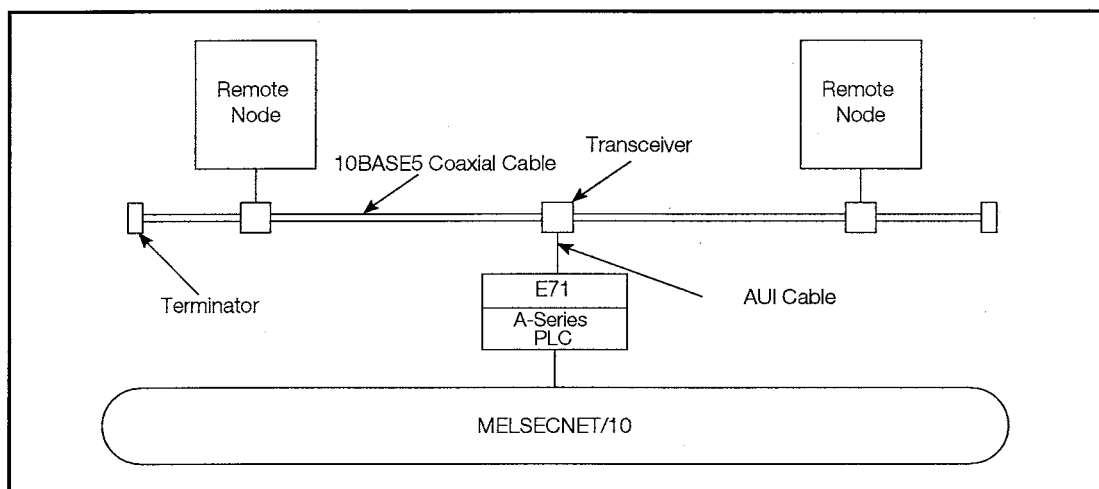


Fig 1.1 Connection Diagram Using 10BASE5 (Ethernet)

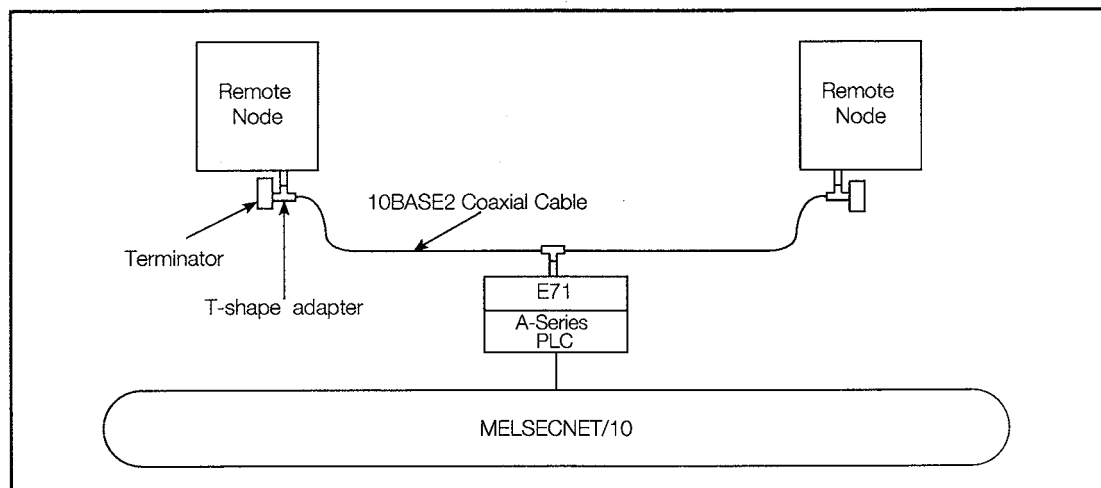


Fig 1.2 Connection Diagram Using 10BASE2 (Cheapernet)

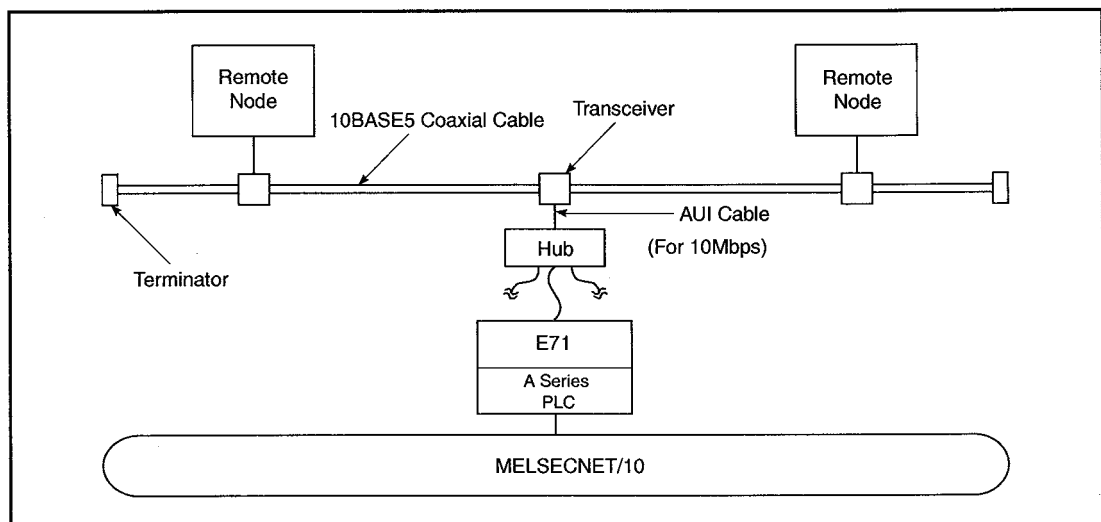


Fig 1.3 Connection Diagram Using 10BASE-T

1.1 Software Configuration

E71 supports the TCP/IP and the UDP/IP protocols.

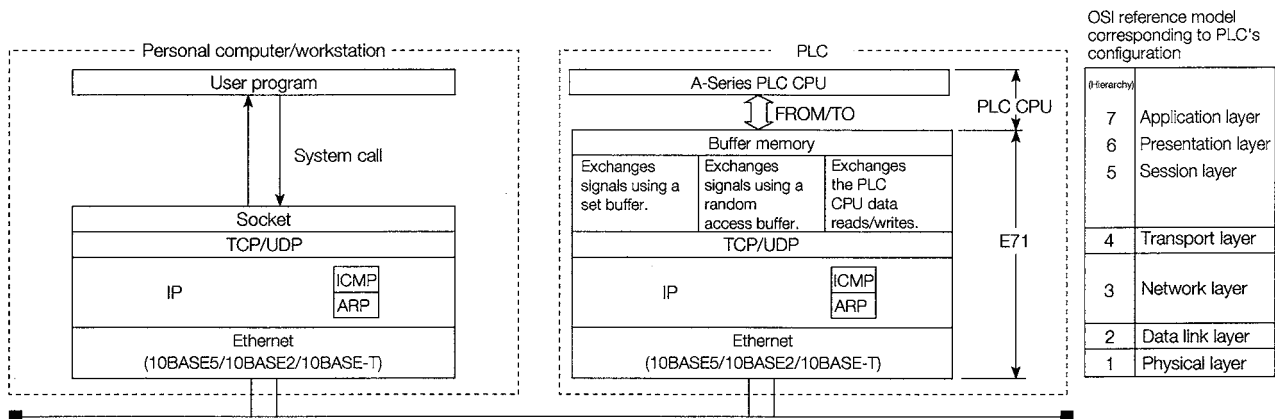


Fig 1.4 Software Configuration Diagram

1 TCP (Transmission control protocol)

This protocol retains the data reliability and correctness for the TCP protocol level.

- Establishing a connection creates a theoretical connection as if a special line were created between the nodes.
- A maximum of 8 connections can be established at the same time and communication to multiple buffers can be done at the same time.
- Data reliability is maintained by using a check sum for PLC control and data read transmit functions using the PLC No.
- The communication data flow can be controlled using window operations.
- Supports the Maximum Segment option.

The Maximum Segment option is enabled during TCP retransmission.

It can also be enabled during TCP transmission.

The data reception side must check the length of received data before processing the message.

2 UDP (User datagram protocol)

This protocol retains the data reliability and correctness on the UDP protocol level.

However if the data does not reach the target node it will not be retransmitted.

- Because it is connectionless, high speed transmission is possible.
- A check sum is used to increase the reliability of the transmitted data. However when greater reliability must be maintained, use a user application or TCP.

3**IP (Internet protocol)**

- Data transmissions can be sent and received using the datagram format.
- The transmitted data can be divided and reassembled.
- Routing options are not supported.

4**ARP (Address resolution protocol)**

- A broadcast is used to find the Ethernet physical address from the IP address.

5**ICMP (Internet control message protocol)**

- Has a function to transmit IP error messages.
- Please refer to the attachment for information regarding the ICMP option support type (ICMP protocol).

1.2 Features

E71 is a unit used to connect the A-series PLC to the Ethernet.

By combining a A-series PLC in the Ethernet it is possible to construct a network system that combines data link system/network with the Ethernet.

It is possible to conduct fixed buffer communication with a remote node and to read and write data from the random access buffer exchange area from the PLC CPU.

Fixed buffer exchange using TCP/IP or UDP/IP, random access buffer exchange, and reading and writing data inside the PLC CPU (general data exchange) from a remote node is possible.

The main features of the E71 are explained below.

1

Selecting the exchange format and exchange node units is possible (see Chapter 5 for a detailed explanation)

- ① Whether to use the TCP/IP or the UDP/IP communication protocols can be selected for each remote node that exchanges data, and the communication line for the target remote node can be set to open (communication line connect).
- ② Eight communication lines can be open at the same time and data can be exchanged with multiple remote nodes.
- ③ The relationship between the E71 data exchange and the selectable exchange formats is shown below.

Exchange Function		TCP/IP	UDP/IP
Exchanging using a fixed buffer	With procedures	○	○
	Without procedures	○	○
Exchange using a random access buffer		○	○
Reading/writing data inside the PLC CPU using requests from a remote node (General data exchange)		○	○

2**Data exchange while the PLC CPU is stopped is possible (Detailed explanation in Item 5.6).**

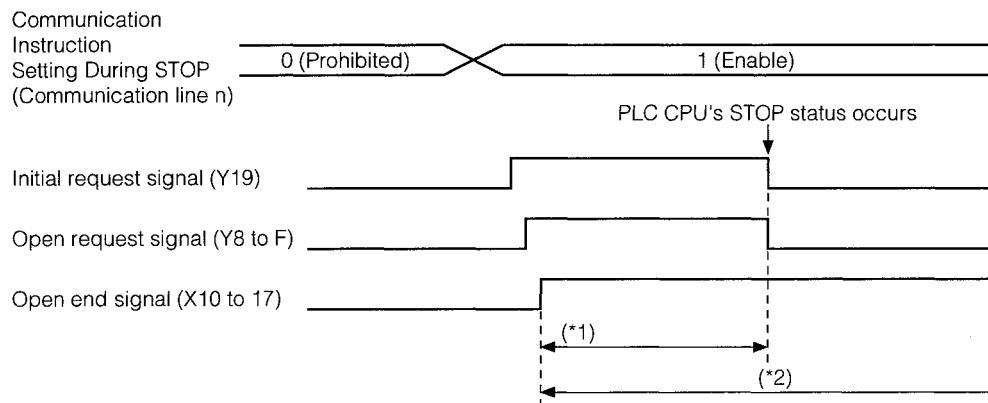
- ① When conducting the following data exchange, the data exchange with remote nodes can be continued even if the PLC CPU installed in the E71 is stopped after the communication line is opened by the PLC program.

(Function that makes it possible to continue exchange when in STOP status)

- Exchange using random access buffer
- Writing/reading data inside the PLC CPU with a request from a remote node (general data exchange)

* In either case exchange can be continued using the communication protocol at the time the communication line is opened.

- ② Data exchange while the PLC CPU is stopped is conducted by setting the buffer memory's Exchange Instruction Area During STOP (address: 496) to enable. (Set for each communication line.)



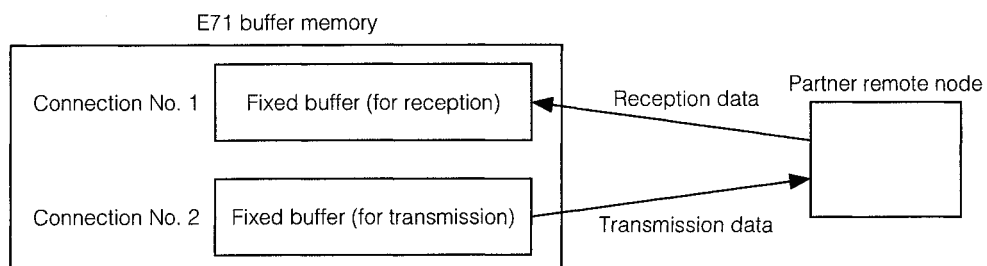
*1 Exchange possible range when the exchange instruction during STOP is set to prohibit.

*2 Exchange possible range when the exchange instruction during STOP is set to enable.

3**Data exchange on a 1:1 or 1:n with each remote node (exchange using the fixed buffer)**

- (a) This conducts data exchange between the remote node and the PLC CPU on a 1:1 (TCP/UDP) or a 1:n (UDP only) basis using the E71's fixed buffer.
- (b) The E71 has 8 fixed buffers with a memory capacity of 1k words, and the partner remote node with which exchange will be done, application (transmission/reception), and the protocol to be used (TCP/UDP) can be set for each fixed buffer. (Exchange between an E71 and another E71 is possible.) When conducting transmission and reception with the same remote node, 2 fixed buffers are required.
- (c) Setting pairing open using communication line open processing creates a pair containing a reception fixed buffer and a transmission fixed buffer and connects the partner remote node with 1 port through which data can be exchange. (Detailed explanation in Item 5.4.4)

(Example)



- (d) When exchanging with fixed buffers, exchange can be done using either the E71 procedure (with procedure) or without procedures.

(Please refer to Item 3.3 for details regarding the amount of data that can be transmitted at one time.)

- ① Exchanging when there is with procedure (Refer to Chapter 6 for details.)

The E71 protocol transmits and receives data on a 1:1 basis using a handshake between the specified node and the PLC CPU.

Use when transmitting or receiving simple data from the PLC program.

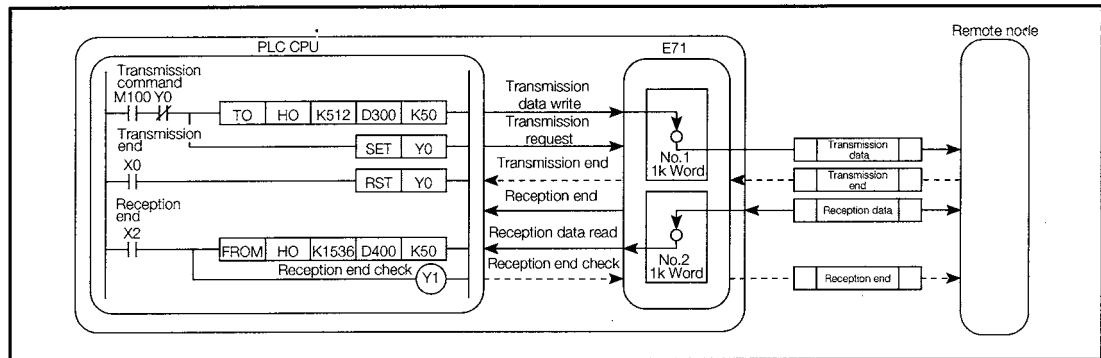


Fig 1.5 Fixed Buffer Exchange (With Procedure)

- When transmitting data

After writing the transmission data in the fixed buffer, the transmission request signal will turn on and data of the specified number of words will be transmitted.

When a transmission end response is received from the remote node the transmission end signal will turn on.

- When receiving data

When the data of the specified number of words is stored in the reception fixed buffer, the reception end signal will turn on.

When the reception data is read from the fixed buffer and the reception end check signal is turned on, the reception end check response will be sent and the reception end signal will be turned off.

② When exchanging without procedure (Detailed explanation in Chapter 7)

Data transmission is conducted when the specific node and the PLC CPU are 1:1 or 1:n by simultaneous broadcast communication (Simultaneous broadcast communication function, refer to 4).

Used to transmit fixed buffer data or to put reception data in the fixed buffer the way it is received.

Because it is without procedure the handshake with the remote node must be done by the sequence program.

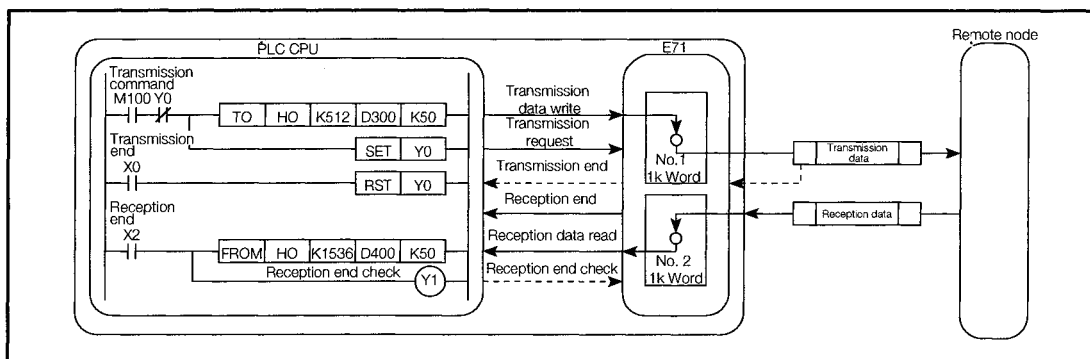


Fig 1.6 Fixed Buffer Exchange (Without Procedure)

- When transmitting data

After the transmitted data is written in the fixed buffer and the transmission request signal is turned on, the data of the specified number of words is transmitted and the transmission end signal is turned on.

The transmission end response does not wait for a reception signal.

- When receiving data

When the data of the specified number of words is received and stored in the fixed buffer, the reception end signal is turned on.

When the reception data is read from the fixed buffer and the reception end check signal is turned on, the reception end signal turns off.

The reception end response does not wait for a transmission signal.

4

Simultaneous broadcast communication (Details explained in Item 7.3)

User for simultaneous broadcast to all remote nodes on the same Ethernet that is connected to the E71 using the UDP/IP functions, refer to fixed buffer exchange without procedure. This makes it possible to write the same data. However, the remote node must be performed read and delete processing when received message is not required by this simultaneous broadcast communication.

5

Data exchange from a read/write request from a remote node (Random access buffer exchange)

Use when processing the maximum 6k word data quantity with the sequence program and when processing transmission and reception data when the sequence program and the remote node are non synchronous (Detailed explanation in Chapter 8).

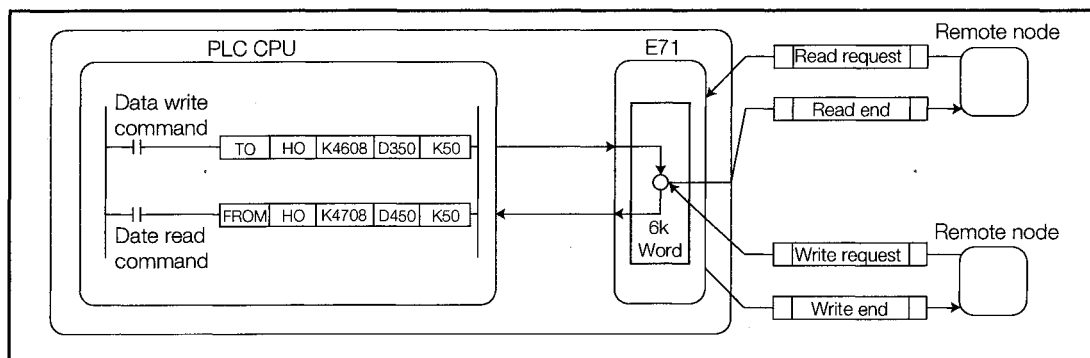


Fig 1.7 Random Access Buffer Exchange

(a) The random access buffer can read and write the same address to multiple nodes. However, exchange between the PLC CPU and the remote node is non synchronous so the user must conduct interlock processing.

(b) The random access buffer holds 6k words (3k for channel 0 and 3k for channel 1).

The area is not set for each connection as for fixed buffer exchange.

(c) The PLC CPU reads and writes data to and from the random access buffer by switching channels in the 3k word unit.

However, communicating nodes use this buffer area as one continuous area of 6k words.

(For details refer to Item 3.3)

(d) The PLC CPU processing when data is transmitted and received is as follows.

- When transmitting data

The transmitted data can be written into any area of the random access buffer.

When a read request is received from a remote node the data written in the specified area of the random access buffer and an end response is transmitted.

- When receiving data

When a write request is received from a remote node the received data is stored in the specified area of the random access buffer and a write end response is transmitted.

The received data is read from the random access buffer.

(e) Writing to and reading from the random access buffer from a remote node can be freely done between nodes set in the E71 parameter.

For this reason, the random access buffer can be used to store common data and to receive and relay data between remote nodes (There is no need to use the PLC CPU memory).

6

Reading and writing inside the PLC CPU via a request from a remote node (general data exchange : detailed explanation in Chapters 9 and 10)

Use to read and write remote station PLC data via local station PLC installed in the E71 and data link systems/network systems from the remote node with MITSUBISHI MELSEC communication support tool (*1) inserted. In addition, the PLC CPU state can be controlled from a remote node using remote RUN/STOP, etc.

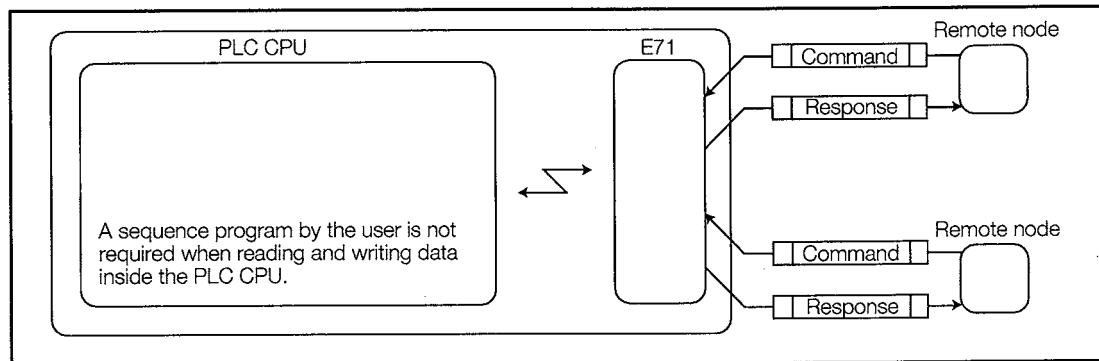


Fig 1.8 Reading and Writing the PLC CPU

- When a remote node transmits a request for a read/write of data inside the PLC CPU to the E71, the data in all devices, programs, commands, and parameters is transmitted to or received from the E71.
- When the PLC CPU installed in the E71 is connected to a data link system or a network system, data can be exchanged to and from the remote PLC CPU. (For details refer to Item 9.1.)
- Because all data exchange is conducted between the E71 and the remote node, data exchange can be conducted by having the sequence program only conduct initial processing and communication line open processing.

It is not necessary to create a special sequence program to exchange data.

*1 Communication programs in the personal computers to be connected to Ethernet or computer link can be simplified by using the following communication support tools manufactured by Mitsubishi Electric, which support communication between MELSEC-A or QnA series PLC and personal computers.

- MX Component (SW0D5C-ACT-E or later)

The overview and application examples of basic communication support tools are shown in Section 10 in the Appendix. Refer to it as needed.

7

Selecting the exchanged data's data code (Details explained in Item 3.3)

Use the following functions to select an exchange data code (ASCII code/binary code) that matches that of the remote node when exchanging data between the E71 and the remote node.

- Fixed buffer exchange buffer exchange with procedure
- Random access buffer exchange
- Reading/writing data from the PLC CPU using a request from a remote node

Code conversion during exchange is conducted by the E71 and all data received between the E71 and PLC CPU is in binary code.

For this reason, a sequence program for code conversion is not required. However, the selection of exchange data codes is done on a unit basis so selection for each port cannot be done.

8 Router relay function (Details explained in Chapter 12)

Used when exchanging by relaying the router.

This function does not operate as a router but is a function to make exchange via routers and gateways.

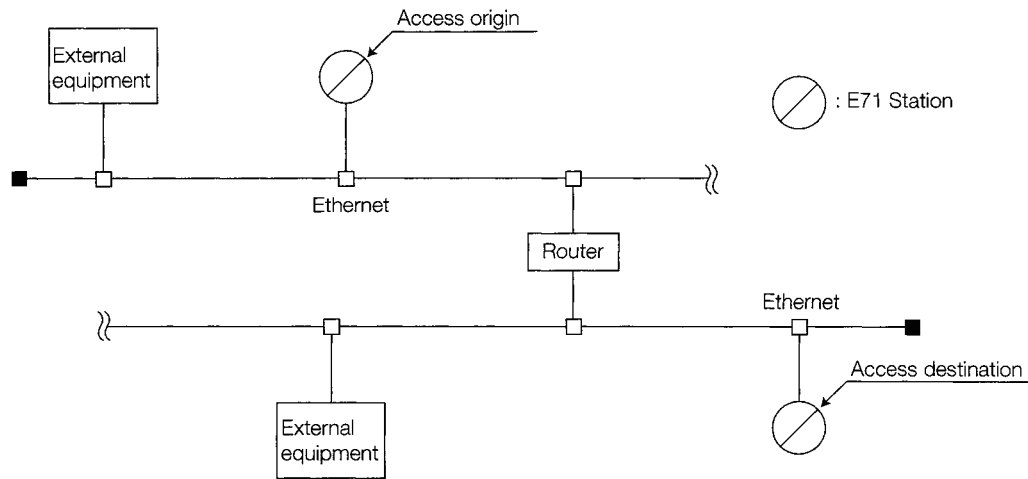


Fig 1.9 Router Relay Function

9 Other remote node existence check function (Details explained in Item 5.3.1)

Used to have the E71 regularly check if the other remote node for which the connection was made exists.

When exchange has not been conducted with the other node for a specified period of time the E71 checks whether the node is operating properly.

When the other node is not operating properly the E71 closes the line (connection forced disconnect).

10 Accessing a PLC CPU from GX Developer through Ethernet connection (Details explained in the GX Developer Manual)

Access can be made to other station's PLC CPU on the MELSECNET/10 or MELSECNET (II)/B via the ACPU of a station equipped with an E71 or via a station equipped with an E71 through Ethernet connection from GX Developer (*1).

*1 Indicates a personal computer in which the GX Developer software package (SW2D5C/F-GPPW-E or later) has been installed.

1.3 Comparison with Previous products

The differences in the specifications between the E71 and the conventional Ethernet interface modules are shown below.

When there is compatibility between the functions of these interface modules, a circle is placed in the applicable columns.

Data Exchange Functions			AJ71E71 (Previous products)	AJ71E71-S3 (Previous products)	E71 (This product)	Remarks
1	Communication protocol selection function for the partner remote node unit		○	○	○	_____
2	Fixed buffer exchange	With procedure	○	○	○	_____
		Without procedure	×	○	○	
3	Exchange using pairing open		×	○	○	For fixed buffer exchange
4	Simultaneous broadcast exchange		×	○	○	Exchange is possible using a fixed buffer without procedures (UDP/IP open is possible)
5	Random access buffer memory exchange		○	○	○	_____
6	Data read/write in the PLC CPU		○	○	○	General data exchange function
7	Exchange while the PLC CPU is stopped		×	○	○	Exchange is possible after the port is opened regardless of the PLC CPU's RUN/STOP status.
8	Exchange data code (ASCII code/binary code) selection		○	○	○	_____
9	Router relay exchange		×	○	○	Static router relay
10	Partner remote node existence check		×	○	○	_____
11	Timer setting value units for data exchange	500ms	×(*1)	○	○	Timer value units to be set during initial processing.
		2s	○ (Fixed)	○	○	
12	COM. ERR LED turned on/off notification		×	○	○	I/O signal with the PLC CPU (X1B)
13	Ethernet connection of GX Developer (SW2D5C/F-GPPW-E or later)		×	○	○	Connection via Ethernet

*1 When the module software version is before the Q version.

Data Exchange Functions			AJ71E71 (Previous products)	AJ71E71-S3 (Previous products)	E71 (This product)	Remarks
14	Ethernet connection of MX Component		○	○	○	There are some restrictions. For details, refer to the manual of each product.
15	Other station access from MELSOFT products	GX Developer	×	○	○	
		MX Component	○	○	○	
16	Connection line with remote nodes	10BASE2	○	○	○	Interfaces readily available in the module
		10BASE5	○	○	○	
		10BASE-T	×	×	○	

Remarks

- (1) The E71 (this product) has the same functions as the AJ71E71-S3 (previous product). However, the response speed is different between the E71 (this product) and the AJ71E71 and AJ71E71-S3 (previous products). For module compatibility and how to utilize the programs used for the previous products, refer to Appendix 1.
- (2) For how to utilize the programs on the remote node side that performs data exchange with a conventional Ethernet interface module in the data exchange with the E71 (this program), refer to Appendix 1.2.

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2. SYSTEM CONFIGURATION

This section explains the system configurations that are possible in combination with the E71.

2.1 Overall Configuration

Following shows a system configuration with an E71 PLC installed in the Ethernet.

Please refer to Item 2.3 for information regarding other arrangements that must be made by the user.

1

When connecting the PLC CPU with the Ethernet

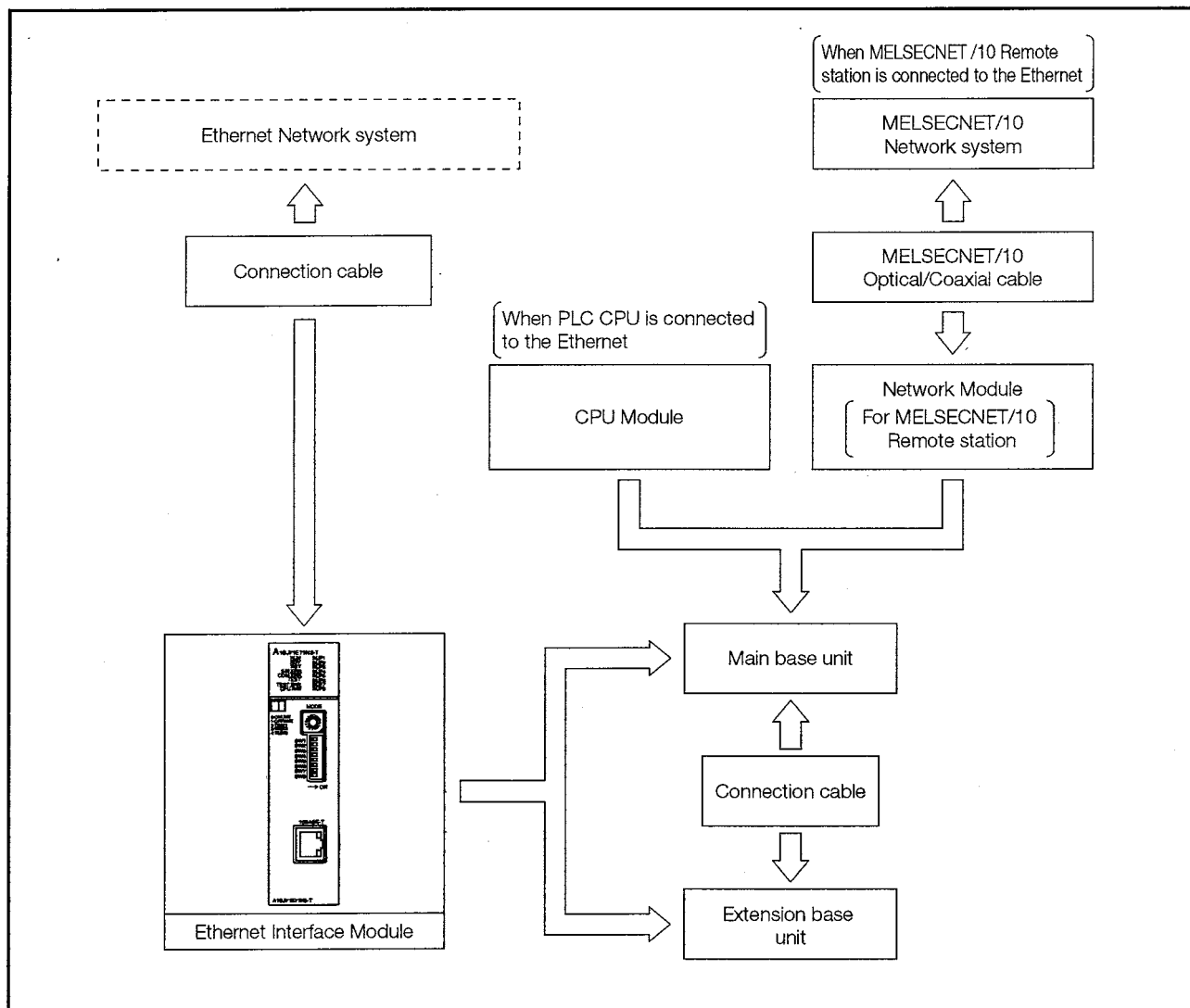


Fig 2.1 System Configuration Diagram

2.2 Supported Systems

The E71 can be used for the system described below.

1

Supported units and installable number of units

The following table shows the PLC modules that can be installed in the E71 and the number that can be installed.

E71 installation station	Application module	Number of modules that can be installed	Remarks
CPU module	A0J2H A1, A1N A1S(-S1), A1SJ A1SH, A1SJH A2(-S1) A2N(-S1) A2S(-S1) A2SH(-S1) A3, A3N	2	When using both the AnS series and A series special function modules GOT series shown below, the total number of modules that can be installed includes the number of these modules used and connected. · A1SJ71UC24-R2 · A1SJ71C24-R2 · A1SJ71UC24-R4 · A1SJ71C24-R4 · A1SJ71UC24-PRF · A1SJ71C24-PRF · A1SD51S · A1SJ71E71N3-T · A1SJ71E71N-T/B5/B2/B5T · A1SJ71E71-B2/B5-S3 · A1SJ71E71-B2/B5 · A1SJ61BT11: Intelligent mode only · AD51(-S3) · AD51H(-S3) · AD51FD(-S3) · AD57G(-S3) · AJ71C21(-S1) For only the BASIC program · AJ71C23(-S3) · AJ71UC24 · AJ71C24(-S3/-S6/S8) · AJ71P41 · AJ71E71N3-T · AJ71E71N-T/B5/B2/B5T · AJ71E71-S3 · AJ71E71 · AJ61BT11: Intelligent mode only · A0J2C214-S1 · GOT series (Only when bus connection)
	A1SCPU-C24	1	
	A2A(-S1) A3A A2U(-S1) A3U A4U A2AS(-S1)	6	
	Q2AS(-S1) Q2ASH(-S1) Q2A(-S1) Q3A Q4A, Q4AR		
	Q02-A Q02H-A Q06H-A		
MELSECNET/10 (Remote station)	AJ72LP25(G) AJ72BR15 AJ72LR25 AJ72QLP25(G) AJ72QBR15 AJ72QLR25 A1SJ72QLP25 A1SJ72QBR15 A1SJ72QLR25	2	* When using a computer link module (AJ71UC24, etc.) as a multiple drop link module, it is not included in the above restrictions on the number of modules that can be installed. Multiple modules can be installed within the PLC CPU's I/O points. * When using a CC-Link system master/local module (AJ61BT11, etc.) in the I/O mode, it is not included in the above restrictions on the number of modules that can be installed. Multiple modules can be installed within the PLC CPU's I/O points.

2

Installable base units

Except where noted below the basic base unit and the extension base unit can be freely used in installation slots of the E71.

- (a) Installing the extension base unit (models A52B, and A55B) without a power unit could make the amount of power supply insufficient, so doing so should be avoided as far as possible. If this unit is installed be sure to give sufficient consideration to the current capacity of the main base unit's power unit, and the extension cable voltage drop when selecting the extension cable. (For details to the usable CPU unit's users manual) (Refer to (1).)
- (b) The E71 can be installed in the PLC CPU based unit and the MELSECNET/10 remote station. It cannot be installed in MELSECNET (II) and MELSECNET/B remote stations.

3 Accessible PLC

This shows the remote station PLC that can be accessed via an E71 installable station from a remote node. Each accessible CPU unit has a unit that includes MELSECNET link functions.

(Example) In the case of the A3ACPU, the A3ACPUP21 and A3ACPUR21 can be accessed.

① PLC CPU

PLC CPUs that can be accessed from remote nodes can also be accessed via data link systems and network systems. For access refer to Item 9 and Item 10.

PLC CPU	A0J2H	A1	A1N	A1S(S1)	A1SJ	A1SH	A1SJH
	A2(S1)	A2N(S1)	A2A(S1)	A2U(S1)	A2S(S1)	A2SH(S1)	A2AS(S1)
	A2C	A2CJ	A3	A3N	A3A	A3U	A4U
	Q2A(S1)	Q2AS(S1)	Q2ASH(S1)	Q3A	Q4A	Q4AR	

② Remote station

Shows the remote stations that can be accessed from a remote node via the data link system and network system. The buffer memories of the special functions units of the remote stations that are connected by the link units that are named below can be accessed.

MELSECNET/10	AJ72QLP25(G)	AJ72QBR15	AJ72QLR25	A1SJ72QLP25	
	A1SJ72QBR15	A1SJ72QLR25	AJ72LP25(G)	AJ72BR15	AJ72LR25
MELSECNET (II)	AJ72P25	AJ72R25			
MELSECNET/B	AJ72T25B	A1SJ72T25B			

4 Applicable software packages

The following table lists the software packages that support the E71.

(a) Communication support tools for remote nodes

Item name	Model	Remark
MX Component	SWnD5C-ACT-E	Active X Control library. The "n" in the model name is 0 or greater. (*1)

*1 Depending on the version of MX Component used, different versions of E71 are supported.
Refer to the manual of MX Component for the details.

Remarks

Access can be made to other station's PLC CPU on the MELSECNET/10 or MELSECNET (II)/B via the ACPUR of a station equipped with an E71 or via a station equipped with an E71 through Ethernet connection from GX Developer (SW2D5C/F-GPPW-E or later).

2.3 Devices Required for Network Configuration

1

The device shown in Figure 2.2 are required when connecting to 10BASE5. The user will please make the arrangements.

- (a) Only use 10BASE5 coaxial cable, N-type connectors, N-type terminators, transceivers, AUI cable (transceiver cable) that meet Ethernet standards. Please use transceivers that have signals that are generally called SQETEST or Heartbeat (transceiver function that uses a signal to check if the transceiver is operating correctly after transmission).

(SQETEST: Signal Quality Error TEST)

SQETEST = ON, or Heartbeat signal = valid

10BASE5																																					
Transmission medium	Coaxial cable (Ethernet standard cable) 50 Ω																																				
AUI cable (Transceiver cable)	Twisted pair cable with 15 pin D connector *E71s 10BASE5 connection connector layout <table><tr><td>Pin No</td><td>Signal Name</td><td>Pin No</td><td>Signal Name</td></tr><tr><td>1</td><td>FG</td><td>9</td><td>Collision detection (-)</td></tr><tr><td>2</td><td>Collision detection (+)</td><td>10</td><td>Transmission (-)</td></tr><tr><td>3</td><td>Transmission (+)</td><td>11</td><td>N.C.</td></tr><tr><td>4</td><td>N.C.</td><td>12</td><td>Reception (-)</td></tr><tr><td>5</td><td>Reception (+)</td><td>13</td><td>+12V</td></tr><tr><td>6</td><td>12G</td><td>14</td><td>N.C.</td></tr><tr><td>7</td><td>N.C.</td><td>15</td><td>N.C.</td></tr><tr><td>8</td><td>N.C.</td><td>Shell</td><td>FG</td></tr></table>	Pin No	Signal Name	Pin No	Signal Name	1	FG	9	Collision detection (-)	2	Collision detection (+)	10	Transmission (-)	3	Transmission (+)	11	N.C.	4	N.C.	12	Reception (-)	5	Reception (+)	13	+12V	6	12G	14	N.C.	7	N.C.	15	N.C.	8	N.C.	Shell	FG
Pin No	Signal Name	Pin No	Signal Name																																		
1	FG	9	Collision detection (-)																																		
2	Collision detection (+)	10	Transmission (-)																																		
3	Transmission (+)	11	N.C.																																		
4	N.C.	12	Reception (-)																																		
5	Reception (+)	13	+12V																																		
6	12G	14	N.C.																																		
7	N.C.	15	N.C.																																		
8	N.C.	Shell	FG																																		

- (b) Please use cable that meets the transceiver and AUI cable specifications for the transceiver supply power.

Use the AJ71E71N-B5 in consideration of the voltage drop (maximum 0.8V) of the AJ71E71N-B5 module.

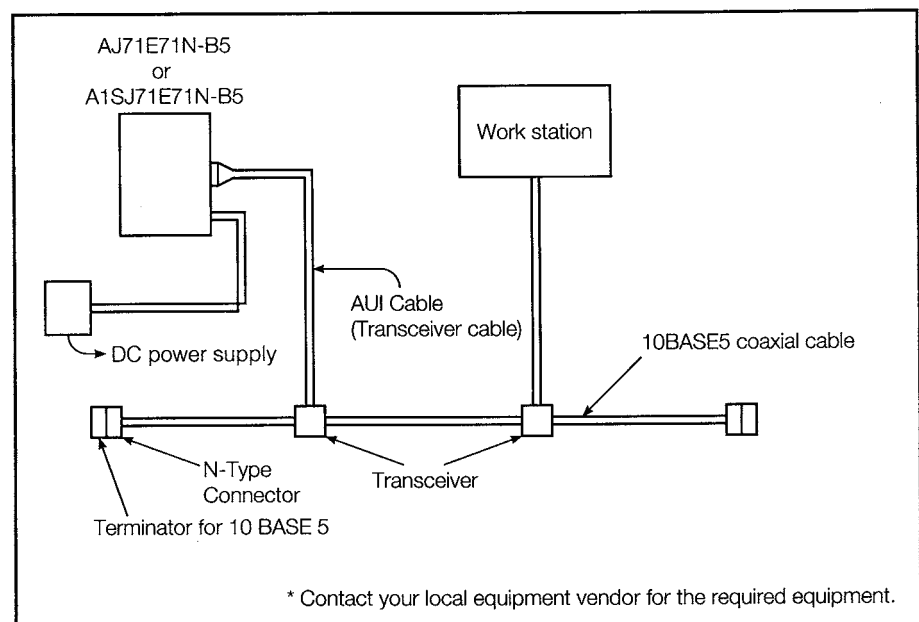


Fig 2.2 Example Network System Configuration

Remarks

The transceiver power characteristics are

- Input terminal voltage 12V^{-6%} to 15V^{+5%}
- AUI cable direct current resistance 40 Ω/km or under, maximum length 50m(164.04 ft.)
- Maximum current consumption 500mA or less

So, the transceiver supply power scale is 13.28V to 15.75V.

Since the 0.8V voltage drop of the module is considered for the AJ71E71N-B5, the guideline of the transceiver supply power is 14.08V to 15.75V.

* Calculating the voltage drop (V) of the transceiver supply voltage

Voltage drop (V) = AUI cable direct current resistance (Ω/m) × AUI cable length (m) × 2 (both directions) × transceiver consumption current (A) + AJ71E71N-B5 main body voltage drop (V)

(Example) When using the A1SJ71E71N-B5

$$2.0 \text{ (V)} = 0.04 \text{ (}\Omega/\text{m)} \times 50 \text{ (m)} \times 2 \times 0.5 \text{ (A)}$$

In this case, the target value of the transceiver supply power will be larger than 13.28 V.

$$13.28 \text{ (V)} = 12 \text{ V}^{6\%} (11.28 \text{ V}) + 2.0 \text{ (V)}$$

2

The device shown in Fig 2.3 are required when connecting to 10BASE2. The user will please arrange other than the F-type connector.

(a) 10BASE2 coaxial cable

	10BASE2
Transmission medium	Coaxial cable 50 Ω RG58A/U or RG58C/U

(b) F-type connector (for connecting to the E71, included in the packaging)

A6RCON-F suitable products

(c) Terminator for 10BASE2 (BNC plug terminator for 10BASE2)

221629-4 (Tyco Electronics AMP K. K.) or equivalent product.

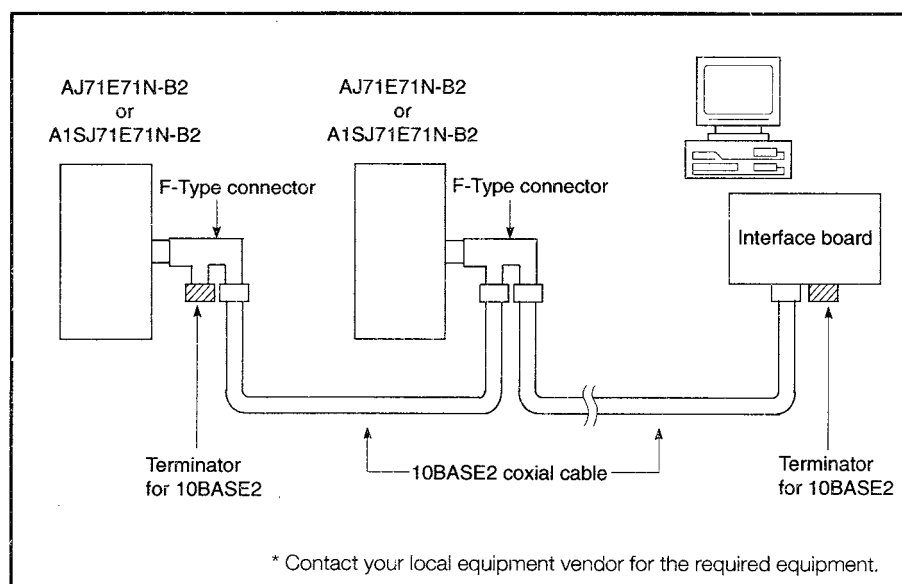


Fig 2.3 Example System Configuration

- 3** The devices shown in Figure 2.4 are required when connecting to the 10BASE-T. The user will please arrange the required devices in advance.
- (a) Use devices that meet the standard of the 10BASE-T communication interface in IEEE802.3.
 - (b) Hub
 - Use a hub that supports 10Mbps.
 - (c) Twisted pair cables
 - ① Unshielded twisted pair cable (UTP) or shielded twisted pair cable (STP) category 3, 4, 5
 - * Straight cables can be used.
 - (Correct operation is not guaranteed if a crossed cable is used to connect to an external device via the 10BASE-T of the E71. However, it is possible to use crossed cables to connect to a E71 for data communication or to connect to a GOT.)
 - ② RJ45

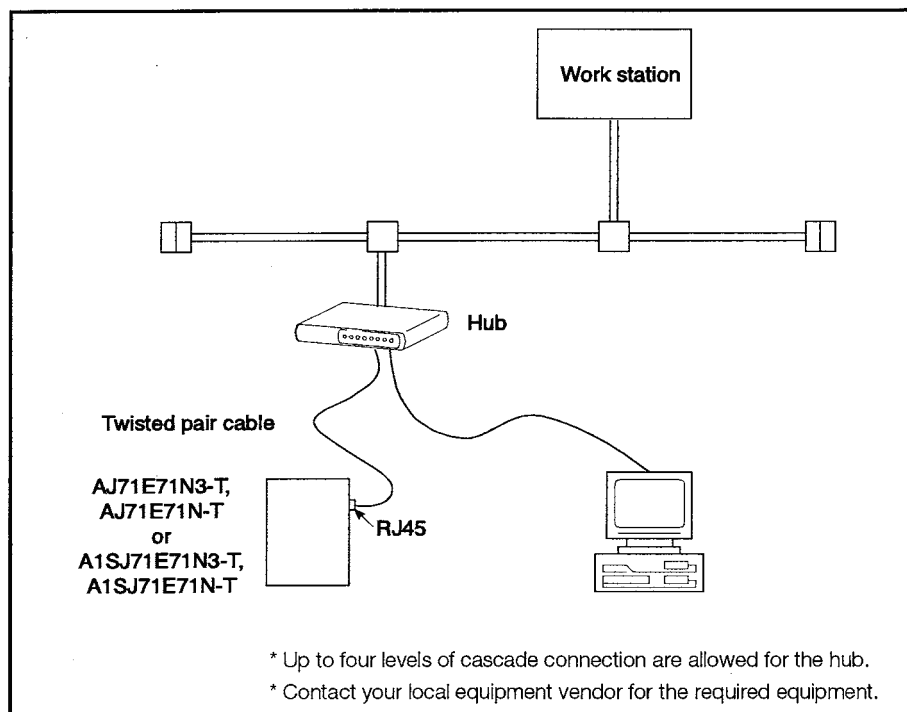


Fig 2.4 Example System Configuration

2.4 Precautions for System Configuration

This section describes the precautions for the system configuration using the E71.

2.4.1 Precautions for the Selection of the E71

1

Communication frames when sending/receiving data to/from a remote node

- (a) The E71 can communicate with a remote node that handles frames of which the Ethernet header for the data link layer is in accordance with the Ethernet standard.

It cannot communicate with a remote node that handles frames of which the Ethernet header is in accordance with the IEEE802.3 standard.

This restriction is due to the differences in the types and lengths used in these Ethernet headers. (Refer to Item 8 in Appendix.)

- (b) As one method to confirm whether communication can be performed with the E71 from a remote node, send the "Ping command" to a E71 in which initial processing has normally been completed. If a reply is returned from the E71 to the remote node, it means that the communication frame is compatible.

*The status of the E71 in which initial processing has normally been completed:
The RDY LED flushes and X19 is ON.

*"Ping command": ICMP's echo request/echo reply function

2

Communication with remote nodes

- (a) The E71 can send/receive data to/from a remote node that can support the TCP/IP or UDP/IP communication protocol.
- (b) A node, which is performing a normal communication using the "Ping command" mentioned above via the TCP/IP or UDP/IP communication protocol, can communicate with the E71.

2.4.2 Precautions for the System Design

1**Installation of equipment**

- (a) The installation distances between devices and the segment lengths must be within the specification value ranges.
- (b) For the installation of the 10BASE5, 10BASE2 and 10BASE-T, we recommend that each customer contact a professional contractor/cable manufacturer.

2**Separating the communication lines**

To prevent communication problems that may be caused by excessive messages being transmitted over the communication line (congestion), do not allow unwanted messages to be transmitted over the communication line as much as possible.

Some examples of preventing communication line congestion are as follows:

- (a) Using a switching hub, gateway or router, separate the communication line for control and the communication line for transmission/reception of general data and office information.
- (b) Using a switching hub, gateway or router, separate the communication line to which a personal computer is connected (segment) and the communication line to which a computer for process control and a E71 are connected (segment), so that no data other than communication messages between connected devices can be transmitted over other lines (segments).

3**Checking normal operation in the communication counterpart's remote node**

The following examples show how to check the status on the remote node side to prevent communication from being disabled due to the occurrence of a sudden problem (down) on the remote node side with which communication is being performed in the communication between devices.

- (a) Using the E71's existence check function

This function sends the "Ping command" from the E71.

For more details, refer to *2 at the end of Item 5.3.1.

- (b) Duplex connection

By providing a separate connection for checking the status of the remote node side, the status of the local station can be periodically notified to the remote node side. (The status information of the self node is exchanged using a separate connection.)

* In this status check, change all connections with the applicable device from "closed" to "open" when an abnormality is detected on the remote node side.

4**Restarting the communication system**

When designing a system, implement a feature to restart the communication system quickly with which a communication error can be notified to the maintenance personnel by the use of lamp display or buzzer sound in case the communication between devices is disabled due to a device failure or line problem.

This image shows a single sheet of white paper with horizontal ruling lines. The lines are evenly spaced and run across the width of the page. There are no margins, text, or other markings on the paper.

3. SPECIFICATIONS

This section explains the E71 general specifications, performance specifications, and transmission specifications.

3.1 General Specifications

This section explains the E71 general specifications.

Table 3.1 General Specifications

Item	Specification					
Ambient operating temperature	0 to 55°C					
Ambient storage temperature	-20 to 75°C					
Ambient operating humidity	10 to 90%RH, Non-condensing					
Ambient storage humidity	10 to 90%RH, Non-condensing					
Vibration resistance	Conforming to JIS B 3502, IEC 61131-2		Frequency	Acceleration	Amplitude	No. of sweeps
		Under intermittent vibration	10 to 57Hz	—	0.075mm (0.003inch)	10 times in each of directions X, Y, Z (for 80 min.)
			57 to 150Hz	9.8m/s ²	—	
		Under continuous vibration	10 to 57Hz	—	0.035mm (0.001inch)	
			57 to 150Hz	4.9m/s ²	—	
Shock resistance	Conforming to JIS B 3502, IEC 61131-2 (147m/s ² , 3 times in each of 3 directions X Y Z)					
Operating ambience	No corrosive gases					
Operating elevation*3	2000m (6562 feet) max.					
Installation location	Control panel					
Over voltage category *1	II max.					
Pollution level *2	2 max.					

*1 This indicates the section of the power supply to which the equipment is assumed to be connected between the public electrical power distribution network and the machinery within the premises. Category II applies to equipment for which electrical power is supplied from fixed facilities. The surge voltage withstand level for up to the rated voltage of 300V is 2500V.

*2 This index indicates the degree to which conductive material is generated in terms of the environment in which the equipment is used. Pollution level 2 is when only non-conductive pollution occurs. A temporary conductivity caused by condensation must be expected occasionally.

*3 Do not use or store the PC in the environment where the pressure is higher than the atmospheric pressure at sea level. Otherwise, malfunction may result. To use the PC in high-pressure environment, contact your nearest Mitsubishi representative.

3.2 Performance Specifications

This section explains the E71 performance specifications.

Table 3.2 Performance Specifications

Item		Specifications			
		AJ71E71N3-T A1SJ71E71N3-T	AJ71E71N-T A1SJ71E71N-T	AJ71E71N-B5 A1SJ71E71N-B5	AJ71E71N-B2 A1SJ71E71N-B2
		10BASE-T		10BASE5	10BASE2
Transmission specifications	Data transmission speed	10Mbps			
	Communication mode	Half-duplex			
	Transmission method	Base band			
	Maximum distance between nodes	—		2500 m (8202.10 ft.)	925 m (3034.77 ft.)
	Maximum segment length	100 m (328.08 ft.) (*1)		500 m (1640.42 ft.)	185 m (606.96 ft.)
	Maximum number of nodes/connection	Cascade connection, up to 4 stages		100 units/segment	30 units/segment
	Minimum node interval	—		2.5 m (8.20 ft.)	0.5 m (1.64 ft.)
Transmission and reception data storage memory	Number of allowable simultaneously open connections	8 connections			
	Fixed buffer	1K words × 8			
	Random access buffer	3K words × 2			
Number of remote nodes that can be communicated in a single initial processing		No restrictions			
Number of occupied I/O points		32 points/1 slot (I/O assignment: special 32 points)			
5 VDC internal current consumption		AJ71E71N3-T : 0.69A A1SJ71E71N3-T: 0.69A	AJ71E71N-T : 0.55A A1SJ71E71N-T: 0.56A	AJ71E71N-B5 : 0.55A A1SJ71E71N-B5: 0.57A	AJ71E71N-B2 : 0.67A A1SJ71E71N-B2: 0.66A
Connector		Modular jack (RJ45)		D-sub connector (male 15 pins)	BNC connector
Connection cable		Unshielded twisted pair cable (UTP), or shielded twisted pair cable (STP) rated in category 3, 4 or 5	Unshielded twisted pair cable (UTP) rated in category 3, 4 or 5	AUI cable (twisted pair cable)	Coaxial cable (RG58A/U or RG58C/U)
12 VDC external power supply capacity (for transceiver)		—		(*2)	—
External dimensions		AJ71E71N3-T, AJ71E71N-T, AJ71E71N-B5, AJ71E71N-B2 : 250 (9.84) (H) × 37.5 (1.48) (W) × 106 (4.17) (D) [mm(in.)] A1SJ71E71N3-T, A1SJ71E71N-T, A1SJ71E71N-B5, A1SJ71E71N-B2 : 130 (5.12) (H) × 34.5 (1.36) (W) × 94 (3.71) (D) [mm(in.)] •All do not include the protruded section on the front surface.			
Weight		AJ71E71N3-T : 0.30 kg (0.66lb.) A1SJ71E71N3-T: 0.17 kg (0.37lb.)	AJ71E71N-T : 0.30 kg (0.66lb.) A1SJ71E71N-T: 0.17 kg (0.37lb.)	AJ71E71N-B5 : 0.33kg (0.73lb.) A1SJ71E71N-B5 : 0.20kg (0.44lb.) (*3)	AJ71E71N-B2 : 0.35kg (0.77lb.) A1SJ71E71N-B2 : 0.21kg (0.46lb.) (*3)

*1 The length between the hub and a node.

*2 It is necessary to use a power supply that meets the specifications of both the transceiver and the AUI cable (refer to Item 2.3).

Since the 0.8V voltage drop of the module is considered for the AJ71E71N-B5, the guideline of the transceiver supply power is 14.08V to 15.75V.

*3 When the hardware version is "A", the weight is as follows:

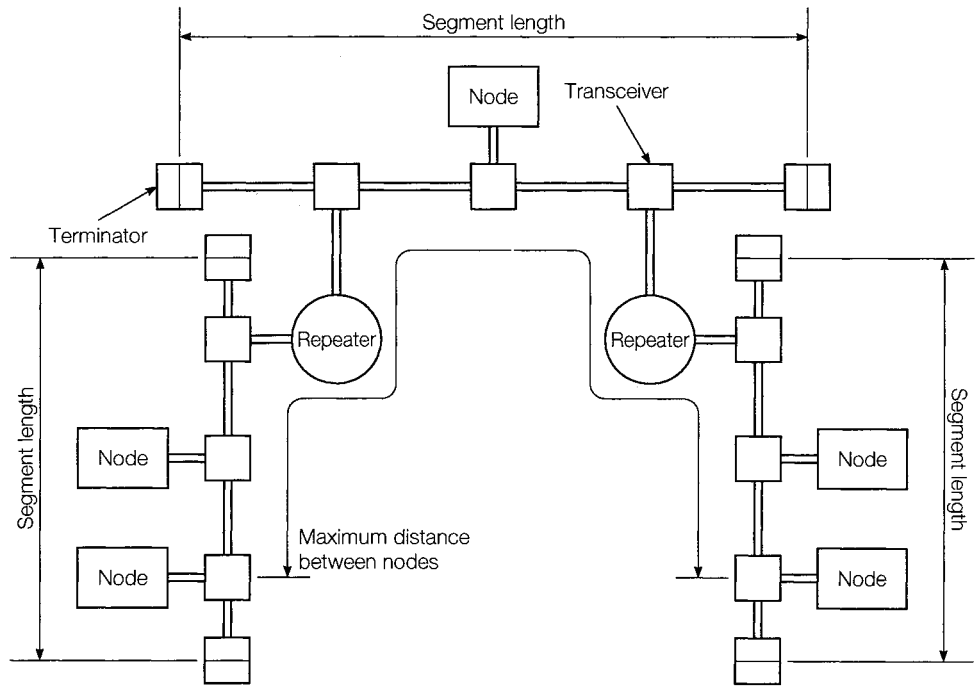
A1SJ71E71N-B5: 0.19kg (0.42lb.)

A1SJ71E71N-B2: 0.20kg (0.44lb.)

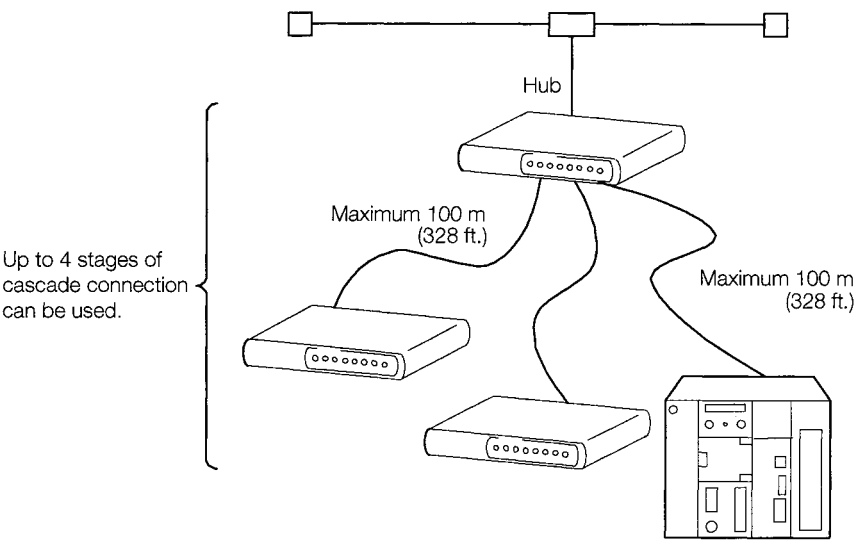
Remarks

The maximum distance between nodes and the segment legs are shown in the following diagram.

(a) 10BASE5, 10BASE2



(b) 10BASE-T



3.3 Data Codes during Communication and Exchangeable Data Amount

This section explains the data codes used when exchanging between the E71 and remote node or PLC CPU.

1 The data codes used during exchange are given below.

① Between E71 and the remote node

The data exchange function makes it possible to conduct exchange by selecting either binary code or ASCII code as shown in the table below.

Switching between binary code and ASCII code is done using the exchange condition setting switch (SW2 : Data code setting) on the E71.

(For details refer to Item 4.3.2)

○ : Selection possible
× : Not possible

Table 3.3 Codes that can be Selected

Data Exchange Function		Binary Code	ASCII Code	Function Explanation Item
Fixed buffer exchange	With procedure	○	○	Chapter 6
	Without procedure	○	× (*1)	Chapter 7
Random access buffer exchange		○	○	Chapter 8
Reading/writing data in the PLC CPU (General data exchange)		○	○	Chapter 9 Chapter 10

*1 Communication can be performed using the binary codes shown in the figure below:

② Between E71 and PLC CPU

Communicated binary code.

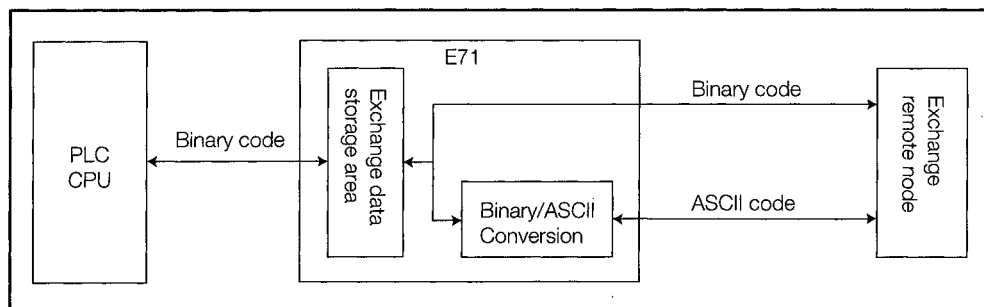
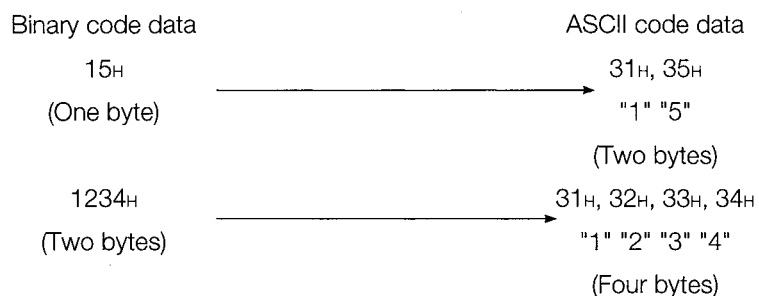


Fig 3.1 Exchange Data Code System

- 2** When exchanging using the ASCII code, the 1 byte binary code data is automatically converted into the 2 byte ASCII code.

Example:



- 3** The amount of data that can be exchanged at one time between the E71 and an external node depends on the function used and the data code setting (binary/ASCII) selected using the exchange condition setting switch (SW2: Data code setting) on the E71.

The following table shows the maximum amount of exchange data that can be sent at one time for each data exchange function.

Table 3.4 Exchangeable Data Amount

Data Exchange Function		Exchangeable Data Amount
Fixed buffer exchange	With procedure	1017 Words (Binary Code), 508 Words (ASCII Code)
	Without procedure	2046 Bytes
Random access buffer exchange		1017 Words (Binary Code), 508 Words (ASCII Code)
Reading and writing data in the PLC CPU (General data exchange)		The maximum number of operations that can be specified for each command/instruction : Maximum 256 points

3.4 Functions

3.4.1 List of Functions

Table 3.5 List of E71 Functions

Functions	Description of Functions	Exchange Partner Unit						
		Remote Node	E71	E71	E71	AJ71 E71	E71	QE71
		↓ E71	↓ Remote Node	↓ E71	↓ AJ71 E71	↓ E71	↓ QE71	↓ E71
Fixed buffer exchange	(1) Exchange between the PLC CPU and remote nodes in the Ethernet is done on a 1:1 basis. When With Procedure is used, exchange is conducted while a handshake is being done with the remote node. (2) When conducting exchange with a remote node, the fixed buffer (one area for each 1k word) has 8 areas (however, please refer to Item 3.3 regarding the amount of data that can be exchanged at one time.) (3) The exchange partner and usage application (transmission/reception) for the fixed buffer is set by the exchange parameters. (4) Exchange can be done with the connection and remote node that is in the Ethernet.	○	○	○	○	○	○	○
	With procedure							
Simultaneous broadcast communication	(1) By UDP/IP [fixed buffer exchange without procedure] function, simultaneous broadcast of the appropriate data to all remote nodes within the same Ethernet that E71 is installed can be executed. However, the remote node must be performed read and delete processing when received message is not required by this simultaneous broadcast communication. (2) Exchange can be done from remote nodes that are ending connection open processing in the Ethernet.	○	○	○	×	×	○	○
	Without procedure							
Random access buffer exchange	(1) Read and write exchange for the E71 random access buffer memory can be conducted from multiple nodes. (2) Random access buffer used to exchange with remote nodes is 6k words. A continuous area can be read/written from the remote nodes. (However, please refer to Item 3.3 regarding the amount of data that can be exchanged at one time.) (3) During random access buffer exchange, the random access buffer can be used as a common buffer memory within the network without specifying the memory area for each connection. (4) Exchange can be done with remote nodes that are ending the connection open processing in the Ethernet.	○	×	×	×	×	×	×
Reading and writing data in the PLC CPU	(1) The data in the PLC CPU such as that for each device, file data, and special function unit buffer memories, etc., are read and written in the PLC that is installed in the E71 using request from the remote node. (2) When the PLC installed in the E71 is connected to the MELSECNET, the exchange from remote node with the remote station PLC CPU can be done via the MELSECNET. (Please refer to Item 9.1 for details regarding data exchange with remote stations.) (3) Exchange can be done with remote node that is doing connection open processing end in the Ethernet. In addition, if the data exchange function is used, exchange can be done even if the local PLC CPU is stopped.	○	×	×	×	×	×	×

Functions	Description of Functions	Exchange Partner Unit						
		Remote Node ↓ E71	E71 ↓ Remote Node	E71 ↓ E71	E71 ↓ AJ71 E71	AJ71 E71 ↓ E71	E71 ↓ QE71	QE71 ↓ E71
Router relay exchange	(1) Exchanges data via the routers that are connected in the Ethernet network system. (Do not operate as a router.) (2) Exchange is possible via a router by data transmission after TCP's active open and UDP open. (3) Exchange can be done with the remote node that is ending the connection open processing in the Ethernet.	○	○	○	○	×	○	○
Existence check	(1) Checks if the partner node is operating correctly when exchange has not been done with the partner node for a specified period of time after connection open processing has ended. (2) Closes the line (connection forced disconnect) if the E71 exchange condition setting switch (SW1: Line processing selection during TCP time out error) is off when the partner node is not operating correctly.	○	○	○	○	×	○	○
Exchanged error storage	(1) Stores a maximum of 10 sets of error history information, such as message subheaders and partner node IP address, in the buffer memory when a data exchange error occurs. (2) This error history information makes it easy to analyze the cause of data exchange trouble.	/	/	/	/	/	/	/
Self wrap-up test	(1) Conducts a hardware check including E71 transmission and reception circuits.	/	/	/	/	/	/	/

○ : Executable × : Not executable

*1 Procedures must be created using the sequence program.

3.4.2 Relationship between Communication Remote Node and Added Functions for Each Communication Function

Shows what partner equipment can be exchanged with and what added functions can be used for each exchange function.

Table 3.6 Relation between Exchange Partner Nodes and Added Functions

Exchange Functions		Communication Format with Exchange Partner Nodes		Added Functions			
		TCP/IP	UDP/IP	Simultaneous Broadcast	Pairing Exchange (*1)	Existence check	Exchange when PLC CPU is stopped
Fixed buffer exchange	With procedure	○	○	×	○	○	×
	Without procedure	○	○	○ (*2)	○	○	×
Random access buffer exchange		○	○	×	—	○	○
Reading and writing Data in the PLC CPU		○	○	×	—	○ (*3)	○
Router relay exchange (router relay functions)		○	○	×	—	—	—

*1 For information regarding pairing exchange please refer to Item 5.4.1 1 (b) ③ and Item 5.4.4.

*2 Only valid during UDPs without procedure exchange.

*3 Only valid with nodes opened by the sequence program.

3.5 Send and Receive Processing

3.5.1 Message Division and Data Length

The message exchanged between the E71 and the remote node is sometimes allocated by the local node or remote node transmission/reception buffer capacity.

Below is shown the message data length when it is allocated and transmitted or received.

1 When the E71 receives data the message is reconstructed using the following data link (hereafter internal data length) when the message is reconstructed from the TCP/UDP level.

- ① Data length in the data bearing fixed buffer exchange or random access buffer exchange.
- ② The data length calculated from the command message contents when data is read or written in the PLC CPU.

2 The E71 performs the following processing when the internal data length and the data amount that is actually transmitted differ.

- ① When the transmitted amount is less
 - Waits until the remaining data is transmitted.
 - If the next data is not sent within the response monitoring time the message currently being received will be ignored and the next processing (receive rate from initial message) will begin.
- ② When the transmitted data amount is more
 - Determined to be a transmission with two or more messages linked together.
 - The data from the first data received until the internal data length is handled as the initial message and that coming immediately thereafter is handled as the next message. (An error will be generated if there is subheader analysis being conducted or if processing is being conducted for the second message or thereafter.)

(Example) Message transmitted has one message from the partner node



Determined as a message by the E71



↑
This portion is actually not a subheader
so a command/response undefined
error occurs.

Remarks

(1) If the error mentioned in **2** above is generated, an error code is stored for one of the error information storage areas shown below in the E71 buffer memory.

- Exchange state storage area
- Error log area

3.5.2 Continuous Processing Over the Same Connection

The following explains the procedure to transmit data between E71 and other nodes over the same connection, and the E71 processing when continuous data transmission is performed from other nodes to the E71 over the same connection.

1 Procedure to transmit data between other nodes and E71

When transmitting data between other nodes and E71, perform data transmission after the processing for the previous data transmission is complete.

For example, when data transmissions accompany responses, the next data transmission should be commenced after the response to the previous transmission is received.

* If data transmission is continued without following the above procedure, an error may be generated on the E71 side, the connection may be closed, or the open error detection signal (X18) may turn on.

2 The E71 processing during continuous data transmission is performed from other nodes to E71

The following explains the E71 processing when it receives a new message that has no relationship to the current processing, which is the processing of the message received previously over the same connection.

- (a) The processing of the message received first is performed.
- (b) After the processing of the message received first is complete, the processing of the next message will be commenced. (The processing of the next message will be suspended.)

Example: When a random access buffer read request is received via connection 1 while the receive processing for fixed buffer exchange is in progress

- ① The receive processing for the fixed buffer exchange is continued (*1).
- ② After the receive processing for the fixed buffer exchange is completed, the processing for random access buffer read is executed.

*1 The "receive processing for fixed buffer exchange in progress" is defined as the period of time from when the receive end signal (X0) turns on until the receive end confirmation signal (Y0) turns off.

3.5.3 Conditions for Issuing a Forced Disconnect

When the following conditions occur with the TCP/IP communication, the E71 sends a connection forced disconnect to the remote node to forcefully close the line. (ABORT (RST) command transmission)

- ① When the initial request signal is turned off in the state that the open processing has ended on the connection used.
- ② When the remaining messages are not received within the response monitoring time when data allocation is being received.
- ③ TCP transmission error occurs when OFF (close) is selected using the exchange condition setting switch (SW1 : line processing selection during TCP time out error).
- ④ When an existence check trouble is detected for the existence check connection.
- ⑤ When an open request is performed again from the remote node side to the E71 port that either active or full-passive open for performing TCP/IP communication has normally been ended. Or when an open request is performed using the same port number again from the identical remote node to the E71 port that Unpassive open has normally been ended.

3.6 I/O Signals for the PLC CPU

This section explains the E71 I/O signals.

The I/O signal allocation shows that the E71 is installed in the 0 slot of the basic unit. It means that device X receives an input signal from the E71 to the PLC CPU and that device Y receives an output signal from the PLC CPU to the E71.

3.6.1 List of I/O Signals

Table 3.7 List of I/O Signals for the PLC CPU

Signal direction E71 to PLC CPU			Signal direction PLC CPU to E71		
Device No.	Signal name		Device No.	Signal name	
X0	Transmission normal end signal or reception end signal	For connection 1's	Y0	Connection number 1	Transmission request signal and reception end check signal
X1	Transmission error detection signal or reception error detection signal	fixed buffer exchange	Y1	Connection number 2	
X2	Transmission normal end signal or reception end signal	For connection 2's	Y2	Connection number 3	
X3	Transmission error detection signal or reception error detection signal	fixed buffer exchange	Y3	Connection number 4	
X4	Transmission normal end signal or reception end signal	For connection 3's	Y4	Connection number 5	
X5	Transmission error detection signal or reception error detection signal	fixed buffer exchange	Y5	Connection number 6	
X6	Transmission normal end signal or reception end signal	For connection 4's	Y6	Connection number 7	
X7	Transmission error detection signal or reception error detection signal	fixed buffer exchange	Y7	Connection number 8	
X8	Transmission normal end signal or reception end signal	For connection 5's	Y8	Connection number 1	Open request signal
X9	Transmission error detection signal or reception error detection signal	fixed buffer exchange	Y9	Connection number 2	
XA	Transmission normal end signal or reception end signal	For connection 6's	YA	Connection number 3	
XB	Transmission error detection signal or reception error detection signal	fixed buffer exchange	YB	Connection number 4	
XC	Transmission normal end signal or reception end signal	For connection 7's	YC	Connection number 5	
XD	Transmission error detection signal or reception error detection signal	fixed buffer exchange	YD	Connection number 6	
XE	Transmission normal end signal or reception end signal	For connection 8's	YE	Connection number 7	
XF	Transmission error detection signal or reception error detection signal	fixed buffer exchange	YF	Connection number 8	
X10	Connection number 1	Open end signal	Y10	Usage prohibited	
X11	Connection number 2		Y11		
X12	Connection number 3		Y12		
X13	Connection number 4		Y13		
X14	Connection number 5		Y14		
X15	Connection number 6		Y15		
X16	Connection number 7		Y16		
X17	Connection number 8		Y17	COM.ERR LED turn off request signal	
X18	Open error detection signal		Y18	Usage prohibited	
X19	Initial normal end signal		Y19	Initial request signal	
X1A	Initial error detection signal		Y1A	Usage prohibited	
X1B	Usage prohibited		Y1B		
X1C	COM.ERR LED turned on signal		Y1C	Buffer memory channel switching	
X1D	Usage prohibited		Y1D	Usage prohibited	
X1E			Y1E		
X1F	Watchdog timer error detection signal		Y1F		

Important

- (1) Among the I/O signals for the PLC CPU, do not output (ON) the signals that are "usage prohibited." If any output is performed with respect to "usage prohibited" signals, it may result in the malfunction of the PLC system.
- (2) To perform each processing shown in Chapter 5 and succeeding sections, be sure to check the ON/OFF timing of the output signal (Y) and the input signal (X) shown in the applicable explanatory items, and then perform output relative to the output signal.
If ON/OFF of the output signal has not been performed according to the procedure shown in the applicable explanatory items, the E71 will not be able to operate normally.

3.6.2 Detailed Explanation of I/O Signals

This section explains about the I/O signals ON/OFF timing and conditions shown in table 3.7 (on the previous page). The codes in the parentheses are the device numbers that correspond to table 3.7.

1

Transmission normal end signal and reception normal end signal (X0, X2, X4, X6, X8, XA, XC, XE)

These signals are used when exchange is conducted with the fixed buffer. These signals are not used when exchanging with the random access buffer or when reading and writing data in the PLC CPU. Used as the transmit normal end signal when the appropriate fixed buffer transmission is used. Used as the reception end signal when the appropriate fixed buffer reception is used.

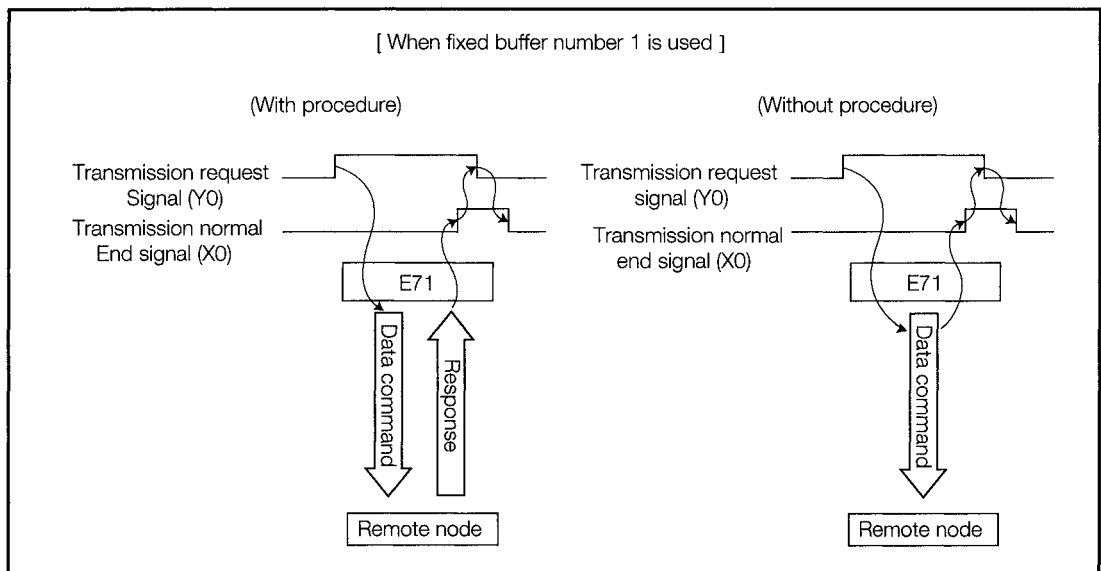
(a) When used as transmission end signal

When exchanging with procedure

- ① Data is transmitted when the data request signal (Y0 to Y7) is on.
- ② The remote node that has received the data returns a response to the E71.
- ③ The transmit normal end signal is turned on when a response is returned by the remote node.
- ④ The transmit normal end signal is turned off when the transmit request signal (Y0 to Y7) is turned off.
- ⑤ The transmission normal end signal is not turned on when the end code returned from the remote node is anything other than 00H. The transmission error detection signal (X1, X3, X5, X7, X9, XB, XD, XF) is turned off.

When exchanging without procedure

- ① The data is transmitted when the transmission request signal (Y0 to Y7) is turned on.
- ② The transmission normal end signal is turned on when the data transmission is completed.
- ③ The transmission normal end signal is turned off when the transmission request signal (Y0 to Y7) is turned off.
- ④ The transmission normal end signal is not turned on when a transmission error is generated. The transmission error detection signal (X1, X3, X5, X7, X9, XB, XD, XF) is turned on.



(b) When used as reception end signal

When exchanging with procedure

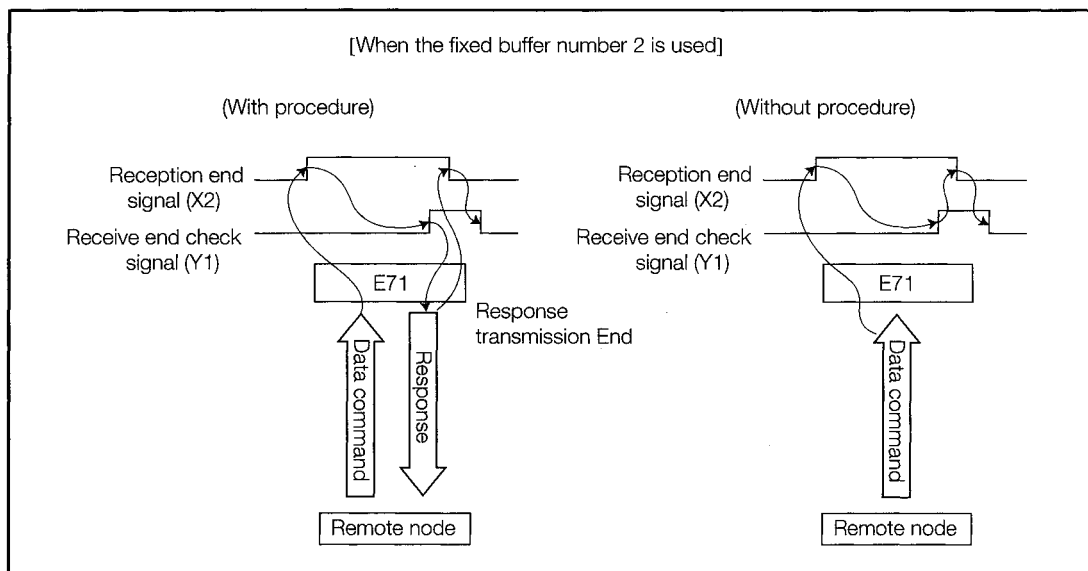
- ① Turns on when the E71 receives data from a remote node.
- ② Can be used as the handshake signal when reception data is read from the PLC CPU using the FROM command, etc.
- ③ The reception end check signal (Y0 to Y7) is turned on after the reception data is read using the FROM command, etc.

A response is returned to the remote node that transmitted the data.

- ④ Reception end signal is automatically turned off after a response is sent to the remote node.
- ⑤ The reception end signal is not turned on when problem data is sent from the remote node.

When exchanging without procedure

- ① Turns on when the E71 receives data from a remote node.
- ② Can be used as the handshake signal when the received data is read from the PLC CPU using a FROM command, etc.
- ③ The receive end check signal (Y0 to Y7) is turned on after the receive data is read using the FROM command, etc.
- ④ The receive end signal is automatically turned off when the receive end check signal is turned on.
- ⑤ The receive end signal is not turned on when following data is transmitted from a remote node.



2

Transmission error detection signal or reception error detection signal (X1, X3, X5, X7, X9, XB, XD, XF)

This signal is used when fixed buffer exchange is performed. This signal is not used for random access buffer exchange or exchange when reading or writing of data inside the PLC CPU is conducted.

When the corresponding fixed buffer is used for transmission, use it as the transmission error detection signal.

When the corresponding fixed buffer is used for reception, use it as the reception error detection signal.

(a) When used as the transmission error detection signal

When exchanging with procedure

- ① The transmit error detection signal is turned on when the response from the remote node is not returned within the response monitor time (Refer to Item 5.3.1) after data is transmitted from the fixed buffer.
- ② The transmission error detection signal is turned on when the specified retry processing (Refer to Item 5.3.1) is conducted when the "ACK" is not returned after data is sent from the fixed buffer that is using the TCP connection. (Retry processing is not performed for UDP)
- ③ The transmission error detection signal is turned on when the finish code response received from the remote node is anything other than 00H after data has been transmitted from the fixed buffer.
- ④ When the transmission error detection signal is turned on, the error contents can be checked by reading the fixed buffer transmission error code storage area (buffer memory 94, 104, ...164).
- ⑤ The transmission error detection signal is turned off when the fixed buffer transmission request signal (Y0 to Y7) is turned off.

When exchanging without procedure

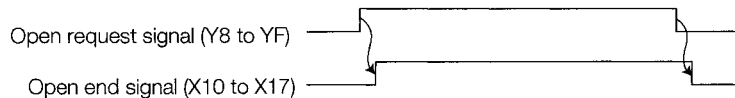
- ① The transmission error detection signal is turned on when the specified retry processing (Refer to Item 5.3.1) is performed when the "ACK" is not returned after the data has been transmitted from the fixed buffer using the TCP connection (Retry processing is not performed for UDP.)
- ② When the transmission error detection signal is turned on, the error contents can be checked by reading the fixed buffer transmission error code storage area (buffer memory 94, 104, ...164).
- ③ The transmission error detection signal is turned off when the fixed buffer transmission request signal (Y0 to Y7) is turned off.

(b) When used as the reception error detection signal

- ① When close processing is performed for the corresponding connection upon receiving Close/Abort (RST) from the partner remote node before the completion of the previous data reception processing, the reception error detection signal may turn on.
- ② When Close/Abort (RST) is received from the partner remote node after the completion of data reception processing, the reception error detection signal does not turn on.
- ③ If the reception error detection signal is on when close processing is performed for the corresponding connection according to (1) above, turn off the open request signal (Y8 to YF) after turning off the reception completion confirmation signal at the timing shown in Remarks in 2 of Item 5.4.3.

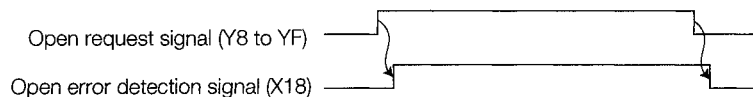
3 Open end signal (X10 to X17)

- (a) When the connection open request signal (Y8 to YF) is turned on by the sequence program the exchange parameters are checked and open processing is executed. Here if open processing is performed normally the open end signal (X10 to X17) is turned on.
- (b) When the open request signal is turned on and open processing is not performed normally the open error detection signal (X18) is turned on. (In this case the open end signal is not turned on.)
- (c) Only data can be exchanged with the remote node for which the open end signal (X10 to X17) is turned on (fixed buffer exchange, random access buffer exchange, and reading and writing from the sequence CPU exchange).
- (d) The open end signal (X10 to X17) ON/OFF can be checked using the LED display (BUF1 to BUF8) on the front of the E71.
- (e) The open end signal (X10 to X17) is turned off when the open request signal is turned off by the sequence program. In the following case the open end signal (X10 to X17) is not turned off. (Refer to Item 5.4.3)
 - ① When an error is generated.
 - ② When CLOSE or ABORT (RST) commands are received from the exchange remote node.
 - ③ When a response monitor timer error occurs.
 - ④ When an error occurs with an existence check function.



4 Open error detection signal (X18)

- (a) When the connection open request signal (Y8 to YF) is turned on by the sequence program the exchange parameters are checked and if an error is detected the open error detection signal is turned on.
- (b) The open error detection signal is turned on when the open request signal (Y8 to YF) is turned on and open processing is not performed normally.
- (c) The open error detection signal is turned on when the TCP or ULP time-out error is generated when the E71's exchange condition setting switch (SW1: line processing selection during TCP time-out error) is turned off (close).
- (d) When the open error detection signal is on the error contents of the connection for which an error has occurred can be checked by reading the error codes such as open error code storage area (buffer memory 93, 103, ...163) or the error log lock in the exchange state storage area.
- (e) The open error detection signal (X18) is turned off when the open request signal (Y9 to YF) is turned off for the connection for which the open error is occurring.
- (f) When there are multiple open errors the open error detection signal (X18) will not turn off unless all of the corresponding open request signals are turned off.



5 Initial normal end signal (X19)

- (a) When the initial request signal (Y19) is turned on by the sequence program the initial parameters are checked and initial processing is executed. Here if initial processing is conducted normally the initial normal end signal (X19) is turned on. (The module's RDY LED will flash after normal end.)
- (b) If the initial processing is not conducted normally the initial error detection signal (X1A) is turned on. (In this case the initial normal end signal (X19) is not turned on.)

6 Initial error detection signal (X1A)

- (a) The initial error detection signal (X1A) is turned on and the initial request signal (Y19) is on and initial processing does not end normally.
- (b) The initial error detection signal turns on when a hardware error occurs after initial processing ends normally. (The initial normal end signal is turned off.)
- (c) When the initial error detection signal (X1A) is turned on, the error contents can be checked by reading the exchange state storage area's initial error code storage area (Refer to Item 5.5.1 buffer memory 80).
- (d) The initial error detection signal (X1A) is turned off when the initial request signal is turned off.

7 COM.ERR LED turned on signal (X1C)

- (a) The COM.ERR LED turned on signal (X1C) is turned on when the COM.ERR LED is turned on when an exchange error occurs. (Refer to Item 13.2*1)
- (b) The COM.ERR LED turned on signal (X1C) is turned off when the COM.ERR LED turn off request signal (Y17) of the sequence program is turned on.

8 Watchdog timer error detection signal (X1F)

The watchdog timer detection signal (X1F) is turned on when a watchdog timer (approximately 300 ms) error occurs when the E71 self diagnostic is used.

9

Transmission request and reception end check signal (Y0 to Y7)

This signal is used when fixed buffer exchange is conducted.

This signal is not used for random access buffer exchange and reading and writing data from the PLC CPU exchange.

Used as a transmission request signal when the appropriate fixed buffer is used for transmission.

Used as the reception end check signal when the appropriate fixed buffer is used for reception.

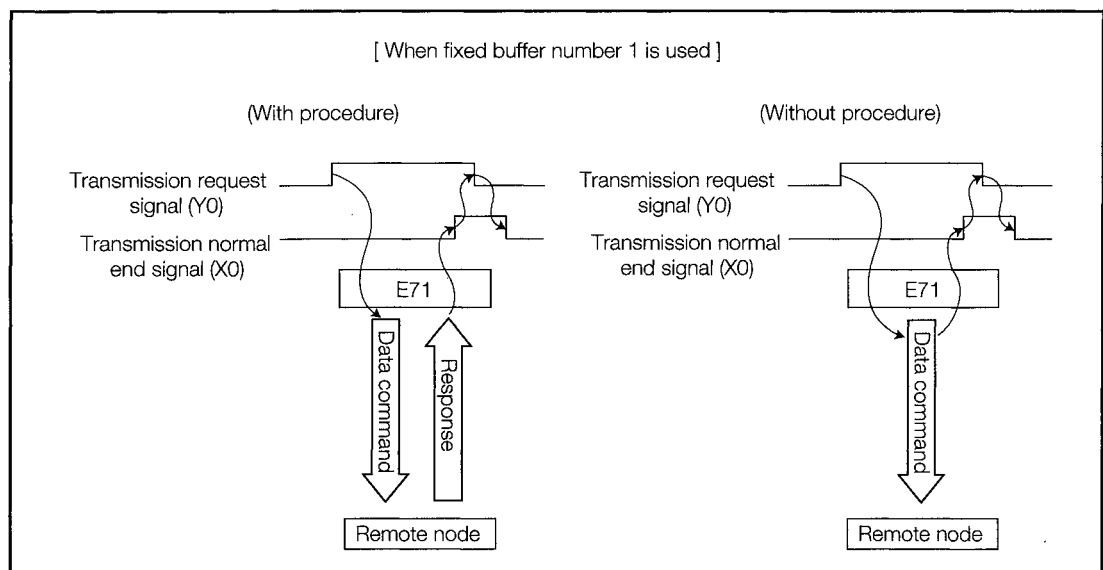
(a) When used as transmission request signal

When exchanging with procedure

- ① The E71 transmits data to the node specified by the parameters when the transmission request signal (Y0 to Y7) is turned on by the sequence program.
- ② Transmission is ended when the transmission end signal (X0: when the fixed buffer is No. 1) when a response is returned from the remote node after data transmission.

When exchanging without procedure

- ① Data is transmitted by the E71 to the remote node specified by the parameters when the transmission request signal (Y0 to Y7) is turned on by the sequence program.
- ② Transmission ends when the transmission end signal (X0: when the fixed buffer is No. 1) is turned on after the data is transmitted.



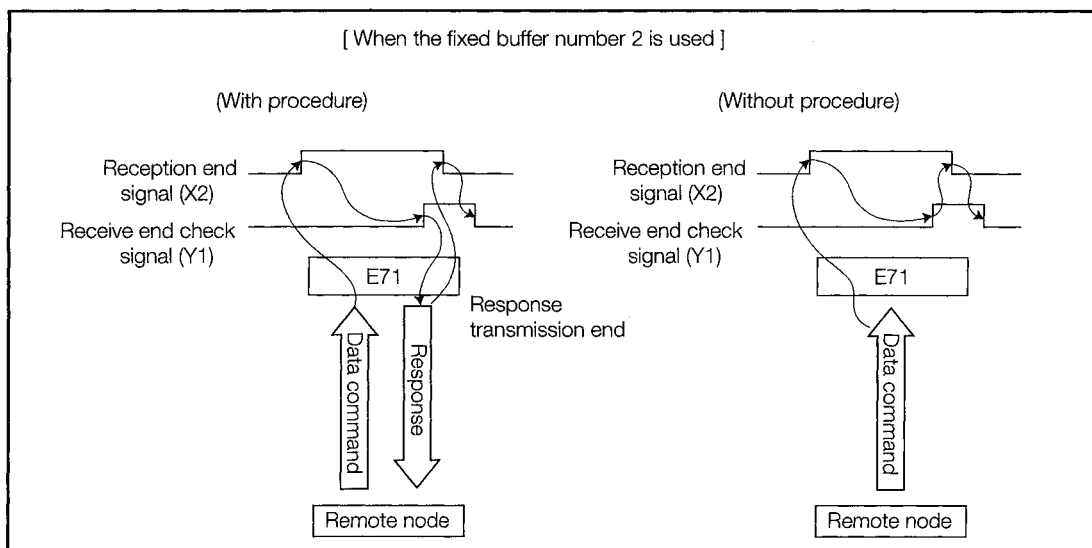
(b) When used as reception end check signal

When exchanging with procedure

- ① The reception end signal (X2 : when the fixed buffer is number 2) is turned on after the E71 receives data from the remote node.
- ② A response is returned to the remote node when the reception end check signal (Y0 to Y7) is turned on after the reception end signal (X2: when the fixed buffer is number 2) is in the on state after check by the sequence program.

When exchanging without procedure

- ① The reception end signal (X2 : when the fixed buffer is No. 2) is turned on after data is received to the E71 from the remote node.
- ② The reception end signal is turned off when the reception end check signal (Y0 to Y7) is turned on and the reception end signal (X2: when the fixed buffer is No. 2) is in the on state after check by the sequence program.



10

Open request signal (Y8 to Y7)

- (a) This is the on signal for exchanging data (fixed buffer exchange, random access buffer exchange, reading and writing from the PLC CPU exchange) between the E71 and the remote node.
- (b) When each connection open request signal (Y8 to YF) is turned on by the sequence program the exchange parameters are checked and if normal open processing is conducted. If error is detected the open error detection signal (X18) is turned on.
- (c) When the open request signal is on and open processing is conducted normally the open end signal (X10 to X17) is turned on. If an error is detected the open error detection signal (X18) is turned on.
- (d) The open error detection signal (X18) is turned off when the open request signal (Y8 to YF) is turned off. When errors occur at multiple connections, the open request signal for all connections where errors are occurring are turned off.

When the open error detection signal (X18) is turned ON, be sure to read the open error code storage area (buffer memory 93, 103, ...163) before the open request signal is turned ON again.

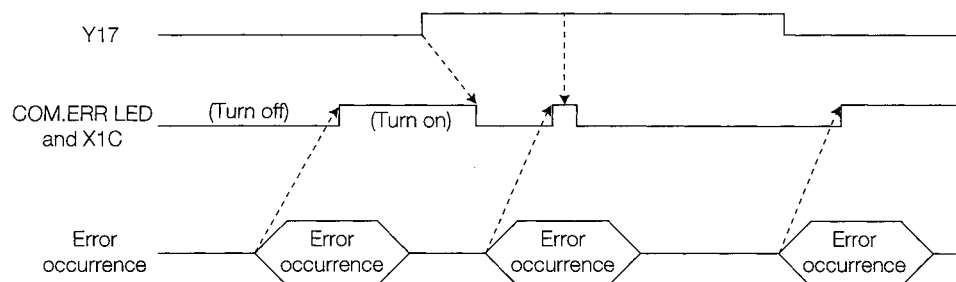
- (e) When turning OFF the open request signal, be sure that the I/O signals and other items related to the corresponding connection to the E71 are in the following status:
- Transmission request signal/receive end check signal (Y0 to Y7), transmission normal end signal/reception end signal (X0, X2 and beyond), transmission error detection signal (X1, X3 and beyond) are all OFF.
 - Open end signal (X10 to X17) is ON. Or, if the open error detection signal (X18) is ON, the open error code for the corresponding connection is other than 0.
 - * When "passive open" is specified in the open processing of TCP/IP communication, the open request signal can be turned OFF before the open end signal or open error detection signal turns ON. In such cases, when reopening processing using passive open, turn ON the open request signal (OFF → ON) once 500 ms have passed after the open request signal is turned OFF.

11

COM.ERR LED turn off request signal (Y17)

This signal is used to turn off the COM.ERR LED on the front of the E71 that is lit when an exchange error occurs.

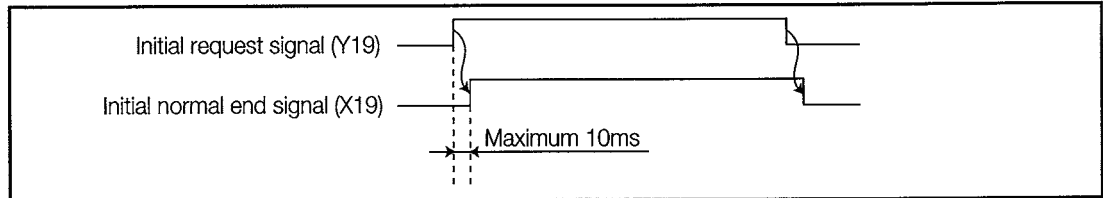
- (a) The COM.ERR LED is turned off when the turn off request signal (Y17) is turned on.
- (b) Normally, turn off processing is conducted when the turn off request signal (Y17) is on.
- (c) When the turn off request signal (Y17) is turned on, the error information in the buffer memory error log area is cleared (deleted).

**Point**

The "COM. ERR" LED on the front panel of the E71 can be turned off by clicking the **COM. ERR OFF** button on the "Ethernet Diagnosis" screen of GX Developer. However, the error information in the error log area of the buffer memory will not be cleared (erased). For more details on the "Ethernet Diagnosis" screen of GX Developer, refer to Section 13.4.1.

12**Initial request signal (Y19)**

- (a) This signal is for conducting initialization before the E71 conducts exchange.
- (b) Turning on the initial request signal (Y19), checks the initial parameters and if they are normal executes initial processing. If an error is detected the initial error detection signal (X1A) is turned on.



- (c) When the initial request signal is turned on and initial processing is conducted normally, the initial normal end signal (X19) is turned on. Here, if an error is detected, the initial error detection signal (X1A) is turned on.
- (d) When an E71 system error occurs the initial error detection signal (X1A) is turned on. Please turn off the initial request signal (Y19). The error code at this time is stored in the initial error code storage area (buffer memory 80).
- (e) After checking that the following signals are turned off, turn off the initial request signal.
 - Transmission request signal/reception end check signal (Y0 to Y7)
Transmission normal end signal/reception end signal (X0, X2...)
Transmission error detection signal (X1, X3...)
 - Open request signal (Y8 to YF)
Open end signal (X10 to X17)
Open error detection signal (X18)

13 Buffer memory channel switching signal (Y1C)

This signal is used to specify the buffer memory channel. It is turned on/off by the sequence program before the read/write to the E71 buffer memory (*1) is conducted by the sequence program's FROM/TO instruction.

OFF : Channel 0 becomes valid.

ON : Channel 1 becomes valid.

*1 When the PLC CPU conducts data read/write for the fixed buffer (address: 512 to 4607) and the random access buffer (address: 4608 to 7679), the buffer memory channel switching signal (Y1C) is turned on/off by the PLC CPU. Turning the buffer memory channel switching signal (Y1C) on/off conducts data read/write to the next area.

- When the buffer memory channel switching signal (Y1C) is off
Fixed buffer No. 1 to No. 4 area, random access buffer (first half 3k words)
- When the buffer memory channel switching signal (Y1C) is on
Fixed buffer No. 5 to No. 8 areas, random access buffer (last half 3k words)

When the I/O control method of the PLC CPU installed in the E71 is the refresh method, one of the following is performed when the above buffer memory read/write is conducted after the buffer memory channel switching signal (Y1C) is turned from on to off/off to on.

- ① The next sequence scan is read/written after the buffer memory channel switching (Y1C) is turned on/off.
- ② Read/write is performed after the signal (Y1C) range is refreshed by the common instruction's SEG after the buffer memory channel switching signal (Y1C) is turned on/off.
- ③ Read/write is performed after the buffer memory channel switching signal (Y1C) is direct set/reset by the dedicated instruction's DSET/DRST.
- ④ Read/write is conducted after the signal (Y1C) is direct output by the dedicated instruction's DOUT after the buffer memory channel switching signal (Y1C) is turned on/off.

3.7 Buffer Memory

This section explains about the PLC CPU and the data reception buffer memory in the E71.

3.7.1 Buffer Memory Applications

The buffer memory is composed of the following user areas and system area.

1

User area

- ① This is the area other than the system areas given below.
- ② These areas are the areas where the parameter types are set for initial processing and data exchange, data exchange areas, and areas for storing exchange state and exchange error information.
- ③ The default values used when the E71 is booted up are stored in the parameter settings area for initial processing and data exchange. These default values make exchange with a remote node possible, but sometimes they need to be changed. Before conducting processing, write the set values only into the areas that need to be changed.
- ④ When reading from and writing to the user area, please follow the detailed instruction items. In addition, only execute post reading and writing (FROMP, TOP, etc.) when necessary. The data exchange time sometimes becomes long during normal execution.

2

System area

This is the area used by the E71.

In addition to the system areas shown in Item 3.7.2, system areas partially exist in the user areas described in 1 above.

Important

Do not write data in the "system area" in the buffer memory of the special function module. If data is written to the "system area", it may result in the malfunctioning of the PLC system.

Point

The access from PLC CPU is given priority over other special function module processing. Therefore, if the PLC CPU frequently accesses the buffer memory of a special function module, not only the scan time of the PLC CPU is prolonged, but each processing of the special function module is delayed.
Perform access to the buffer memory from the PLC CPU using FROM/TO instruction or other means only when it is necessary.

3.7.2 List of Buffer Memory Allocations

The buffer memory is comprised of 1 address of 16 bits. The overall configuration of the buffer memory is shown below. For details regarding each area, please refer to the explanation references shown on the right side of each area. (How to read the buffer memory address)

Addresses are written using the following method and the same explanation is given hereafter.

□ H (□)

↑ The address for this area is shown in decimal numbers.

↑ The address for this area is shown in hexadecimal numbers.

(address)		Buffer memory		Detailed Explanation
0 to	FH (0 to 15)	Initial processing parameter settings area (16 words)		Item 5.3.1
10 to	4FH (16 to 79)	Exchange parameter setting area (64 words)		Item 5.4.1
50 to	55H (80 to 85)	Exchange state storage area	Initial processing state storage area (6 words)	Item 5.5.1
56 to	58H (86 to 88)		System area (Use prohibited...3 words)	—————
59 to	A8H (89 to 168)		Exchange state storage area (80 words)	Item 5.5.2
A9 to	B3H (169 to 179)		Error log area (11 words)	Item 5.5.3
B4 to	16FH (180 to 367)		System area (Use prohibited...188 words)	—————
170 to	1BFH (368 to 447)		Each protocol state storage area (80 words)	Item 5.5.4
1C0 to	1C1H (448 to 449)	Sub-net mask setting area (2 words)		Item 11.2
1C2 to	1D8H (450 to 472)	Router relay parameter setting area (23 words)		Item 12.4
1D9 to	1EFH (473 to 495)	Systems area (Use prohibited...23 words)		—————
	1F0H (496)	Instructions area	Exchange specification during STOP area(1 Word)	Item 5.4.1
1F1 to	1FFH (497 to 511)	Systems area (Use prohibited...15 words)		—————
200 to	5FFH (512 to 1535)	Fixed buffer No. 1 (1024 words)	Fixed buffer No. 5 (1024 words)	Chapter 6 Chapter 7
600 to	9FFH (1536 to 2559)	Fixed buffer No. 2 (1024 words)	Fixed buffer No. 6 (1024 words)	
A00 to	DFFH (2560 to 3583)	Fixed buffer No. 3 (1024 words)	Fixed buffer No. 7 (1024 words)	
E00 to	11FFH (3584 to 4607)	Fixed buffer No. 4 (1024 words)	Fixed buffer No. 8 (1024 words)	
1200 to	1DFFH (4608 to 7679)	Random access buffer (First half 3072 words)	Random access buffer (Last half 3072 words)	Chapter 8

Access to the fixed buffer and random access buffer by the PLC CPU.

↑ Access is possible with Y1C = OFF.
(Channel 0 specified)

↑ Access is possible with Y1C = ON.
(Channel 1 specified)

(Address)	Buffer memory		Default Values
0 to 1H (0 to 1)	Local station E71's IP address	(2 words)	0H (0)
2H (2)	Special function settings	(1 word)	0H (0)
3H (3)	Timer setting time units	(1 word)	7D0H(2000)
4 to 5H (4 to 5)	System area (Use prohibited)	(2 words)	—
6H (6)	TCP Maximum Segment transmission setting (*1)	(1 word)	8000H
7H (7)	Destination existence check start interval timer value	(1 word)	12CH (300)
8H (8)	Destination existence check interval timer value	(1 word)	5H (5)
9H (9)	Number of retransmit tries for destination existence check	(1 word)	3H (3)
AH (10)	TCP/ULP time out value	(1 word)	FH (15)
BH (11)	TCP zero window timer value	(1 word)	5H (5)
CH (12)	TCP retransmit timer value	(1 word)	5H (5)
DH (13)	TCP end timer value	(1 word)	AH (10)
EH (14)	IP setup timer value	(1 word)	3H (3)
FH (15)	Response monitoring timer value	(1 word)	FH (15)
10H (16)	Connection No.1	Usage available settings area (1 word each)	0H (0)
11H (17)	Connection No.2		0H (0)
12H (18)	Connection No.3		0H (0)
13H (19)	Connection No.4		0H (0)
14H (20)	Connection No.5		0H (0)
15H (21)	Connection No.6		0H (0)
16H (22)	Connection No.7		0H (0)
17H (23)	Connection No.8		0H (0)
18H (24)	E71's Port No.	Exchange address settings area (For Connection No.1 7 words)	0H (0)
19H (25)	Remote node IP address		0H (0)
1AH (26)	Remote node port No.		0H (0)
1BH (27)	Remote node (L)		FFFFFFFFFFFFH
1CH (28)	Ethernet to		
1DH (29)	Address (H)		
1EH (30)			
1F to 25H (31 to 37)	E71's port No. to	Exchange address settings area (For connection No.2 7 words)	(Same as connection No.1)
26 to 2CH (38 to 44)	E71's port No. to	Exchange address settings area (For connection No.3 7 words)	(Same as connection No.1)
2D to 33H (45 to 51)	E71's port No. to	Exchange address settings area (For connections No.4 7 words)	(Same as connection No.1)
34 to 3AH (52 to 58)	E71's port No. to	Exchange address settings area (For connections No.5 7 words)	(Same as connection No.1)
3B to 41H (59 to 65)	E71's port No. to	Exchange address settings area (For connections No.6 7 words)	(Same as connection No.1)
42 to 48H (66 to 72)	E71's port No. to	Exchange address settings area (For connections No.7 7 words)	(Same as connection No.1)
49H (73)	E71's port No.	Exchange address settings area (For connections No.8 7 words)	0H (0)
4AH (74)	Remote node IP address		0H (0)
4BH (75)	Remote node port No.		0H (0)
4CH (76)	Remote node (L)		FFFFFFFFFFFFH
4DH (77)	Ethernet to		
4EH (78)	Address (H)		
4FH (79)			

(Address)	Buffer Memory		Default Value
50H (80)	Initial error code	(1 word)	0H (0)
51 to 52H (81 to 82)	Local station E71's IP address	(2 words)	0H (0)
53 to 55H (83 to 85)	Local station E71's Ethernet address	(3 words)	0H (0)
56 to 58H (86 to 88)	System area (Use prohibited)	(3 words)	—
59H (89)	Local station E71's port No.	Information by Connection (10 words for connection No.1)	0H (0)
5A to 5BH (90 to 91)	Remote node IP address		0H (0)
5CH (92)	Remote node port No.		0H (0)
5DH (93)	Open error code		0H (0)
5EH (94)	Fixed buffer transmission/reception error code		0H (0)
5FH (95)	Fixed buffer exchange end code		0H (0)
60H (96)	Maximum value		0H (0)
61H (97)	Minimum value	Fixed buffer exchange's ex- change time	0H (0)
62H (98)	Current value		0H (0)
63 to 6CH (99 to 108)	Local station E71's port No. to	Information by connection (For connection No.2)	(Same as above)
6D to 76H (109 to 118)	Local station E71's port No. to	Information by connection (For connection No.3)	(Same as above)
77 to 80H (119 to 128)	Local station E71's port No. to	Information by connection (For connection No.4)	(Same as above)
81 to 8AH (129 to 138)	Local station E71's port No. to	Information by connection (For connection No.5)	(Same as above)
8B to 94H (139 to 148)	Local station E71's port No. to	Information by connection (For connection No.6)	(Same as above)
95 to 9EH (149 to 158)	Local station E71's port No. to	Information by connection (For connection No.7)	(Same as above)
9F to A8H (159 to 168)	Local station E71's port No. to	Information by connection (For connection No.8)	(Same as above)
A9H (169)	Error log	Area-1 (1 word)	0H (0)
AAH (170)		Area-2 (1 word)	0H (0)
ABH (171)		Area-3 (1 word)	0H (0)
ACH (172)		Area-4 (1 word)	0H (0)
ADH (173)		Area-5 (1 word)	0H (0)
AEH (174)		Area-6 (1 word)	0H (0)
AFH (175)		Area-7 (1 word)	0H (0)
B0H (176)		Area-8 (1 word)	0H (0)
B1H (177)		Area-9 (1 word)	0H (0)
B2H (178)		Area-10 (1 word)	0H (0)
B3H (179)		Area-11 (1 word)	0H (0)
B4 to 16FH (180 to 367)	System area (Use prohibited)	(188 words)	—

(Address)		Buffer Memory		Default Value
170H (368)	Total number of times IP packets received	(1 word)	0H (0)
171H (369)	Total number of times received IP packet discarded because of check sum error	(1 word)	0H (0)
172H (370)	Total number of transmitted IP packets	(1 word)	0H (0)
173H (371)	System area (Use prohibited)	(1 word)	—
174H (372)	Number of received ARP packets	(1 word)	0H (0)
175H (373)	Number of responses to ARP packets	(1 word)	0H (0)
176H (374)	Number of received IP packets that are not broadcasted to local station	(1 word)	0H (0)
177H (375)	System area (Use prohibited)	(1 word)	0H (0)
178H (376)	Number of transmitted ARP packets	(1 word)	0H (0)
179H (377)	Number of transmitted broadcast packets	(1 word)	0H (0)
17A to 17FH (378 to 383)	383)	System area (Use prohibited)	(6 words)	—
180H (384)	Total number of received ICMP	(1 word)	0H (0)
181H (385)	Total number of times received ICMP packet discarded because of check sum error	(1 word)	0H (0)
182H (386)	Total number of transmitted ICMP packets	(1 word)	0H (0)
183H (387)	Total number of received ICMP echo request packets	(1 word)	0H (0)
184H (388)	Total number of transmitted ICMP echo reply packets	(1 word)	0H (0)
185H (389)	Total number of transmitted ICMP echo request packets	(1 word)	0H (0)
186H (390)	Total number of received ICMP echo reply packets	(1 word)	0H (0)
187 to 18FH (391 to 399)	399)	System area (Use prohibited)	(9 words)	—
190H (400)	Total number of received TCP packets	(1 word)	0H (0)
191H (401)	Total number of times received TCP packet discarded because of check sum error	(1 word)	0H (0)
192H (402)	Total number of transmitted TCP packets	(1 word)	0H (0)
193H (403)	Number of ZERO-Window time-outs	(1 word)	0H (0)
194 to 19FH (404 to 415)	415)	System area (Use prohibited)	(12 words)	—
1A0H (416)	Total number of received UDP packets	(1 word)	0H (0)
1A1H (417)	Total number of times received UDP packet discarded because of check sum error	(1 word)	0H (0)
1A2H (418)	Total number of transmitted UDP packets	(1 word)	0H (0)
1A3H (419)	Number of times received UDP packets discarded because addressed to other than the host	(1 word)	0H (0)
1A4 to 1BFH (420 to 447)	447)	System area (Use prohibited)	(28 words)	—
1C0 to 1C1H (448 to 449)	449)	Subnet mask field	(2 words)	0H (0)
1C2 to 1C3H (450 to 451)	451)	Default router IP address	(2 words)	0H (0)
1C4H (452)	Registered number of routers	(1 word)	0H (0)
1C5 to 1C6H (453 to 454)	454)	Router 1 setting	Subnet address 1 (2 words)	0H (0)
1C7 to 1C8H (455 to 456)	456)		Router IP address 1 (2 words)	0H (0)
1C9 to 1CAH (457 to 458)	458)	Router 2 setting	Subnet address 2 (2 words)	0H (0)
1CB to 1CCH (459 to 460)	460)		Router IP address 2 (2 words)	0H (0)
1CD to 1CEH (461 to 462)	462)	Router 3 setting	Subnet address 3 (2 words)	0H (0)
1CF to 1D0H (463 to 464)	464)		Router IP address 3 (2 words)	0H (0)
1D1 to 1D2H (465 to 466)	466)	Router 4 setting	Subnet address 4 (2 words)	0H (0)
1D3 to 1D4H (467 to 468)	468)		Router IP address 4 (2 words)	0H (0)
1D5 to 1D6H (469 to 470)	470)	Router 5 setting	Subnet address 5 (2 words)	0H (0)
1D7 to 1D8H (471 to 472)	472)		Router IP address 5 (2 words)	0H (0)
1D9 to 1EFH (473 to 495)	495)	System area (Use Prohibited)	(23 words)	—
1F0H (496)	Communication specification during STOP	(1 word)	0H (0)
1F1 to 1FFH (497 to 511)	511)	System area (Use prohibited)	(15 words)	—

(Address)		Buffer Memory		Default Value
200H (512)	Transmission data length storage area/ reception data length storage area	Fixed buffer No.1/ Fixed buffer No.5 (1024 words)	0H (0)
201 to 5FEH (513 to 1535)	Transmission data write area/ reception data storage area		
600H (1536)	Transmission data length storage area/ reception data length storage area	Fixed buffer No.2/ Fixed buffer No.6 (1024 words)	
601 to 9FFH (1537 to 2559)	Transmission data write area/ reception data storage area		
A00H (2560)	Transmission data length storage area/ reception data length storage area	Fixed buffer No.3/ Fixed buffer No.7 (1024 words)	
A01 to DFFH (2561 to 3583)	Transmission data write area/ reception data storage area		
E00H (3584)	Transmission data length storage area/ reception data length storage area	Fixed buffer No.4/ Fixed buffer No.8 (1024 words)	
E01 to 11FFH (3585 to 4607)	Transmission data write area/ reception data storage area		
1200 to 1DFFH (4608 to 7679)	Buffer for random access	First half/last half (3072 words)	

*1 Applicable to the product whose software version is as follows.

AJ71E71N3-T, A1SJ71E71N3-T : "Version A" or later

E71 other than above : "Version E" or later

For the product whose software version is earlier than that, the TCP Maximum Segment transmission function is not available. This area is used as a system area.

Point

The following table shows the relationship of the parameters and functions that must be set when using the E71 functions.

Parameters	Functions	Fixed Buffer Exchange		Random Access Buffer Exchange	Reading and Writing Data in the PLC CPU	Router Relay
		With Procedure	Without Procedure			
Parameters for initial processing						○
Local station IP address		○	○	○	○	○
Special function settings		×	×	×	×	○
Various timer values		○	○	○	○	
Exchange parameters						
Usage applications						×
Bit 0 (Buffer application)		○	○	×	×	×
Bit 1 (Existence check)		△	△	△	△	×
Bit 7 (Pairing)		△	△	×	×	○
Bit 8 (Communication format)		○	○	○	○	×
Bit 9 (Exchange procedure)		○	○	×	×	○
Bit 14 · 15 (Open method)		○	○	○	○	
Exchange address *1						
E71 port No.		○	○	○	○	×
Remote node IP address						
Remote node port No.						
Remote ethernet address						
Router relay parameter		×	×	×	×	○

○ : Setting is required when using these functions (default value/change value)

×

*1 Set the parameters by the open method used for the open processing before communication line connection.

Parameters	Communication Format Open Method	TCP				UDP	
		Active		Passive		Remote Node ARP Functions	
		Remote Node ARP Functions		Unpassive	Full passive		
		Yes	No			Yes	No
E71 port No.		○	○	○	○	○	○
Remote node IP address		○	○	×	○	○	○
Remote node No.		○	○	×	○	○	○
Remote node ethernet address		○**	○	×	×	○**	○

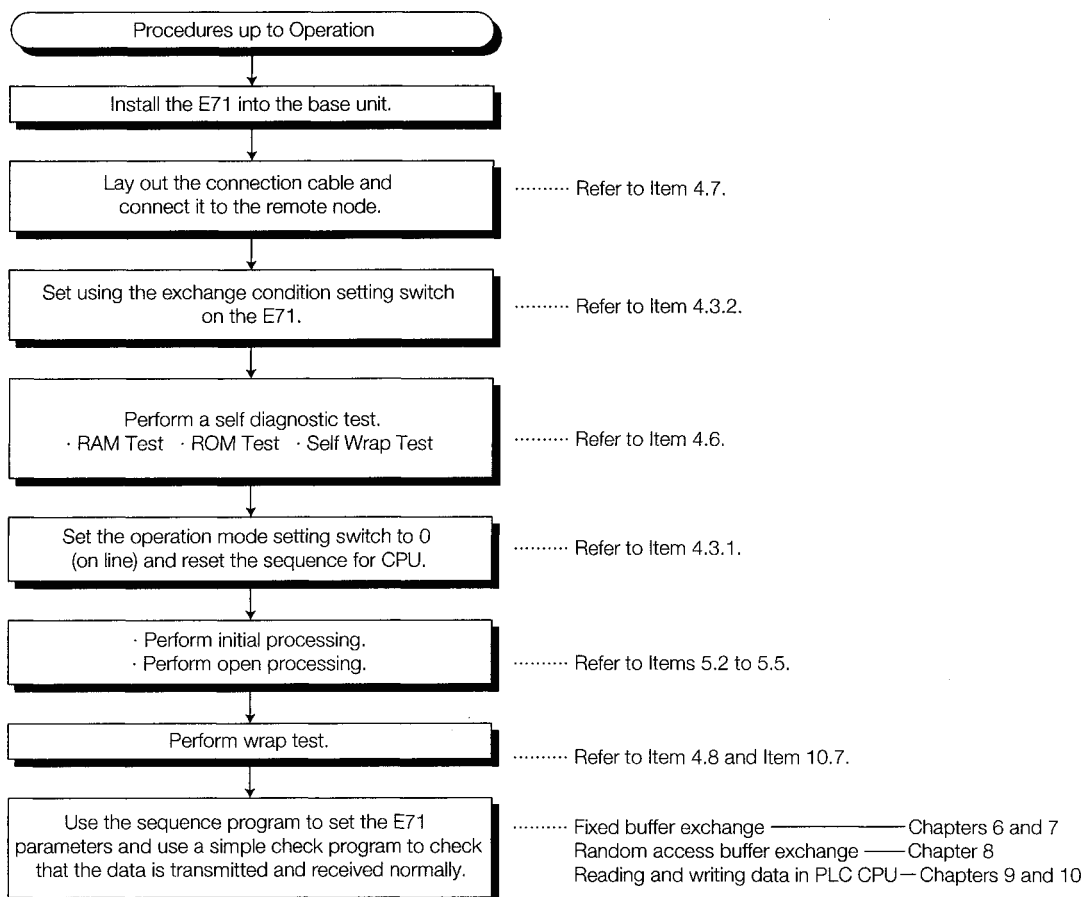
** Please make the default value (FFFFFFFFFH)

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4. SETTINGS AND PROCEDURES UP TO OPERATION

This section explains the procedures and the setting methods for the system that uses E71 up to the point of E71 operation.

4.1 Abbreviated Procedures Up to Operation



DANGER

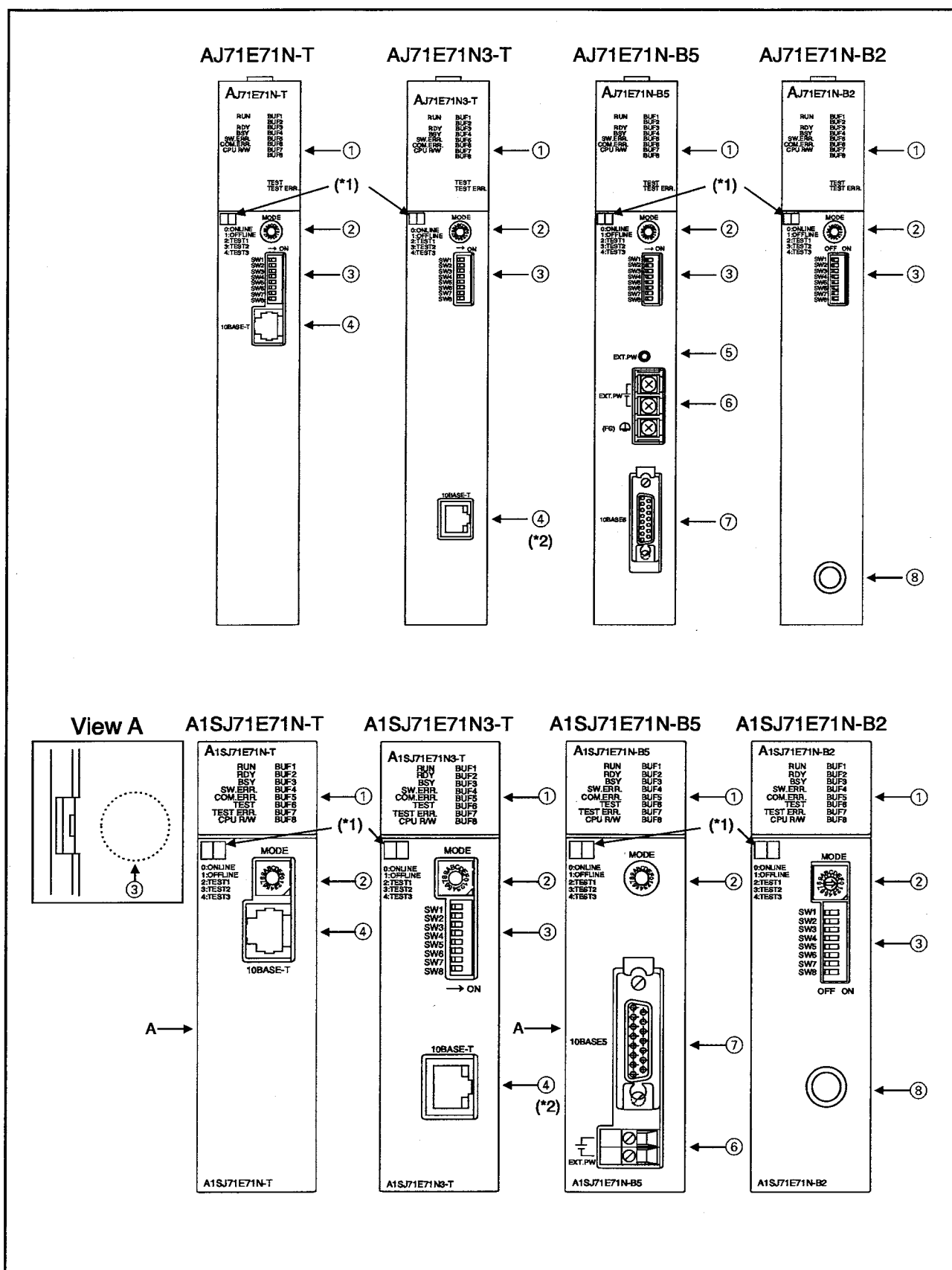
- Do not touch the terminals while the electricity is on.
Doing so could cause erroneous operation.
- Make sure to switch all phases of the external power supply off before cleaning or re-tightening screws.
If you do not switch off the external power supply, it will cause failure or erroneous operation of the module.
If the screws are loose, it may result in fallout, short circuits, or erroneous operation.
Tightening the screws too far may cause damage to the screws and/or the module, resulting in fallout, short circuits, or erroneous operation.

CAUTION

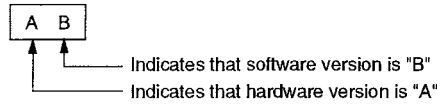
- Do not disassemble or modify the module.
It may cause failure, erroneous operation, injury, or fire.
- Make sure to switch all phases of the external power supply off before cleaning or removing the module.
If you do not switch off the external power supply, it will cause failure or erroneous operation of the module.

4.2 Names of Parts

This section explains the names and setting method for the E71 parts.



*1 The sticker shows the hardware version and software version of the module.
(Example)



*2 The LED on the connector does not turn on.

No.	Name	Description and explanation	Reference item
①	Display LED	Displays the operating state, data transmission and reception, and error description. The description when the LED is turned on or off varies for each LED.	Item 4.4
②	Operation mode setting switch	Select from on-line, off-line, self diagnostic test, or operation mode. Normally on-line is selected. The setting at the time of factory shipment is 0 (on-line).	Item 4.3.1
③	Exchange condition setting switch	Selects the start up conditions, exchange processing conditions, code types during exchange, and whether there is TCP time out error processing. The setting at the time of factory shipment is that SW1 to SW8 are all turned off.	Item 4.3.2
④	10BASE-T connector(RJ45)	This connector used to connect the E71 to the 10BASE-T	Item 4.7.4
⑤	External power supply indicator lamp	This light confirms that power is being supplied to the transceiver . ON: Power is being supplied OFF: Power has not been supplied	Item 2.3
⑥	External power supply terminal	Power supply terminal for supplying power to the transceiver	Item 2.3
⑦	AUI cable connector	This connector is used to connect the E71 to the 10BASE5.	Item 2.3 Item 4.7.2
⑧	10BASE2 connector	This connector is used to connect the E71 to the 10BASE2.	Item 2.3 Item 4.7.3

4.3 Switch Settings

4.3.1 Operation Mode Settings

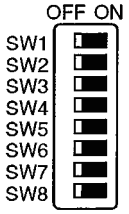
Table 4.1 List of Operation Mode Settings and Descriptions

Operation Mode Setting Switches	Setting No.	Setting Name	Settings Description
	0	On-line	Conducts exchange with remote node in the normal operation mode.
	1	Off-line	Disconnects the local station from the network.
	2	Test 1	Conducts a self diagnostic test using a self wrap test.
	3	Test 2	Conducts an RAM test.
	4	Test 3	Conducts a ROM test.
	5	Usage Not Possible	
	to		
	F		

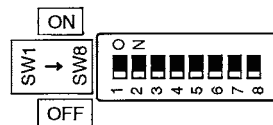
Point

When changing the operation mode, reset the PLC CPU after changing the operation mode setting switch. The resetting operation will start up the selected operation mode.

4.3.2 Exchange Condition Settings

Exchange condition setting switch*1	Switch	Setting items	Setting description	At time of factory shipment
	SW1	Line processing selection during TCP time out error	Selects the line processing when the TCP ULP time out error occurs. Off: Closes the line when the TCP ULP time out error occurs. On: Does not close the line even if a TCP ULP time out error occurs.	OFF
	SW2	Data code setting	Selects the type of data code for exchanging data with the remote node. Off: Conducts exchange in binary code. On: Conducts exchange in ASCII code.	OFF
	SW3	Usage not possible (Fixed to off)	Usage not possible (Fixed to off)	OFF
	SW4			OFF
	SW5			OFF
	SW6			OFF
	SW7	CPU exchange timing setting	Selects whether to approve or forbid data arriving from the remote node when a PLC CPU is running. Off: Forbids writing from the remote node when the PLC CPU is running. On: Conducts writing from the remote node when the PLC CPU is running.	OFF
	SW8	Initial timing setting	Selects the initial processing start up timing. Off: Quick start (starts without a delay time) Set when one network is used for the entire configuration. On: Normal start (starts after a delay of 20 seconds) Use when the entire configuration is made up of multiple networks.	OFF

*1 When the hardware version is "B" or later, the communications exchange condition setting switches for the A1SJ71E71N-B5 are as shown below.



(1) Line processing selection during TCP time out error (SW1)

A TCP ULP time out error occurs when an ACK is not returned, even when the specified retry processing is conducted when using the TCP/IP communication. Selects the connection processing at this time.

Normally set this switch to OFF. To set this switch to ON, perform the close processing and open processing of the communication line using a sequence program when a TCP ULP time out error (error code: 9059H) has occurred in data exchange with a remote node.

(2) Data code setting (SW2)

Selects the data code type (binary, ASCII) when conducting data exchange with a remote node. (Refer to item 3.3)

(3) Initial timing setting (SW8)

Freezes for approximately 20 seconds a connection that has been closed once when using TCP/IP communication. Because there will be a wait when the same IP address and the same port No. are reopened, the system start up time should be set in anticipation of this. This setting will change the time from when the initial request signal (Y19) is turned on, to when the initial normal end signal (X19) is turned on. Normally set this to OFF.

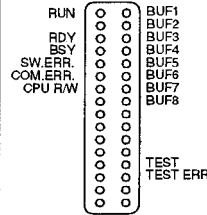
Point

The exchange condition setting switches should be set when the E71's power is turned off.

4.4 Description of Display LED's Display

This section explains the signal names and describes the displays of the display LEDs on the top front of the E71.

Table 4.2 List of the Display LEDs and the Display Contents

Display LEDs AJ71E71N3-T AJ71E71N-T AJ71E71N-B5 AJ71E71N-B2	LED names	Description of LED displays		When the LED is on	When the LED is off
	RUN	Normal operation display		Normal	Error
	RDY	Exchange ready end display		Starts flashing when on-line operations begin.	
	BSY	Exchange processing executing display		Turns on when exchange processing with remote node is being executed.	
	SW. ERR.	(For system)		—	—
	COM.ERR.	Exchange error detection display (Refer to Item 13.2*1)		Exchange error	Normal
	CPU R/W	Exchange processing executing with PLC CPU display		Exchanging	Not exchanging
	BUF 1	Connection No.1	Communication line connection state display	Open completed	Closed state
	BUF 2	Connection No.2			
	BUF 3	Connection No.3			
	BUF 4	Connection No.4			
	BUF 5	Connection No.5			
	BUF 6	Connection No.6			
	BUF 7	Connection No.7			
	BUF 8	Connection No.8			
	TEST	Self diagnostic executing display	Valid only during self diagnosis	Self diagnosis executing	Self diagnosis completed
	TEST ERR.	Self diagnosis results display		Error	Normal
	TEST ERR.	Self diagnosis results display		Error	Normal
	TEST ERR.	Self diagnosis results display		Error	Normal
	TEST ERR.	Self diagnosis results display		Error	Normal
	TEST ERR.	Self diagnosis results display		Error	Normal
	TEST ERR.	Self diagnosis results display		Error	Normal

- (1) For the above LEDs, when RUN turns off after the power is turned on, a watch dog timer error can be suspected. The watch dog timer error detection signal (X1F) will also turn on.
- (2) The RDY in the above LED flashes when operation is started by the online mode (mode setting switch is set to 0) and the initial processing finishes normally.
- (3) Of the above LEDs, the BSY exchange processing executing is done during the following times. BSY is lit while retrying processing during data transmission.
 - (a) For Transmission Procedure
 - Until a reception response is received from the command transmission
 - Until a transmission error occurs from the command transmission (until time out)
 - (b) For Reception Procedure
 - Until a response is returned from the command reception
- (4) For the LEDs shown in Table 4.2, the BUF1 to BUF8 communication line connection state shows the line connection state with the partner set by the exchange parameters. The open completed signal (X10 to X17) ON/OFF state can be checked using these LEDs. Here data can only be exchanged using connections that are opened.
- (5) Of the above-mentioned LED's, COM.ERR goes off by the COM.ERR LED off request signal (Y17).

(6) The following statuses can be checked with the display LEDs of the E71.

	LED name	Status to check	Cause/corrective action
1	[RUN]	Stays off even after the power is on. (*1)	① Watchdog timer error When an abnormality of the operation of the E71 itself is detected by the self diagnosis function of the E71, the watchdog timer error detection signal (X1F) turns on. ② E71 installation failure <ul style="list-style-type: none"> • Check whether or not the power supply capacity (5 VDC) of the power supply module is sufficient. • Turn off the power supply and reinstall the E71.
2	[RDY]	Stays off even after initial processing. (*1)	Check/modify the setting values of the parameters for the initial processing of the E71.
3	[COM. ERR.]	Turns on after powering on the E71.	① Check the content of the error according to the error code stored when an error is detected in any of the following processing, and remove the error cause. (Refer to *1 in Item 13.2.) <ul style="list-style-type: none"> • Initial processing • Open processing • Fixed buffer transmission processing • Data exchange processing • Other processing (processing for which an error code is stored in the error log area) ② For a list of error codes, refer to Item 13.1.
4	[TEST ERR.]	Turns on after a self-diagnostic test.	① Hardware failure of the E71 ② Poor cable connections, line abnormality Check the cable connections. (*2) ③ Abnormality of 12 VDC external supply power (only when a 10BASE5 is connected) Check by referring to Item 2.3.

*1 By performing a self-diagnosis test, check whether the E71 operates normally. (Refer to Item 4.6.)

*2 Be sure to check the completion of the initial processing of the E71 and whether there are any problems in the cable connections and Ethernet line (Refer to Item 13.3).

4.5 Mounting and Installation

This section explains the handling precautions that are common for modules, and about the installation environment from the time the E71 is unpacked until installation.

For details regarding module mounting and installation, please refer to the user's manual of the PLC CPU module being used.

4.5.1 Handling Precautions

This section explains the handling precautions for the E71 module itself.

For the cautionary items regarding module installation and removal, refer to the ●Safety Precautions● described in the beginning of this manual.

- (1) The E71's case is made of plastic, so do not drop it or subject it to strong impacts.
- (2) The tightening torque for module terminal screws and installation screws should be kept within the following range.

Screw Locations	Tightening Torque Range
External power supply terminal screws (*1)	AJ71E71N-B5 : 98 to 137 N·cm(M4 screws)
	A1SJ71E71N-B5 : 40 N·cm(M2.5 screws)
Module installation screws	78 to 118 N · cm (M4 screws)

*1 This is used as an external power input terminal for supplying power to the transceiver when being connected to a 10BASE5.

CAUTION

- Insert the tabs at the bottom of the module into the mounting holes in the base unit before installing the module. (Modules in AnS series, make sure screws are securely tightened to base unit with specified torques.)
Improper installation may cause erroneous operation, failure, or the module to fall out.
- Be sure that cuttings, wire chips, or other foreign matter do not enter the module. Foreign matter may start a fire or cause an accident or erroneous operation.
- Do not disassemble or rebuild the module. It may cause failure, erroneous operation, injury, or fire.
- Make sure to switch all phases of the external power supply off before mounting or removing the module.
If you do not switch off the external power supply, it will cause failure or erroneous operation of the module.
- Tighten the terminal screws within the range of specified torque. If the screws are loose, it may result in short circuits or malfunctions.
Tightening the screws too far may cause damage to the screw and/or the module, resulting in fallout, short circuits, or erroneous operation.
- Do not touch the electronic parts or the module conducting area.
It may cause erroneous operation or failure.
- When disposing of this product, handle it as industrial waste.

4.5.2 Installation Environment

The following environments should be avoided when installing the QnA series PLC.

- Areas where the temperature range of the surrounding temperature is outside 0 to 55 degrees Celsius.
- Areas where the surrounding humidity exceeds the range of 10 to 90% RH
- Areas where there are sharp changes in humidity or where condensation forms
- Areas where there are corrosive gases or flammable gases
- Areas where there are conducting powders such as dust or iron dust, and where there is oil mist, salt, or organic solvents
- Locations that are struck by direct sunlight
- Areas where there are strong electric fields or strong magnetic fields
- Areas where direct vibrations or shocks will be transmitted to the module

**CAUTION**

- Use the PLC in the environment given in the general specifications section of this manual. Using the PLC outside the range of the general specifications may result in electric shock, fires, or erroneous operation or may damage or degrade the product.

4.6 Self-Diagnostic Test

This section explains the self diagnostic test that is used to check the E71's transmission and reception functions and its hardware.

4.6.1 Self-Loopback Test

This section explains the self loopback test that is used to check the hardware that includes the E71's transmission and reception lines.

The self loopback test is a check that transmits a test message to the E71's own node via a line, and that can receive the same message via the network.

Following is an explanation of the self loopback test. This test is conducted in approximately 5 seconds.

Self wrap test method

- ① Connect the E71 to the line. (Refer to Item 4.7)
- ② Set the operation mode setting switch on the front of the E71 to the 2 position.
- ③ Set the PLC CPU's RUN/STOP key switch to STOP.
- ④ When the PLC CPU is reset, the self loopback test will begin.
(The TEST LED is lit.)

Test results

The test results can be determined from the LEDs on the front of the E71.

- ① If the TEST LED is turned off, then the self loopback test is completed.
- ② The test results can be checked using the TEST ERR.'s LED.
When normal LED is turned off
When there is an error The LED is lit
- ③ The following can be suspected causes of errors.
 - E71 hardware error
 - Ethernet line error
 - External supply power 12VDC's error (Only 10BASE5)

Operation after test is completed

The PLC CPU can be reset after the on-line mode or another test mode is changed over using the operation mode setting switch on the front of the E71.

Point

There will be no hardware interference if the self loopback test is conducted while another mode is on-line. If there is a packet in the line, this test may not finish within approximately 5 seconds if interference occurs with the packet. In this case, perform the test after terminating the data exchange with the remote node.

4.6.2 RAM Test

This section explains the RAM test that is performed to check the E71's RAM.

RAM test method

- ① Set the operation mode setting switch on the front of the E71 to the 3 position.
- ② Set the PLC CPU's RUN/STOP key switch to the STOP position.
- ③ When the PLC CPU is reset, the RAM test will begin.
(TEST LED is lit.)

Test results

The test results can be determined from the LEDs on the front of the E71.

- ① If the TEST LED is not lit, the RAM test is completed.
- ② The test results can be checked using the TEST ERR.'s LED.
When normal The LED is not lit
When error The LED is lit
- ③ The following can be suspected as causes of an error.
 - E71 hardware error
 - RAM error

Operation after the test is completed

Reset the PLC CPU after changing the operation mode setting switch on the front of the E71 to either the on-line mode or the other test mode.

Point

When there is an error for the test results of the RAM test shown in this item, reconduct the same test.

If an error occurs a second time, then a E71 hardware error can be suspected. For details regarding troubles, please consult with you nearest branch or agent.

4.6.3 ROM Test

This section explains the ROM test that is used to check the E71's ROM.

ROM test method

- ① Set the operation mode setting switch on the front of the E71 to the 4 position.
- ② Set the PLC CPU's RUN/STOP key switch to the STOP position.
- ③ The ROM test will begin after the PLC CPU is reset.
(TEST LED is lit.)

Test results

The test results can be determined from the LEDs on the front of the E71.

- ① If the TEST LED is turned off, the ROM test is completed.
- ② The test results can be checked using the TEST ERR.'s LED.
When normal LED is turned off
When error LED is turned on
- ③ The following can be suspected as causes of an error.
 - E71 hardware error
 - ROM error

Operation after test is completed

Reset the PLC CPU after changing the operation mode setting switch on the front of the E71 to either the on-line mode or another test mode.

Point

If there is an error for the test results of the ROM test shown in this item, reconduct the same test. If an error is again generated, then a E71 hardware error can be suspected. For details regarding troubles, please consult with the branch office or agent nearest you.

4.7 Connecting to the Network

This section explains the method for connecting the E71 to the network.

4.7.1 Connection Precautions

1 Installing

- ① Sufficient safety precautions are required when installing network. Consult with a specialist when installing connection cable terminal processing or trunk line cables, etc.
- ② Use a connection cable that meets the standard shown in Item 2.3.
The allowable bending radius for coaxial cables is set. When bending coaxial cables to connect them, a space that is larger than the coaxial cables' allowable radius is required. For information regarding the coaxial cables' allowable bending radius, please consult the cable manufacturer.
- ③ When connecting the E71 to a 10BASE2 or a 10BASE5, it is necessary to connect a terminator at both ends of the line (both are 50 Ω).
 - Connect a terminator for a 10BASE2/10BASE5 matching the type of the line on the E71 side (both are 50 Ω).
 - For the connection of terminators on the remote node side, connect them by referring to the instruction manual of the remote node. (Do not connect terminators if termination is set by means of terminal setting.)
- ④ When the A1SJ71E71N-B5 is in use and the external power supply side of the power supply source for the transceiver is handling the FG signal, ground the FG signal on the external power supply side.

2

Compliance with the EMC Directive and the Low-Voltage Directive

To comply with the EMC Directive and the Low-Voltage Directive by incorporating a E71 into the customer's product, install a ferrite core according to the method shown in (4) below.

(Also refer to the "Conformation to the EMC Directive and Low Voltage Instruction" (section before Chapter 1).)

3

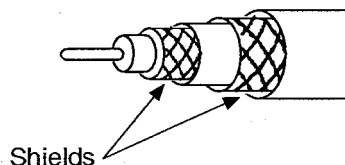
Countermeasures against high-frequency noise

The following countermeasures are available for communication errors that may occur due to high-frequency noise in an installation environment:

- ① Install a ferrite code according to the method shown in (4) below.
- ② Make the cable as short as possible.
- ③ Use the following cable for connection to a 10BASE-T.

Model name	Cable	Category
AJ71E71N3-T, A1SJ71E71N3-T	Shielded twisted cable (STP)	3, 4, 5
AJ71E71N-T, A1SJ71E71N-T	Unshielded twisted cable (UTP)	3, 4, 5

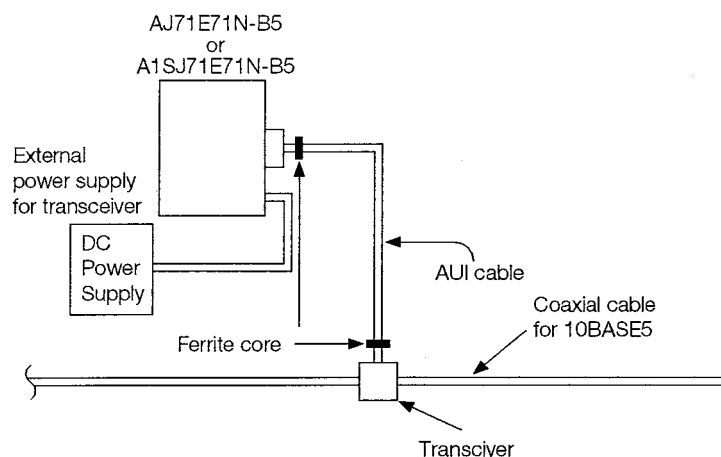
- ④ Use a double-shielded coaxial cable when connecting a E71 to a 10BASE2.



- ⑤ When connecting a E71 to a 10BASE5/10BASE2, ground the shields of the coaxial cable on both the self station side and the connection counterpart device side. (Ground at a location near the connector.)
 - * When using a double-shielded coaxial cable, ground the outer shield.
 - * For grounding on the PLC side, refer to Chapter 3, "EMC Directive and Low-Voltage Instruction" of the user's manual (hardware edition) of the CPU module used.
- ⑥ To perform TCP/IP communication, increase the number of communication retry count. (Specify it in the initial processing of the E71. Refer to *1 at the end of Item 5.3.1.)

4**Ferrite core installation**

- ① Install a ferrite core (*1) at the E71 side and at the external device side/transceiver side of the AUI cable.
 - *1: ZCAT 2032-0930 manufactured by TDK Corporation is usable.
- ② Ferrite cores should be installed as follows for connecting to a network via the 10BASE5.

**CAUTION**

- Do not bundle the control wires and the communication cables with the main circuit and the power wires, and do not install them close to each other. They should be installed 100mm (3.9 inch) or more from each other. Not doing so could result in noise that would cause erroneous operation.
- Do not connect the AUI cables when the module installation station's power is turned on.
- Be sure to fix communication cable and power cables leading from the module by placing them in the duct or clamping them. Cables not placed in the duct or without clamping may hang or shift, allowing them to be accidentally pulled, which may result in a module malfunction and cable damage.
- When detaching the communication cable or power from the module, do not pull the cable portion. For cables with connectors, hold the connector at the junction to the module, then detach it. For connectors without connectors, first loosen the screw at the junction, then detach the cable. Pulling the cable portion while it is connected to the module may cause a malfunction or damage to the module and cable.

4.7.2 Connecting to 10BASE5

This section explains the method for connecting E71 to the 10BASE5 network.

(Applicable modules in the explanation: AJ71E71N-B5, A1SJ71E71N-B5)

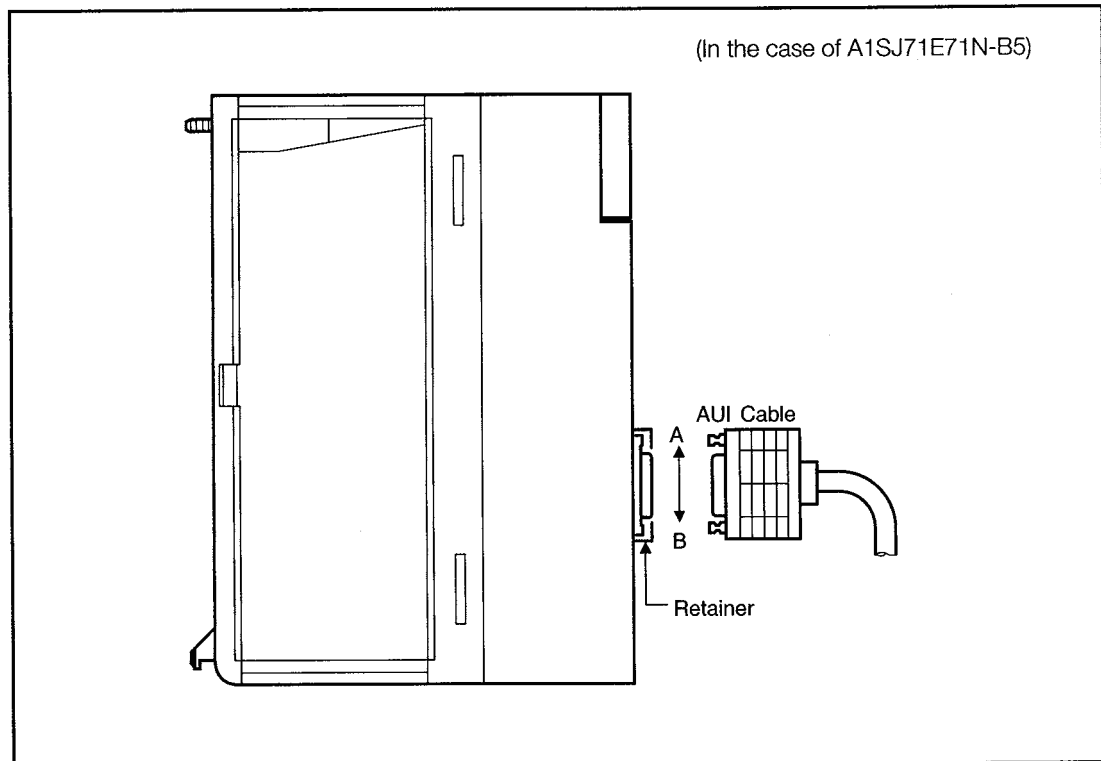


Fig 4.1 AUI Cable Connection Diagram

Method for connecting to the AUI Cable

- (1) Slide the retainer in the A direction as shown in Fig 4.1.
- (2) Push the AUI cable connector to the back.
- (3) Slide the retainer in the B direction as show in Fig 4.1.
- (4) Check that the AUI cable is locked.
- (5) Turn on the transceiver's power. (*1)

*1 Please use transceivers that have functions that are generally called SQETEST or Heartbeat (transceiver function that uses a signal to check if the transceiver is operating correctly after transmission).



CAUTION

- Do not connect the AUI cable when the module installation station's power is turned on.

Point

- (1) When connection to the network is made using the 10BASE5, if countermeasures against high-frequency and noise generated in the installation environment of E71 is necessary, attach a ferrite core to the AUI cable to eliminate these effects.
Refer to (4) in Item 4.7.1 .
- (2) For the devices that are required for connecting a E71 to a 10BASE5 and examples of system configuration, refer to (1) in Item 2.3.

4.7.3 Connecting to 10BASE2

This section explains the method for connecting the E71 to the 10BASE2 network.

(Applicable modules in the explanation: AJ71E71N-B2, A1SJ71E71N-B2)

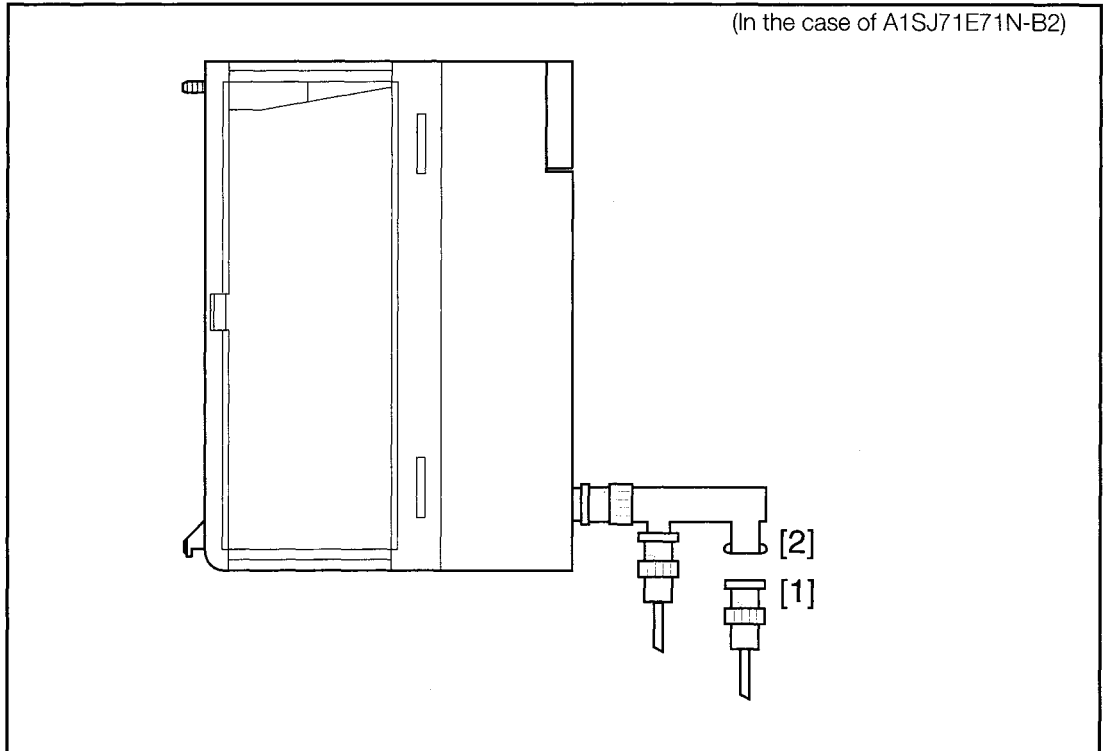


Fig 4.2 10BASE2 Coaxial Cable Connection Diagram

10BASE2 Coaxial cable connection method

- (1) As shown in Fig 4.2, line up and push in the lip [2] into the groove [1].
- (2) Turn the connector 1/4 rotation to the right while pushing it in.
- (3) Turn the connector until it locks.
- (4) Check that the connector is locked.

Point

For the devices that are required for connecting a E71 to a 10BASE2 and examples of system configuration, refer to (2) in Item 2.3.

Remarks

Coaxial cable connector connection

This section explains the method for connecting the BNC connector (coaxial cable connector plug) to the cable.

(1) BNC connector and coaxial cable configuration

Fig 4.3 shows the BNC connector and coaxial cable configuration.

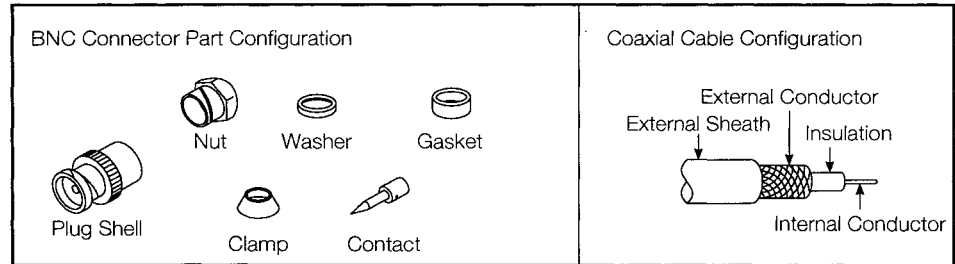
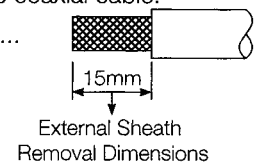


Figure 4.3 BNC Connector and Coaxial cable configuration

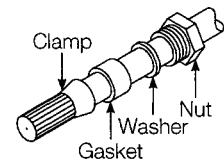
(2) Method for connecting the BNC connector and the coaxial cable

The following shows a method for connecting the BNC connector to the coaxial cable.

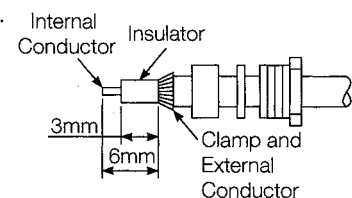
- (a) Remove the coaxial cable's external sheath as shown in the drawing at right. Be careful not to damage the external conductor.



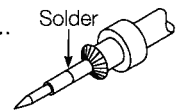
- (b) Place the nut, washer, gasket, and clamp on the coaxial cable as shown in the drawing at right, and then wrap the external conductor.



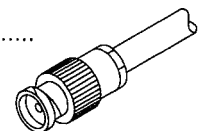
- (c) Cut the external conductor, insulator, and internal conductor to the dimension as shown in the drawing at right. However, to cut the external conductor to the same dimension as the clamp's tapered portion, place the clamp on before cutting.



- (d) Solder the connector to the internal conductor.



- (e) Insert the conductor assembly in (d) into the plug shield, and screw on the plug shield nut.

**Point**

Take the following precautions when soldering the contact to the internal conductor.

- (1) Be sure that the solder does not creep up the soldered area.
- (2) Be sure that there are no gaps in or biting into the conductor and cable's insulation.

4.7.4 Connecting to 10BASE-T

This section explains the method for connecting the E71 to the 10BASE-T network.

(Applicable modules in the explanation: AJ71E71N3-T, AJ71E71N-T, A1SJ71E71N3-T, A1SJ71E71N-T)

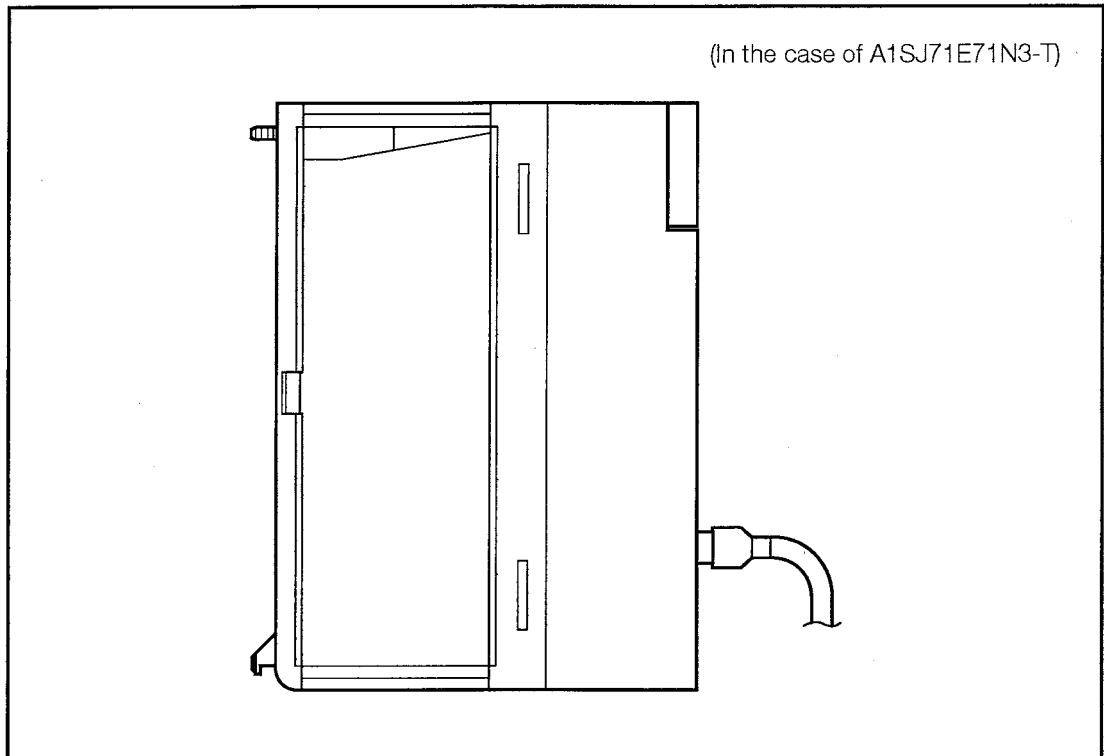


Fig 4.4 10BASE-T Twisted-Pair Cable Connection Diagram

Method for connecting the 10BASE-T twisted-pair cable

- (1) Connect the twisted-pair cable to the hub.
- (2) Connect the twisted-pair cable to the E71.

Point

- (1) The E71 operates in the half duplex communication mode of the 10BASE-T.
- (2) For the devices that are required for connecting the E71 to the 10BASE-T and examples of system configuration, refer to (3) in Item 2.3.

4.8 Loopback Test

The loopback test is a function that tests whether exchange is conducted normally between a node and the local station E71. When a loopback test is conducted, the data transmitted from the remote node is then retransmitted as is by the E71 as a response to the originating station. A loopback test will be conducted using the function shown in Item 10.7 after the initial processing and open processing finish normally.

4.9 Maintenance and Inspection

There are no inspection items for the E71 other than the terminator and checking the cable connections for looseness. In addition to this, to keep the system in good condition at all times and conduct the inspection items contained in the PLC CPU's module's user manual.



DANGER

- Do not touch the terminals while the electricity is on. Doing so could cause erroneous operation.
- Make sure to switch all phases of the external power supply off before cleaning or re-tightening screws.
If you do not switch off the external power supply, it will cause failure or erroneous operation of the module.
If the screws are loose, it may result in fallout, short circuits, or erroneous operation. Tightening the screws too far may cause damage to the screw and/or the module, resulting in fallout, short circuits, or erroneous operation.



CAUTION

- Do not disassemble or rebuild the module. It may cause failure, erroneous operation, injury, or fire.
- Make sure to switch all phases of the external power supply off before mounting or removing the module.
If you do not switch off the external power supply, it will cause failure or erroneous operation of the module.
- Do not touch the electronic parts or the unit conducting area.
It may cause erroneous operation or failure.

Points

- (1) In general, the devices (e.g., E71, communication board) that are connected to an Ethernet store the Ethernet address of the counterpart device with which they have communicated. Therefore, if these devices are replaced, it is necessary to update the Ethernet address of the local station stored by the communication counterpart device. Since these devices do not immediately update the Ethernet address of the communication counterpart device under normal operating condition, it will lead to a communication error. To prevent it, be sure to restart the E71 after replacing the communication board/module on the remote node side that has opened a connection with the E71.
[Restart procedure on the E71 side]
- ① End all communications with remote nodes, and close all connections currently open. (Set the open request signal to OFF.)
 - ② After all open completion signals are set to OFF, perform the end processing of the E71. (Set the initial request signal to OFF.)
 - ③ After the initial normal completion signal is set to OFF, perform the initial processing of the E71. (Set the initial request signal to ON.)
 - ④ After the initial normal completion signal is set to ON, perform open processing to a remote node with which communication is to be established. (Set the open request signal to ON.)
- (2) If a defective E71 is replaced, restart the remote node side below in accordance with the specification, and resume data exchange.
- All remote nodes that were communicating with the station in which the replaced E71 was installed
 - All remote nodes that were communicating with a remote station's PLC via the station in which the replaced E71 was installed

This image shows a single sheet of white paper with horizontal ruling lines. The lines are evenly spaced and run across the width of the page. There are no margins, text, or other markings on the paper.

5. PROCEDURES FOR EXCHANGING WITH REMOTE NODES

5.1 Overview of Exchange Procedures

This section shows the general procedure used to exchange data between the E71 and a remote node.

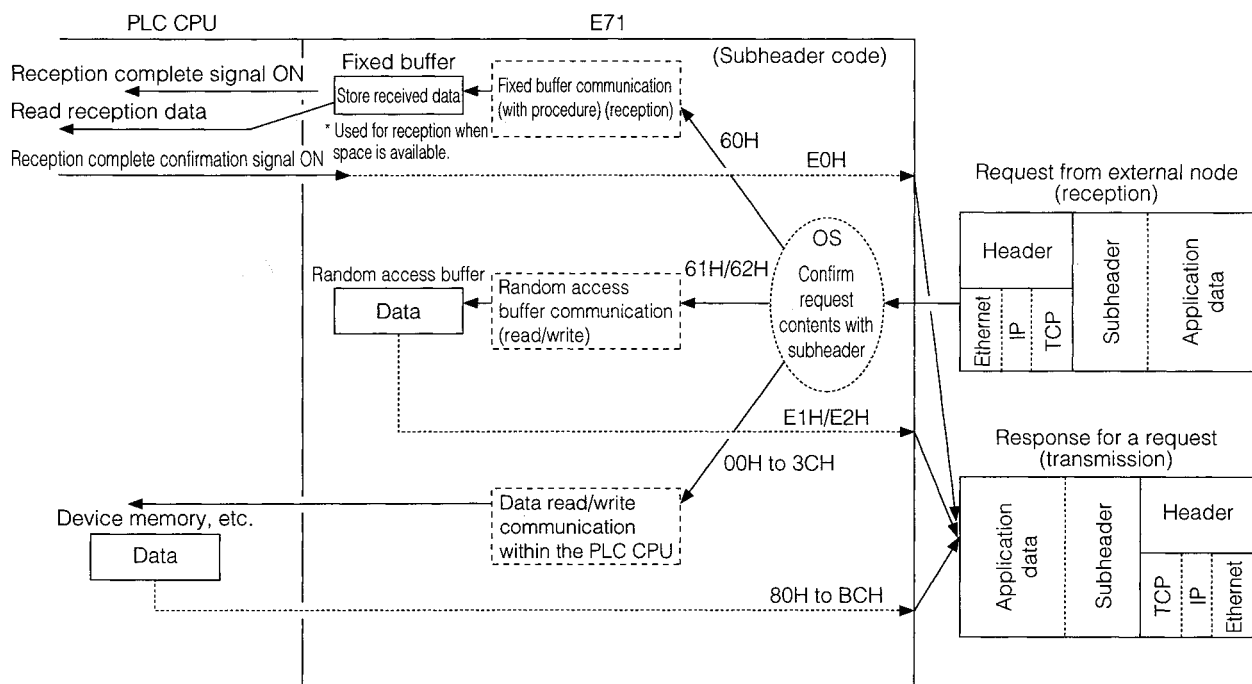
To begin data exchange, the initial processing and open processing must be used to connect with the exchange partner and the communication line.

To end data exchange, conduct close processing and end processing. This disconnects the communication line and ends all exchange processing.

Point

- (1) When conducting either fixed buffer exchange, random access buffer exchange, or reading and writing to the PLC CPU, open processing must be conducted with an exchange remote node. In addition, all of the above three types of exchange can be conducted with user opened remote nodes.

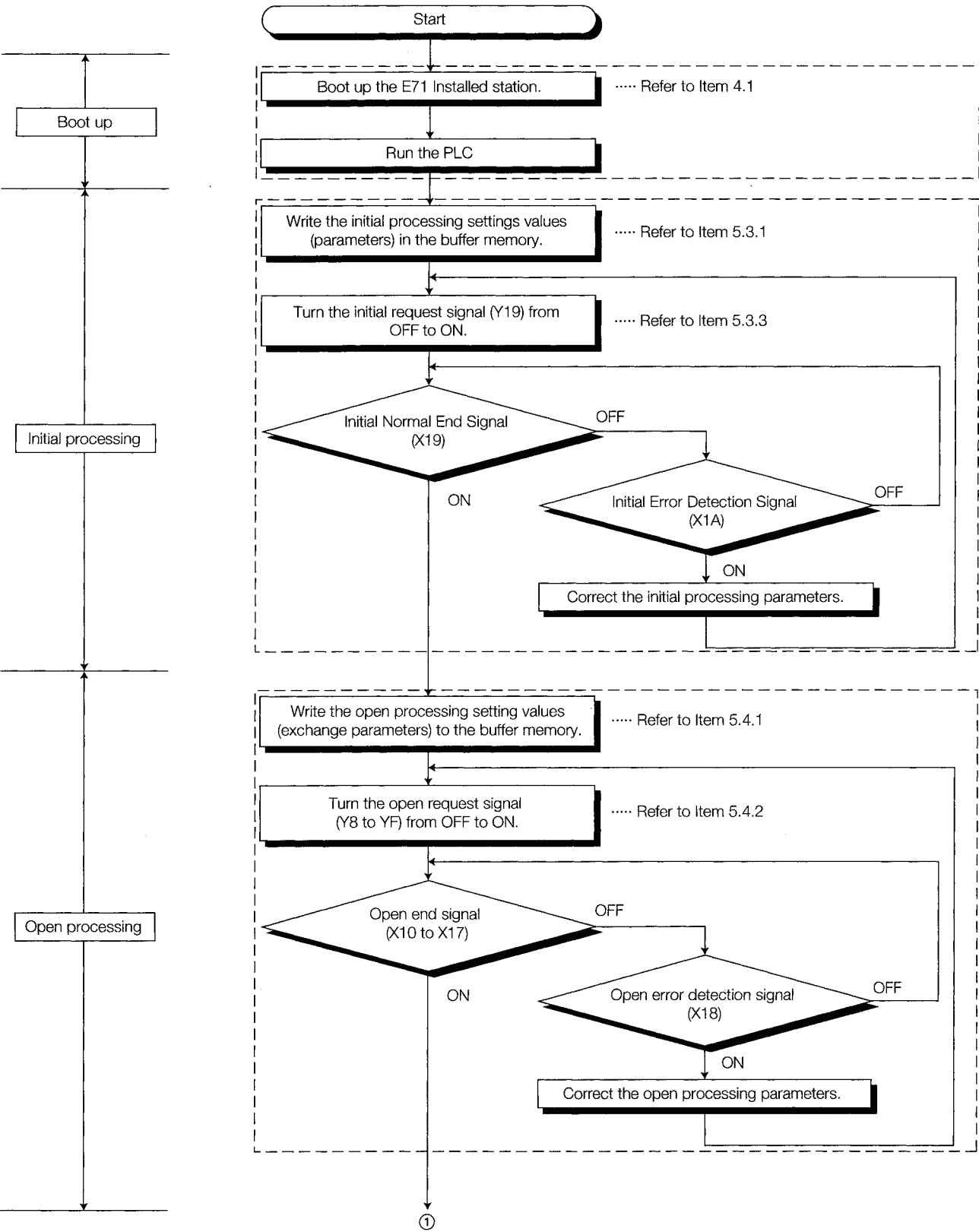
When receiving communication request data from an external node

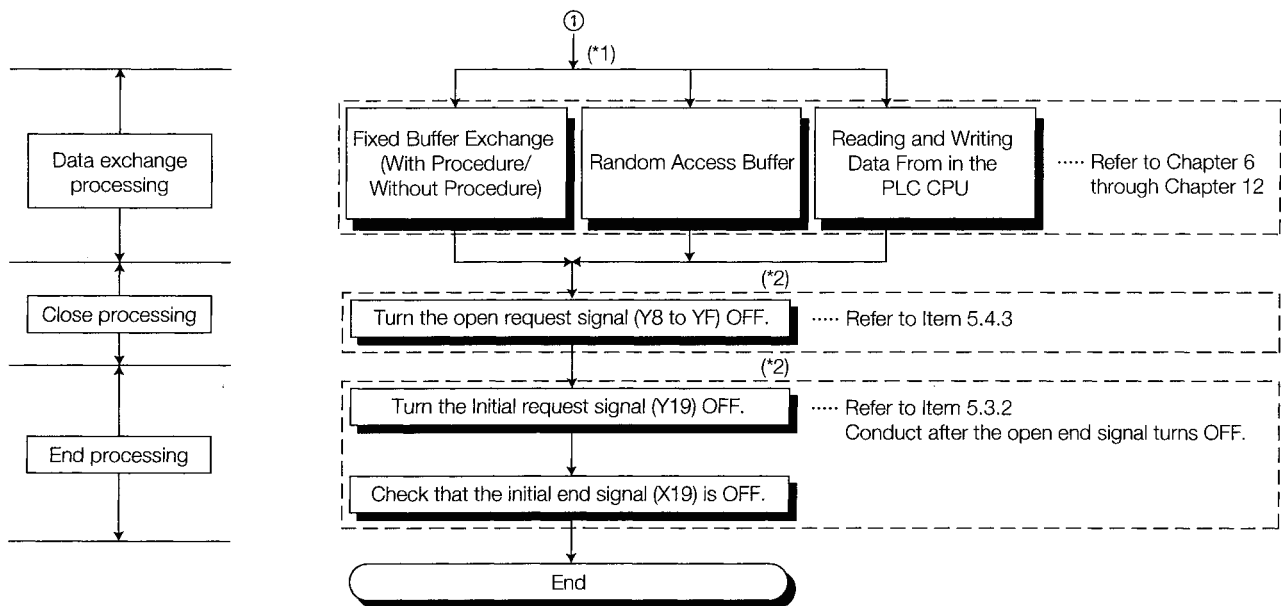


- (2) Open processing can be conducted for a maximum of eight nodes. However, when translating and receiving with the same remote node using a fixed buffer, two fixed buffers are required so the number of nodes to which exchange can be conducted is reduced.
- (3) The following explains the initial processing and the open processing when exchange prohibited is set by the Exchange Specification During STOP using the E71 buffer memory (address: 496).
 - When an E71 installation station's PLC CPU is in the STOP state, the E71's open request signal (Y8 to YF) and initial request signal (Y19) are also turned off and the line to the remote node is closed.
 - When the E71 installed station's PLC CPU is changed from STOP to RUN, reconduct initialization processing and open processing.

(Exchange procedures)

Initial processing, open processing, and data exchange PLC programs for the exchange procedure after the E71's installed station's PLC CPU write processing is completed.





*1 Once a communication line is connected, the following data communication can be performed between E71 and other destination nodes using the port number specified by the communication parameter during the open processing.

① When "procedural fixed buffer communication" is specified during the open processing (When bit 9 of the usage setting communication parameter is OFF (procedural))

Functions that can be communicated using the applicable connection [Refer to Item 3.5.2 for data transmission procedures.]	1	Communication using fixed buffers (Refer to Chapter 6.) (a) Either transmission or reception is possible. * This is determined based on the setting for bit 0 of the usage setting communication parameter. • When bit 0 of the usage setting is OFF, transmission is enabled. • When bit 0 of the usage setting is ON, reception is enabled. (b) Data transmission or reception between the PLC CPU and other nodes is performed using a fixed buffer (buffer memory) of the same number as the connection number of the open request signal sent when the communication line was opened. * The connection number of the open request signal is compatible with the fixed buffer number. (c) When transmitting and receiving data between the E71 and other nodes, two communications lines are required.
	2	Communication using random access buffer (Refer to Chapter 8.) Data is read and written from/to the random access buffer of E71.
	3	Read/write communication with respect to the data within the PLC CPU. (Refer to Chapters 9 and 10.) Data is read and written from/to the device memory of PLC CPU.

② When "non-procedural fixed buffer communication" is specified during the open processing (When bit 9 of usage setting communication parameter is on (non-procedural))

- Communication (transmission or reception) can be performed only by using the fixed buffer (Refer to Chapter 7).
- The number of fixed buffers used and the number of communication lines required for data transmission and reception are the same as those required for procedural fixed buffer communication.

*2 When exchange enable is set using Exchange Specification During STOP using the E71 buffer memory (address: 496) when the open request signal (Y8 to YF) and the initial request signal (Y19) are off, the following data exchange can be continued.

- Exchange using the random access buffer
- Exchange of data read/write in the PLC CPU

Continue data exchange in accordance with Item 5.6.

5.2 Connecting and Disconnecting Communication Lines

The arrangement between the nodes makes it necessary when beginning data exchange to connect a communication line between exchange partners, and when the data exchange is completed, to disconnect the communication line between exchange partners.

Following is an explanation of the connecting and disconnecting of E71 communication lines and of the initial processing setting data for exchanging data between the E71 and a remote node.

1

Connecting communication lines (Initial processing, open processing)

- (a) Conduct E71 initial processing and open processing with the remote node and connect to the communication line using the parameters and switch settings specified by the user. Exchange can only be conducted with the remote mode for which the line was connected using this initial processing and open processing.
- (b) Exchanging with a remote node using the fixed buffer, exchange using the random access buffer, and reading/writing the data in the PLC CPU is possible using the port No. specified during open processing when the communication line is connected.

2

Communication line disconnect (Close processing, end processing)

When data exchange with a remote node has been completed after a communication line was connected, the communication line is disconnected .

- (a) Disconnection of the communication line by user processing
close processing and end processing are conducted by the PLC CPU.
- (b) Disconnection of the communication line by error occurring
If the condition described in Item 3.5.3 occurs, the line will be forcefully closed.
End processing is conducted by the PLC CPU.

Point

Data exchange during the PLC CPU is in the STOP status

- (1) The following data exchange can be continued even when the PLC CPU of the station installed in the E71 is in the STOP status by setting exchange enable at the "Exchange Specification During STOP" using the E71 buffer memory (address: 496).
 - Exchange using random access buffer
 - Read/write exchange of the data in the PLC CPU
- (2) Continue data exchange or conduct reopening in accordance with Item 5.6.

5.3 Initial Processing and End Processing

This section explains about the E71 initial processing and end processing conducted by the PLC CPU when connecting to a communication line and exchanging data with a remote node.

Connect to the line using the normal online operation.

5.3.1 Data for Initial Processing

This section explains the parameter setting area for conducting initial processing. The network manager (the person who plans the network and manages the IP addresses) writes the values to be used in this area before initial processing is conducted. (Refer to Point at the end of Item 3.7.2 about whether the parameter should be set.)

Buffer Memory

(Address)		Initial Processing Parameter Setting Area	(16 Words)	Default Values
0 to 1H (0 to 1)	1)	Local station E71's IP address	(2 words)	0H (0)
2H (2)	2)	Special function settings	(1 word)	0H (0)
3H (3)	3)	Timer setting time units	(1 word)	7D0H(2000)
4 to 5H (4 to 5)	5)	System area (Use prohibited)	(2 words)	—
6H (6)	6)	TCP Maximum Segment transmission setting (*2)	(1 word)	8000H
7H (7)	7)	Destination existence check start interval timer value	(1 word)	12CH (300)*1
8H (8)	8)	Destination existence check interval timer value	(1 word)	5H (5)*1
9H (9)	9)	Number of retransmit tries for destination existence check	(1 word)	3H (3)
AH (10)	10)	TCP/ULP time out value	(1 word)	FH (15)*1
BH (11)	11)	TCP zero window timer value	(1 word)	5H (5)*1
CH (12)	12)	TCP retransmit timer value	(1 word)	5H (5)*1
DH (13)	13)	TCP end timer value	(1 word)	AH (10)*1
EH (14)	14)	IP setup timer value	(1 word)	3H (3)*1
FH (15)	15)	Response monitoring timer value	(1 word)	FH (15)*1

*1 The setting of value units during default 2s can be changed to 500 ms.

(Timer value = setting value × 2 s or 500 ms)

*2 Applicable to the product whose software version is as follows.

AJ71E71N3-T, A1SJ71E71N3-T: "Version A" or later

E71 other than above: "Version E" or later

For the product whose software version is earlier than that, the TCP Maximum Segment transmission function is not available. This area is used as a system area.

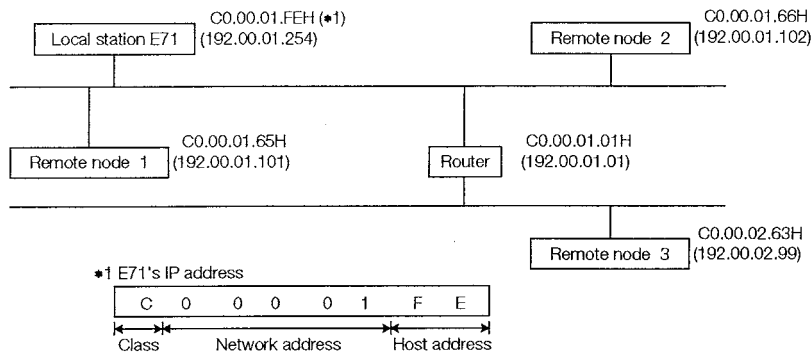
1

Local station E71's IP address (Default Value = 0H) Address 0H to 1H (0 to 1)

(a) The local station E71's IP address is set following the standard IP address (Refer to Item 11.3).

- ① Set it so that the local station E71 and the partner remote node to which exchange is being conducted are set to the same class network address. In the following example, the E71 and the partner remote node ① ② IP address class network address is set at "C00001□□H." (The host address can be freely set to any No. other than "00H" and "FFH.")
- ② When the IP address network address of the local station E71 and the partner remote node to which exchange is being conducted are not the same, a setting for using the router relay function is required (Refer to Chapter 12). In the following example, the setting values (parameters) used for the router relay function when exchange is conducted between the E71 and the remote node ③ are set in the local station E71.

(Example) When the local station E71's IP address is class C (upper level: displayed in hexadecimal numbers, lower stage : displayed in decimal numbers).



- (b) Except when a router relay function (Refer to Chapter 12) , the IP addresses can be freely allocated as described in (a) above.

Remarks

When the router relay function is used, please use addresses that conform with the standard IP addresses used on the global scale.

* Standard IP Address

IP addresses are divided into classes to allow an address system that corresponds to the size of the network to be selected. (Refer to Item 11.3)

2

Special function setting (Default value = 0H) Address 2H (2)

- (a) Sets whether a router relay function is used.
(Static router relay)
- (b) Validates the buffer memory's subnet mask setting area and router relay parameter setting area (address 448 to 472) setting value when set to use the router relay function.

(Bit Position)	b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
																①

- ① Router relay function setting (b0)
0 : Not used (Default value)
1 : Used

3

Timer setting time units (Default value = 7D0H (2000)) Address 3H (3)

- (a) The timer value units set to the buffer memory address 7 to 15 can be set to either 500ms unit or 2s unit. (Default value 7D0H (2000) is shown as the 2s unit.)
- (b) Specify the setting value as 1F4H (500) or other than 1F4H.
1F4H (500) : 500 ms unit
Other than 1F4H : 2s (2000ms) unit
- (c) The timer values set by the buffer memory addresses 7 to 15 using the timer setting time units are specified for the following ranges.

Timer setting time units	Timer setting values setting possible range	Timer time range
2s (2000ms)	1 to 8191 (1 to 1FFFH)	2.0s to 16382.0s
500ms	1 to 32767 (1 to 7FFFH)	0.5s to 16383.5s

* Current timer operation cannot be guaranteed if setting values outside the above range are set.

- (d) The timer times set in buffer memory addresses 7 to 15 are as follows.

Timer time = Timer setting value × timer setting time units

(Example) When the TCP/ULP time out value setting value is 15

- ① When the timer setting time unit is 2s : 15 × 2s = 30s (30000ms)
② When the timer setting time unit is 500ms : 15 × 500ms = 7500ms

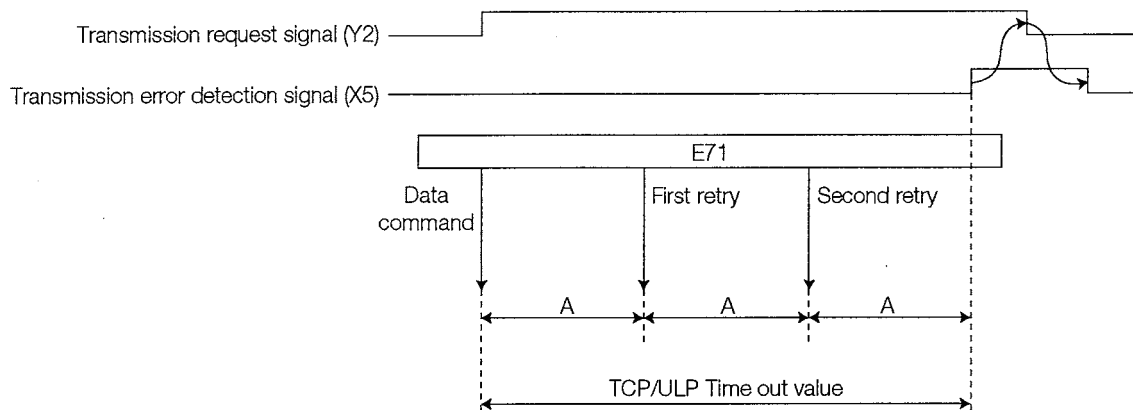
- 4 TCP Maximum Segment transmission setting (Default value = 8000H) Address 6H(6)**
- (a) Set whether TCP Maximum Segment transmission will be made or not for TCP transmission.
 - (b) Specify the setting with either of the following values.
 - 0H: Enable TCP Maximum Segment Size Option transmission.
 - 8000H: Disable TCP Maximum Segment Size Option transmission.
 - (c) The setting is made valid after completion of the initial processing.
 - (d) MELSOFT series products supporting the TCP Maximum Segment transmission function
For setting the "Enable TCP Maximum Segment Size Option transmission", use the following MELSOFT products.
 - GX Developer : Version 8.07H or later
 - MX Component : Version 3.03D or later
 - MX Links : Version 3.08J or later
 - (e) MELSOFT series products not listed above (d)
When using a MELSOFT product that is not listed above (d) to make communication via Ethernet, set it to "Disable TCP Maximum Segment Size Option transmission" or use UDP/IP type communication.
Otherwise, the sequence program may not function correctly (read/write is incorrect).
- 5 Destination existence check begin interval timer value (Default value = 12CH (300), setting time = setting value × (Unit)) Address 11H (17)**
- (a) When the exchange with the partner remote node by the connection opened by destination existence check is finished until existence check is begun.
 - (b) The setting value is set to 1H to 1FFFH/1H to 7FFFH by the timer setting time's unit setting. (10 minutes when the default value is 12CH(300)) (*2)
- 6 Destination existence check interval timer value (Default value = 5H (5), setting time = setting value × (Unit)) Address 8H (8)**
- (a) Sets the interval time for conducting retry and existence check when no response is received from the partner remote node that is conducting the existence check for the connection opened by the destination existence check.
 - (b) The setting value is set to 1H to 1FFFH/1H to 7FFFH by the timer setting time's unit setting.
- 7 Number retries for destination existence check (Default value = 3H (3)) Address 9H (9)**
- (a) Sets the number of retries and existence checks when a response is not received from the partner remote node that is conducting the existence check for the connection that was opened by the destination existence check.
 - (b) The setting value is set to 1H to 7FFFH by the timer setting time's unit setting.
- 8 TCP/ULP time out value (Default value = FH (15), setting time = setting value × (Unit)) Address AH (10)**
- (a) Sets the pocket existence time during TCP data transmission.
This timer value is passed as a parameter when a TCP connection is opened or data are transmitted.
 - (b) The setting value is set to 1H to 1FFFH/1H to 7FFFH by the timer setting time's unit setting. (*1) (*3)

- 9** TCP zero window timer value (Default value = 5H(5), setting time = setting value × (Unit)) Address BH (11)
- (a) The window shows the reception buffer on the reception receiving end.
 - (b) When there is no more space in the reception buffer on the receiving end (window size = 0), the transmitting end waits to transmit data until there is space in the reception buffer on the receiving end. At this time, the receiving end follows the TCP zero window timer value to transmission window check packet to the reception end to check the receiving possibility condition.
 - (c) The setting value is set to 1H to 1FFFH/1H to 7FFFH by the timer setting time's unit setting. (*3)
- 10** TCP retransmit timer value (Default value = 5H (5), setting time = setting value × (Unit)) Address DH (13)
- (a) If ACK is not returned during TCP open and data transmission, the retransmission time will be set. This timer is also used for the existence time of the ARP function. (An ARP is retransmitted after the time of 1/2 TCP retransmission timer value when no response is returned to the transmitted ARP request.)
 - (b) The setting value is set to 1H to 1FFFH/1H to 7FFFH by the timer setting time's unit setting. (*1) (*3)
- 11** TCP end timer value (Default value = 3H(3), setting time = setting value × (Unit)) Address DH (13)
- (a) Sets the monitoring time when waiting for FIN to be received from the partner node after the local station has transmitted FIN and ACK has been received from the partner node when the local station closes the TCP connection.
 - (b) When FIN is not received from the partner node after the TCP end timer time, RST is transmitted to the partner node to forcefully close the line.
 - (c) The setting value is set to 1H to 1FFFH/1H to 7FFFH by the timer setting time's unit setting. (*3)
- 12** IP set up timer value (Default value = 3H(3), setting time = setting value × (Unit)) Address EH (14)
- (a) Exchange data is sometimes divided and transmitted by IP level due to the transmitting station's or the receiving station's buffer limitations. This sets the time to wait until the following divided data is restored (reassembled) when the E71 receives the divided data.
 - (b) The setting value is set to 1H to 1FFFH/1H to 7FFFH by the timer setting time's unit setting. (*3)
- 13** Response monitor timer value (Default value = FH (15), setting time = setting value × (Unit)) Address FH (15)
- (a) Sets to the following time.
 - ① The wait time from when a command is transmitted until a response is received.
 - ② When a divided message is transmitted, the time from the first message transmission until the final message is received.
 - (b) The setting value is set to 1H to 1FFFH/1H to 7FFFH by the timer setting time's unit setting. (*3)

- *1 When exchange errors occur due to noise, change the setting value to a higher number of retries.
The retry number is determined using the following formula. (For the default value $2 = (15 \div 5) - 1$)

$$\left\lceil \begin{array}{c} \text{Number} \\ \text{of retries} \end{array} \right\rceil = \left\lceil \left\lfloor \frac{\text{TCP/ULP time out value}}{\text{TCP retry timer value}} \right\rfloor - 1 \right\rceil$$

(Example) When data cannot be transmitted when the setting value makes the number of retries 2, the transmission error detection signal will turn on with the timing shown in the diagram below. (When fixed buffer No.3 is used)



A : TCP retransmission timer value

(The data retransmission time when ACK is not returned after the data is transmitted)

Remarks

When removing the retry processing shown above (0 times), perform the following setting:

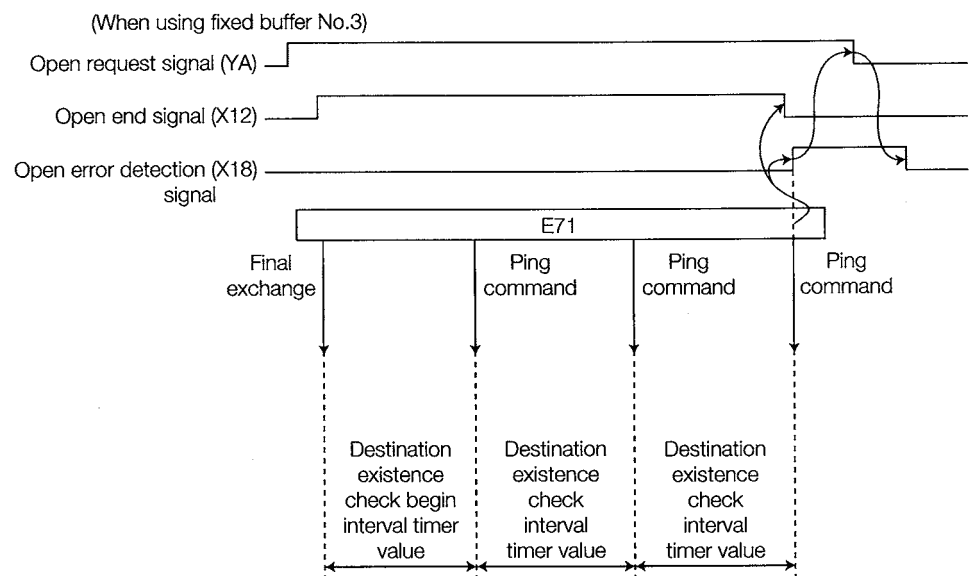
$$\left\lceil \begin{array}{c} \text{TCP/ULP} \\ \text{time out value} \end{array} \right\rceil = \left\lceil \begin{array}{c} \text{TCP end} \\ \text{timer value} \end{array} \right\rceil = \left\lceil \begin{array}{c} \text{TCP retransmit} \\ \text{timer value} \end{array} \right\rceil$$

(Each timer value should be the same.)

- *2 The destination existence check is the function that the E71 uses to check whether the partner remote node is operating correctly when exchange with the partner remote node that is opened by the connection has not been conducted for a set period of time. In particular, when exchange has not been conducted for a set period of time with the remote node, it transmits a PING command (ICMP echo request/response function) to the partner node to conduct an existence check of whether a response can be received.

The E71 conducts existence checks in accordance with the destination existence check settings (Refer to Item 5.4.1 1 (b) ②) for the setting values given in 5 to 7 in this section and during opening processing.

(Example) When the setting value is for the number of retries to be 3, the E71 conducts existence checking with the timing shown in the figure below. When errors are detected, the open error detection signal turns on and stores the error code (108H) to the open error code storage area..



In addition, if the initial processing has normally been completed, the E71 automatically sends a response for a PING command echo request command received from a remote node.

(The E71 sends a response for a PING command received even when the connection to be used for data exchange with a remote node is closed.)

- * If an error is detected in the external device side with this check, the applicable connection will be closed (the line will be disconnected). Open the connection using a user program.

- *3 The setting value of each timer is as follows.

(a) Designate the setting value of each timer on the E71 side in such a way that the following relations are met.

$$\bullet \left[\begin{array}{c} \text{Response} \\ \text{monitor} \\ \text{timer value} \end{array} \right] \geq \left[\begin{array}{c} \text{TCP/ULP} \\ \text{time out value} \end{array} \right] \geq \left[\begin{array}{c} \text{TCP end} \\ \text{timer value} \end{array} \right] \geq \left[\begin{array}{c} \text{TCP} \\ \text{retransmit} \\ \text{timer value} \end{array} \right] > \left[\begin{array}{c} \text{IP setup} \\ \text{timer value} \end{array} \right]$$

$$\bullet \left[\begin{array}{c} \text{TCP retransmit} \\ \text{timer value} \end{array} \right] = \left[\begin{array}{c} \text{TCP zero window} \\ \text{timer value} \end{array} \right]$$

Furthermore, when connecting a line using our products (E71, AJ71E71, QE71), you should make sure that both nodes have the same settings.

- (b) Designate the setting value of each timer on the remote node side in such a way that the following relations are met.

Communication errors such as transmission timeouts may occur more frequently if the timer values are not set so that they satisfy the following relationships.

$$\begin{aligned} & \bullet \left[\begin{array}{c} \text{TCP retransmit timer value} \\ \text{on the remote node side} \end{array} \right] > \left[\begin{array}{c} \text{TCP retransmit timer value} \\ \text{on the E71 side} \end{array} \right] \\ & \bullet \left[\begin{array}{c} \text{Monitoring timer value in application} \\ \text{software on the remote node side} \end{array} \right] > \left\{ \left[\begin{array}{c} \text{TCP/ULP time out value} \\ \text{on the E71 side} \end{array} \right] \times n \times *1 \right\} \\ & \quad *1 \text{ } n \text{ is the number of TCP segment transmissions and can be obtained via the following calculation:} \\ & n = \left\lceil \frac{\text{Size of the message transmitted by E71}}{\text{Maximum Segment size}} \right\rceil \text{ fractions below decimal point are rounded up} \end{aligned}$$

(Example 1) Number of TCP segment transmissions when communicating via the same line

The Maximum Segment size is 1460 bytes via the same line (without going through a router) and the number of TCP segment transmissions is as follows:

$n = 1$, if the size of the message transmitted by E71 is 1460 bytes or less.

$n = 2$, if the size of the message transmitted by E71 is greater than 1460 bytes.

(Example 2) Number of TCP segment transmissions when communicating via separate lines

The Maximum Segment size is at least 536 bytes on a separate line (via dialup router, etc.) and the number of TCP segment transmissions is as follows:

$n = 1$, if the size of the message transmitted by E71 is 536 bytes or less.

$n = 2$, if the size of the message transmitted by E71 is greater than 536 bytes and no more than 1072 bytes.

$n = 3$, if the size of the message transmitted by E71 is greater than 1072 bytes and no more than 1608 bytes.

Remarks

In the above relationship, the number of retries for transmission from E71 can be increased or decreased by changing the TCP retransmit timer value (refer to *1).

By performing the following setting, number of retries becomes 0.

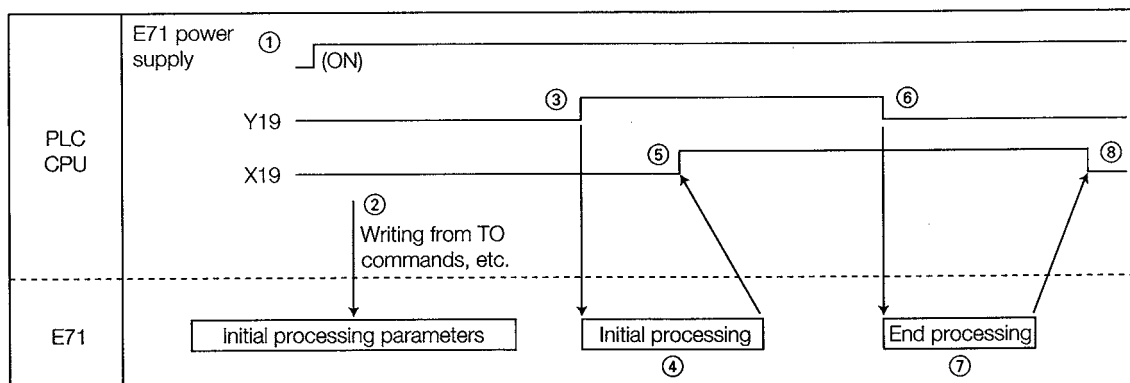
$$\bullet \left[\begin{array}{c} \text{TCP/ULP} \\ \text{time out value} \end{array} \right] = \left[\begin{array}{c} \text{TCP end timer} \\ \text{value} \end{array} \right] = \left[\begin{array}{c} \text{TCP retransmit} \\ \text{timer value} \end{array} \right]$$

Point

- (1) It is recommended that the default values are used for each timer value. Before changing them, consult with the managers of the partner equipment and systems, then increase/decrease each set value according to the above equation.
- (2) Refer to the Point in Item 3.7.2 for information regarding the parameters necessary for settings during E71 initialization processing when the E71 functions are used.

5.3.2 Initial Processing and End Processing Procedures

This section explains about the E71's initial processing and end processing procedures.



Initial processing

- ① Boots up the E71 installation station (turns the power on, etc.), and puts the PLC CPU in the RUN state.
- ② The initial processing parameters are written in the buffer memory.
 - * When the initial processing parameters are written, it is also all right to write the various setting values such as exchange parameters and router relay parameter setting area, etc.
- ③ The PLC program turns the initial request signal (Y19) on.
- ④ The E71 executes initial processing. The initial processing results are stored in the initial processing status storage area. (Refer to Item 5.5.1)
- ⑤ The initial normal end signal (X19) turns on when the initial processing ends normally. (A PING command can be sent from the remote node to the PLC CPU.) If it ends with an error, the initial error detection signal (X1A) will turn on.

End processing (*1)

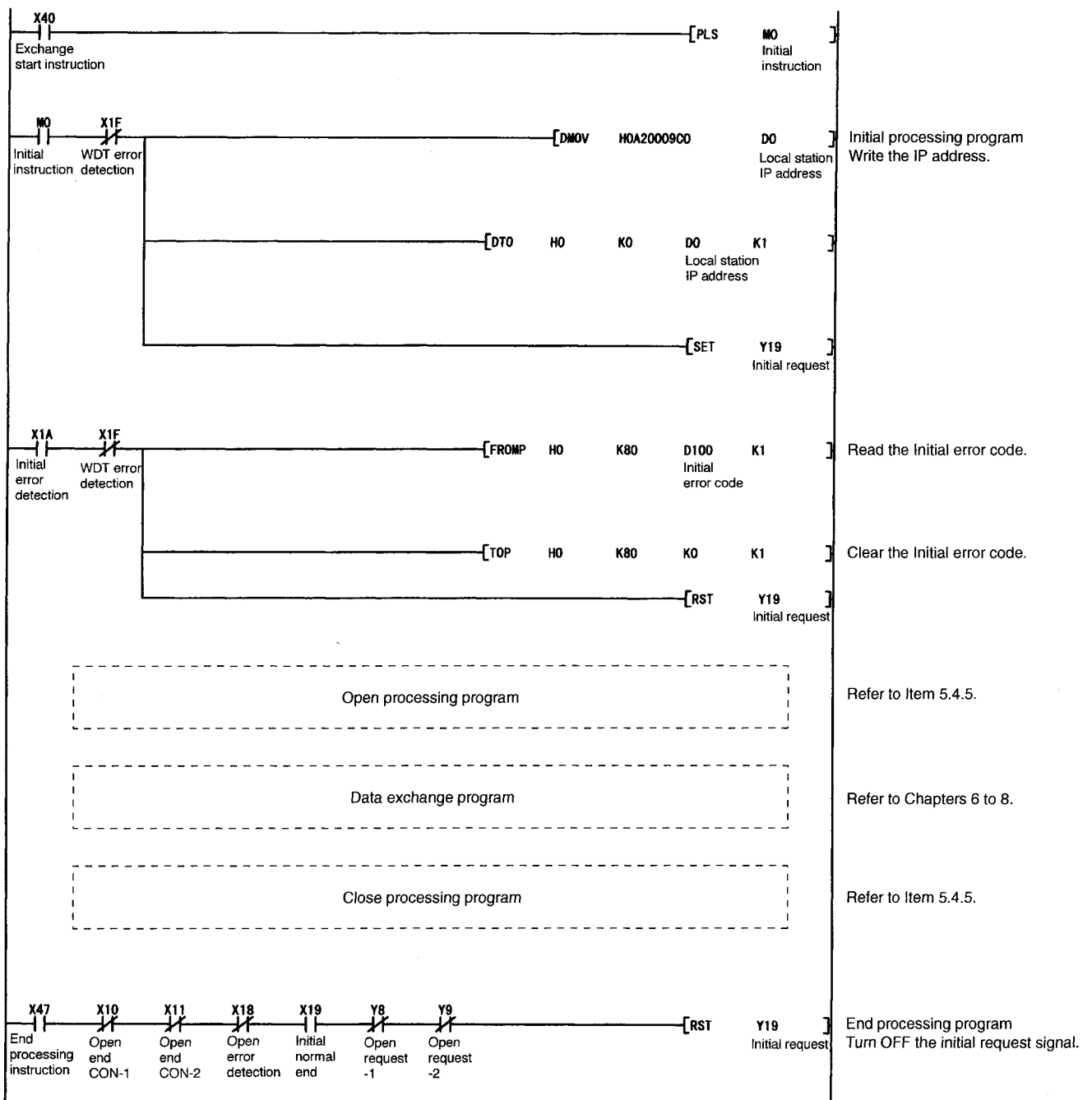
- ⑥ The sequence programmer turns the initial request signal (Y19) off after the next signal off is checked. (*2)
 - Transmission request signal/reception end check signal (Y0 to Y7)
Transmission normal end signal/reception end signal (X0, X2...)
Transmission error detection signal (X1, X3...)
 - Open request signal (Y8 to YF), open end signal (X10 to X17)
Open error detection signal (X18)
 - ⑦ The E71 executes the end processing.
 - ⑧ The initial normal end signal (X19) turns off when the end processing is normal end. When it is error end, the initial error detection signal (X1A) turns on.
- *1 This is the end processing when exchange prohibited is set by the Exchange Instruction During STOP using the E71 buffer memory (address:496). Conduct the end processing when setting exchange enable in accordance with Item 5.6.
- *2 If end processing is requested while the communication line is being connected (the connection is open), close processing will be performed for the open communication line and end processing will then be performed, if data transmission/reception with a remote node is not being performed.
- If data transmission/reception with a remote node is being performed, close processing will be performed forcibly (close processing will be performed after an ABORT (RST) instruction is sent in the case of TCP/IP communication) for the open communication line. Also, the transmission error detection signal (X1, ...) will turn on.

5.3.3 Example Program

This section explains the sequence program example for conducting E71 initial processing and end processing.

(Example) The following is an example program.

- (1) The E71 is installed in the basic base's "0" slot.
- (2) The initial processing parameters are shown below.
 - (a) The E71's IP address is "A20009C0H(162.0.9.192)."
 - (b) Values other than the IP address are used as default values.



5.3.4 Confirming the Completion of Initial Processing

When the initial processing of the E71 is completed normally, the [RDY] LED indicator on the E71 flashes.

Using any of the methods described below, it is possible to check the line connection status between remote nodes and the E71 as well as the completion status of the E71's initial processing.

Point

The status of the E71 becomes data exchange enabled when the initial processing completes normally. Perform data exchange by referring to the reference sections of various data exchange functions.

If the initial does not complete normally, check the contents of the error and take corrective actions as described below, then execute the initial processing again.

- Check the error code using the "Parameter status" of the Ethernet diagnostics with GX Developer. (See Item 13.4.1.)
- Check the contents of the error indicated by the error code, then take corrective actions. (See Item 13.1.)

(Checking with the PING command (from IBM/AT compatible PC to E71) ... See this section

Send a PING command from the PC, and check whether the initial processing of the E71 completes.

(Checking with the loop back test via read/write exchange of data in the PLC CPU)

... See Items 10.7

Send a E71 command for loop back test from the PC, and check whether the initial processing of the E71 completes.

(Checking with GX Developer) ... See Items 13.4.2

Perform a PING test of the Ethernet diagnostic function, and check with GX Developer whether the initial processing of the E71 on the same Ethernet line completes (test via the Ethernet board).

1**Sending the PING command from the PC**

The following example shows how to send a PING command from a remote node (IBM/AT compatible PC) connected on the same Ethernet to the local station's E71 and check the normal completion of the E71's initial processing. (An example of check between devices having the same class and subnet address in the IP addresses)

[Specification method]

ping IP address

[Program example]

IP address for the E71: 192.0.1.254

Example of screen at normal completion

```
C:\>ping 192.0.1.254 ... Executes the PING command

Pinging 192.0.1.254 with 32 bytes of data:

Reply from 192.0.1.254: bytes=32 time=1ms TTL=128
Reply from 192.0.1.254: bytes=32 time<10ms TTL=128
Reply from 192.0.1.254: bytes=32 time<10ms TTL=128
Reply from 192.0.1.254: bytes=32 time<10ms TTL=128

Ping statistics for 192.0.1.254:
    Packets: Sent = 4, Received = 4, Lost = 0 (0% loss)
    Approximate round trip times in milli-seconds:
        Minimum = 0ms, Maximum = 1ms, Average = 0ms

C:\>_
```

Example of screen at abnormal completion

```
C:\>ping 192.0.1.254 ... Executes the PING command

Pinging 192.0.1.254 with 32 bytes of data:

Request timed out:
Request timed out:
Request timed out:
Request timed out:

Ping statistics for 192.0.1.254:
    Packets: Sent = 4, Received = 0, Lost = 4(100% loss)
    Approximate round trip times in milli-seconds:
        Minimum = 0ms, Maximum = 0ms, Average = 0ms

C:\>_
```

<When the test is completed abnormally>

Upon checking the following, send the PING command again.

- Whether the E71 is mounted on the base unit properly
- The connection status to the Ethernet.
- The contents of the parameters loaded to the PLC CPU.
- The operating status of the PLC CPU (whether any errors have occurred).
- The IP addresses of the transmission destination E71 specified by the PING command.

5.4 Communication Line Open and Close

It is possible to exchange data at the same time with a maximum of 8 stations' remote nodes for a communication line connected by the sequence program.

The communication line can conduct fixed buffer exchange, random access buffer exchange, and reading and writing data to the PLC CPU exchange for open remote nodes. However, open processing is required when only random access buffer exchange and reading and writing data to the PLC CPU is performed.

Following is the E71's communication line open processing and close processing performed by the PLC CPU to exchange data between the E71 and remote nodes.

Remarks

- (1) When the PLC program connects a communication line and uses a port to exchange data, the communication format for exchanging with the remote node can be selected during open processing using the following functions.

For each port, please specify whether TCP/IP or UDP/IP will be used for exchange.

The relationship between the E71 data exchange functions and the selectable communication formats are shown below.

Exchange Functions		TCP/IP	UDP/IP
Fixed buffer exchange	With procedure	○	○
	Without procedure	○	○
Random access buffer exchange		○	○
Reading and writing data to the PLC CPU (General data exchange)		○	○
Exchange via a router (Router relay function)		○	○

- (2) Normally, in networks that use IP address, data is exchanged with remote nodes that are part of the same network (subnet address is the same) as that of the local station. When data is exchanged with a remote node from a different network (different subnet address) via a router, it becomes possible to exchange via a router or gateway using the PLC CPU's TCP/IP active open or UDP/IP transmission. (When exchange with a partner remote node via a router is done with the E71 in passive open, exchange can be done without using router relay functions.)

5.4.1 Data for Opening

This section explains about the exchange parameter settings area used to conduct communication line open processing. (Refer to Point at the end of Item 3.7.2 about whether the parameter should be set.)

● Exchange Parameter Settings Area

		Buffer Memory	
(Address)		Exchange Parameter Settings Area (64 Words)	Default Value
10H (16)		Connection No.1	0H (0)
11H (17)		Connection No.2	0H (0)
12H (18)		Connection No.3	0H (0)
13H (19)		Connection No.4	0H (0)
14H (20)		Connection No.5	0H (0)
15H (21)		Connection No.6	0H (0)
16H (22)		Connection No.7	0H (0)
17H (23)		Connection No.8	0H (0)
18H (24)		E71's Port No.	0H (0)
19H (25)		Remote node IP address	0H (0)
1AH (26)		Remote node port No.	0H (0)
1BH (27)		Remote node (L)	FFFFFFFFFFFFH
1CH (28)		Ethernet to	
1DH (29)		Address (*1) (H)	
1EH (30)			
1F to 25H (31 to 37)		E71's port No.	Exchange address settings area (For connection No.2 7 words) (Same as connection No.1)
		to	
26 to 2CH (38 to 44)		E71's port No.	Exchange address settings area (For connection No.3 7 words) (Same as connection No.1)
		to	
2D to 33H (45 to 51)		E71's port No.	Exchange address settings area (For connections No.4 7 words) (Same as connection No.1)
		to	
34 to 3AH (52 to 58)		E71's port No.	Exchange address settings area (For connections No.5 7 words) (Same as connection No.1)
		to	
3B to 41H (59 to 65)		E71's port No.	Exchange address settings area (For connections No.6 7 words) (Same as connection No.1)
		to	
42 to 48H (66 to 72)		E71's port No.	Exchange address settings area (For connections No.7 7 words) (Same as connection No.1)
		to	
49H (73)		E71's port No.	0H (0)
4AH (74)		Remote node IP address	0H (0)
4BH (75)		Remote node port No.	0H (0)
4CH (76)		Remote node (L)	FFFFFFFFFFFFH
4DH (77)		Ethernet to	
4EH (78)		Address (*1) (H)	
4FH (79)			

*1 If the partner remote node connected by the communication line has an ARP function (broadcast), please make the default value (FFFFFFFFFFFFH).

● Instruction Area

		Buffer Memory	
(Address)		Instruction Area (1 Word)	Default Value
1F0H (496)		Communication specification during STOP area (1 word)	0H (0)

1 Usage available settings area (Default value = 0H) address 10H to 17H (16 to 23)

(a) Set the fixed buffer usage availability for open processing to whether or not to perform destination existence check.

(b) Conduct this setting before open processing for each connection.

(Bit position)	b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
	⑥	0					⑤	④	③	0					②	①
(For memo)			0	0	0	0				0	0	0	0	0		

⑥ Open method	⑤ Fixed buffer exchange	④ Communication format	③ Pairing open	② Existence check	① Fixed buffer usage
00: Active, UDP/IP	0: With procedure	0: TCP/IP	0: No pairs	0: No check	0: For transmission/does not communicate
10: Unpassive	1: Without procedure	1: UDP/IP	1: Pairs	1: Check	1: For reception
11: Fullpassive					

① Fixed buffer usage availability setting (b0)

- When conducting exchange using a fixed buffer, set whether the fixed buffer will be used for transmission or reception for the particular connection.
- When conducting transmission and reception using one specific node and fixed buffer, two fixed buffers are required for transmission and reception, so please set two connections.
- Specify one of the following setting values.
 - 0 : For transmission or not to perform fixed buffer exchange (default value)
 - 1 : For reception
- From remote node, random access buffer exchange and reading and writing data to the PLC CPU exchange can be conducted by either reception setting or transmission setting for usage of fixed buffer.

② Destination existence check setting (b1)

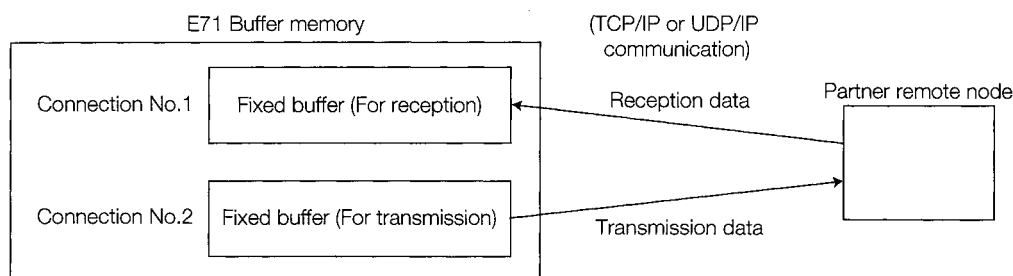
- Set the E71 to check whether the partner remote node is operating normally when exchange with the partner remote node for its connection open processing as ended has not been conducted for a specific period of time. (*2 Refer to Item 5.3.1)
- Specify one of the following specification values.
 - 0 : Does not check existence (no check) (default value)
 - 1 : Checks existence (has check)
- * When existence check is selected, the E71 conducts an existence check for the destination at each specified time interval to check whether the connection destination (partner destination) is operating correctly. The E71 will conduct the following process if an error occurs during the existence check.
 - Force closes the line and stores the error information in the buffer memory error log area (address 169 to 179).
 - The open error detection signal (X18) is turned on when the open end signal (X10 to X17) is turned off.
- When changing the exchange partner remote node during the middle of an operation at the UDP/IP connection, make the setting 0 (no existence check).

When 1 (check existence) is set, the E71 conducts an existence check on the first exchange partner after UDP/IP open. An existence check is not performed for exchange partners after the change.

③ Pairing open setting (b7)

- Sets whether one of the partner remote nodes' ports is connected when the E71 resumption connection and transmission connection are made into one pair when fixed buffer exchange (either with procedure or without procedure can be selected) is conducted.

(Example)



- When pairing opening is set, the corresponding connection No.'s fixed buffer (for reception) and the next connection No.'s fixed buffer (for transmission) are paired. (When the connection No. to be opened is "8," the connection No.8's fixed buffer is used for reception, and the connection No.1's fixed buffer is used for transmission.)

The usage availability settings are conducted as follows.

Connection No. side to be used for reception: b0 = 1 (for reception), b7 = 1 (opens pairing)

Connection No. side to be used for transmission: (The usage availability settings are not required)

- The user opened processing, done during pairing setting, is performed for the reception connection No.

The open processing for the pairing transmission connection No. (open request signals on) is not required. (E71 conducts automatically.)

The open process for the reception connection No., turns on the transmission connection No.'s open end signal.

- An image of open processing when pairing open is set up is shown in Remark in Item 5.4.4.
- Specify one of the following setting values for pairing open setting (b7).
0 : Does not open pairing (default value)
1 : Opens pairing

④ Communication format (Protocol) settings (b8)

- Sets whether TCP/IP or UDP/IP is used as the communication protocol for each connection.
- Select one of the following setting values.
 - 0 : TCP/IP (default value)
 - 1 : UDP/IP

⑤ Fixed buffer exchange procedure existence setting (b9)

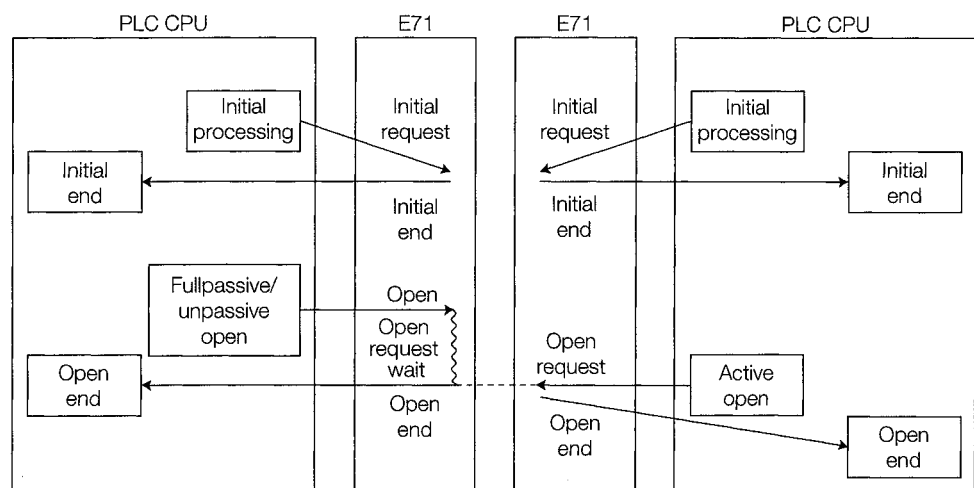
- Sets the exchange method used to conduct fixed buffer exchange.
- Specify one of the following setting values.
 - 0 : With procedure (default value)
 - 1 : Without procedure

* When with procedure is selected, in the subject connection, fixed buffer exchange with procedure, random access buffer exchange, and reading and writing data to the PLC CPU exchange can be conducted.

When without procedure is selected the subject connection becomes a without procedure fixed buffer exchange special use, so fixed buffer exchange with procedure, random access buffer exchange, and reading and writing data to the PLC CPU exchange cannot be conducted at the same time as exchange without procedure.

⑥ Open method setting (b14, b15)

- This setting is valid only when the communication format (protocol) is TCP/IP.
- Setting is not required when UDP/IP is the communication method, so make the setting "00."
- When opening using TCP/IP, open the active open node after the Full passive/Unpassive open node open processing end.



- Specify one of the following settings.
 - 00 : Active open or UDP/IP (default value)
 - 10 : Unpassive open
 - 11 : Full passive open

Remarks

Shows the differences between each open format.

① Active open method

Conducts active open processing for the remote nodes that are in the TCP connection open passive state (Full passive/Unpassive open state).

② Full passive open format

Conducts TCP connection passive open processing for only the specified nodes that are set in the exchange address setting area. Changes to the active open request wait state from the remote node that is set in exchange address setting area.

③ Unpassive open format

Conducts TCP connection passive open processing for all the remote nodes connected to the network. All the remote nodes in the network change to the active open request wait state.

(c) Following is an example of the usage available setting area data setting.

(Bit position)	b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
	⑥			0			⑤	④	③			0			②	①

① Fixed buffer usage availability

② Destination existence check

③ Pairing open setting

④ Communication format

⑤ Fixed buffer communication procedure existence

⑥ Open method setting

(Example 1) When set to ③ is "0" (Pairing open not done), ⑤ is "0" (With procedure).

		④ : 0(TCP)			④ : 1 (UDP)
		⑥ : 00 (Active)	⑥ : 10 (Unpassive)	⑥ : 11 (Fullpassive)	
① : 0 (For transmission)	② : 0 (Does not check)	0000H	8000H	C000H	0100H
	② : 1 (Checks)	0002H	8002H	C002H	0102H
① : 1 (For reception)	② : 0 (Does not check)	0001H	8001H	C001H	0101H
	② : 1 (Checks)	0003H	8003H	C003H	0103H

(Example 2) When ③ is "0" (Pairing open not done), ⑤ is "1" (Without procedure).

		④ : 0(TCP)			④ : 1 (UDP)
		⑥ : 00 (Active)	⑥ : 10 (Unpassive)	⑥ : 11 (Fullpassive)	
① : 0 (For transmission)	② : 0 (Does not check)	0200H	8200H	C200H	0300H
	② : 1 (Checks)	0202H	8202H	C202H	0302H
① : 1 (For reception)	② : 0 (Does not check)	0201H	8201H	C201H	0301H
	② : 1 (Checks)	0203H	8203H	C203H	0303H

(Example 3) ③ is "1" (Pairing open is done), ⑤ is "0" (With procedure) is set.

		④ : 0(TCP)			④ : 1 (UDP)
		⑥ : 00 (Active)	⑥ : 10 (Unpassive)	⑥ : 11 (Fullpassive)	
① : 1 (For reception)	② : 0 (Does not check)	0081H	8081H	C081H	0181H
	② : 1 (Checks)	0083H	8083H	C083H	0183H

2

Exchange address setting area

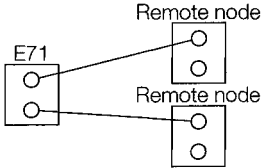
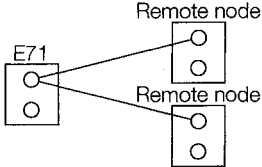
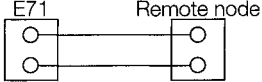
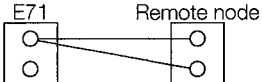
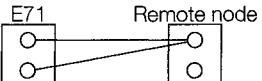
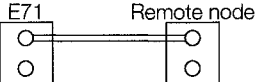
- (a) Sets the local station E71's port No. partner remote node IP address, port No., etc., when communication line is connected using open processing.

When setting the pairing open using the usage application setting bit7 (b7), the next connection No. side exchange parameter setting is not required. (This is automatically conducted by the E71.) The next connection No. open end signal is turned on by the open processing for the connection No. that sets the pairing open.

- (b) For the setting values, please specify the values set by the network manager.
- (c) Sets the data in accordance with the contents of the usage availability setting area's open procedure settings (b14, b15) shown in 1 when settings are conducted for each connection. (refer to key points in Item 3.7.2) Sets these settings before open processing is conducted during TCP open, open processing is conducted before UDP open processing, and before data transmission and reception.

① E71's port No. setting (Default value = 0H): Address 18H (24...)

- Sets the local station E71's port No.
- The setting values are specified to between 100H and FFEH. As far as possible, it is recommended that a port No. be set at 401H or later. Set to No. that is not being used elsewhere.
- Following are the precaution items for port Nos. when multiple connections are made between remote nodes and the local station using open processing. (In the diagram, the nodes are denoted by a square, and the port Nos. are denoted by the circles.)

Connection State (Shows O: Port (Port No.))	Connection Description	Communication Protocol	
		TCP	UDP
	Also sets multiple local station port Nos. even though connections are made with multiple nodes.	○	○
	Sets a single local station port No. when connections are made with multiple nodes. (However, several connections must be opened.) Do not perform this when the local station is unpassive.	○	×
	Also sets multiple E71 port Nos. even though connections are made with multiple remote node ports.	○	○
	Sets a single E71 port No. even though connections are made with multiple remote nodes. (However, several connections must be open.) Do not perform this when the local station is unpassive.	○	×
	Sets multiple E71 port Nos. even though connection is made to the same remote node port. (However, several connections must be open.)	○	○
	Multiple settings when the remote node same port and the E71's same port is only possible for pairing open settings.	○	○

② Remote node IP address setting (Default value = 0H)
Address 19H to 1AH....(25 to 26...)

- Sets the IP address for the partner remote node to which exchange will be conducted.
- Specify the settings value as other than 0H and FFFFFFFFH except for when simultaneous broadcast communication is performed exchanged without procedure (UDP/IP) by fixed buffer. (FFFFFFFH is the setting value for the simultaneous broadcast communication mentioned above.) Conduct setting after checking the mutual remote node's IP address.

③ Remote node port No. setting (Default value = 0H) Address 1BH....(27...)

- Sets the port No. for the primary remote node for which exchange will be done.
- Specify the settings value as between 100H and FFEH except when simultaneous broadcast communication is performed with exchange without procedure (UDP/IP) by the fixed buffer. As far as possible, it is recommended that a port No. be set at 401H or later. (FFFFH is the setting value for the simultaneous broadcast communication described above.) Before setting, check the partner remote node's port No.

④ Remote node ethernet address setting (Default value = FFFFFFFFH)
 Address 1CH to 1EH....(28 to 30...)

- When the partner remote node to which exchange is being conducted does not have ARP functions, set the partner remote node's Ethernet address.
- Set the settings values to those shown below.

When the partner remote node has ARP functions 0H or FFFFFFFFH
 When the partner remote node does not have ARP functions

..... Partner remote node's
 Ethernet address (except
 0H and FFFFFFFFH)

When specifying other than 0H and FFFFFFFFH, check the partner remote node's Ethernet address before making the settings.

* When this setting value is 0H or FFFFFFFFH, the E71 conducts processing as if the partner remote node has ARP functions.

(Example) The settings data when the Ethernet address is 080070220004H is shown below. (For connection No.1)

Address	Buffer Memory							
2CH(44)	0004H	← Write <input type="checkbox"/> <table><tr><td>0800H</td><td>7022H</td><td>0004H</td></tr><tr><td>D117</td><td>D116</td><td>D115</td></tr></table>	0800H	7022H	0004H	D117	D116	D115
0800H	7022H		0004H					
D117	D116		D115					
2DH(45)	7022H							
2EH(46)	0800H							

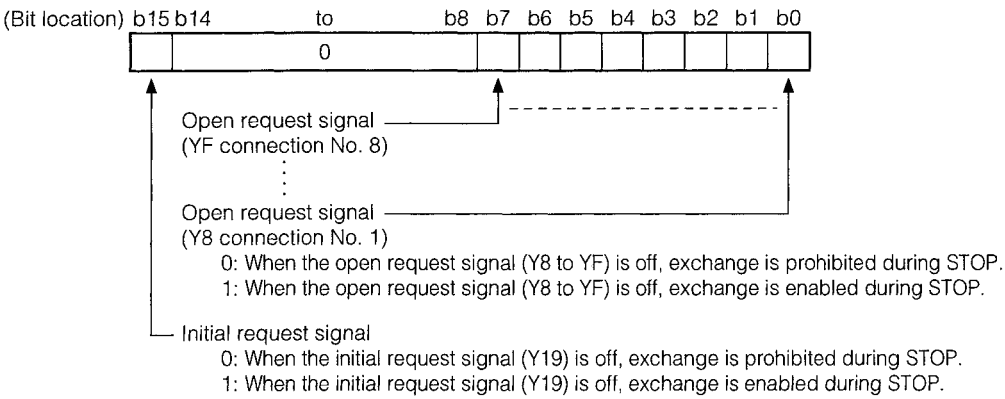
Point

- (1) Determine the setting values by consulting with the partner equipment and the system's managers.
- (2) For information regarding the parameters required for setting when E71 open processing is conducted when E71 functions are used, refer to Point in Item 3.7.2.

3 Exchange instruction area during STOP (default value = 0H)

Address 1F0H(496)

- (a) This is the setting that is used to continue the next data exchange for the E71 from the next remote nodes even when the PLC CPU of the station installed in E71 is in the STOP status and the E71 open request signals (Y8 to YF) and initial request signal (Y19) are off.
 - Random access buffer exchange
 - Exchange of read/write data in the PLC CPU
- (b) This specification is conducted using the communication line unit, and the setting value can be changed even after communication line open processing. Use the system specifications to set the exchange enable/prohibit during STOP.
- (c) Conduct the setting to this specification area, continuing data exchange, and reopen processing in accordance with Item 5.6.
- (d) The setting value is specified as follows.



(Example setting)

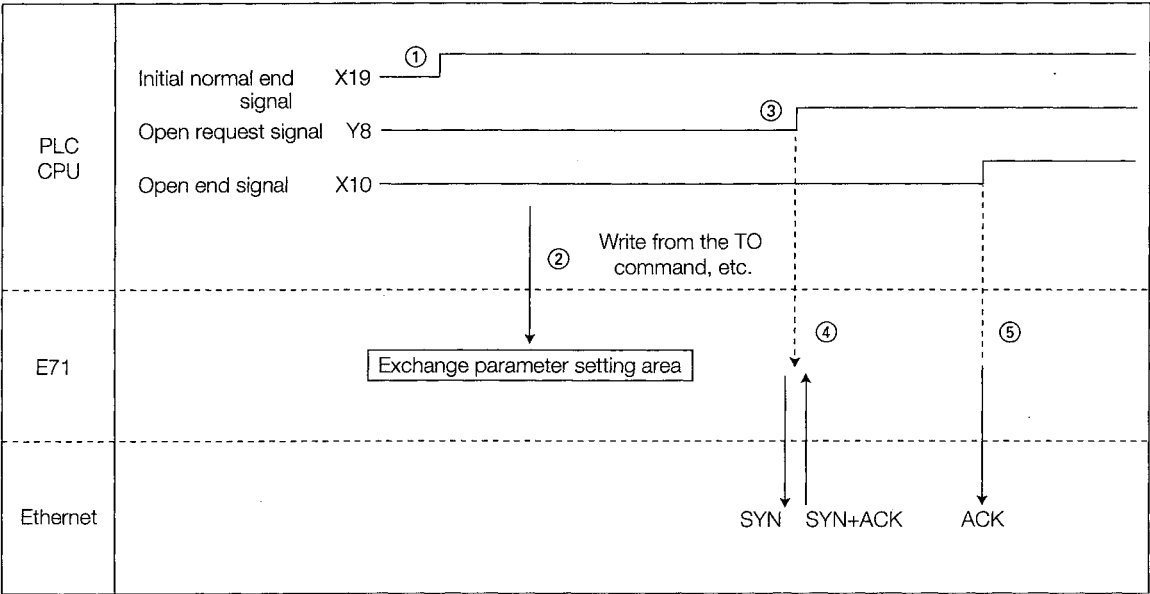
- Set to 0003H to continue exchange between connection No. 1 and connection No. 2 when the initial request signal (Y19) is left on and after the open request signals (Y8, Y9) are turned from on to off.
- Set to 8003H to continue exchange between connection No. 1 and connection No. 2 after the initial request signal (Y19) and the open request signals (Y8, Y9) are turned from on to off.

5.4.2 Communication Line Open Processing Procedure

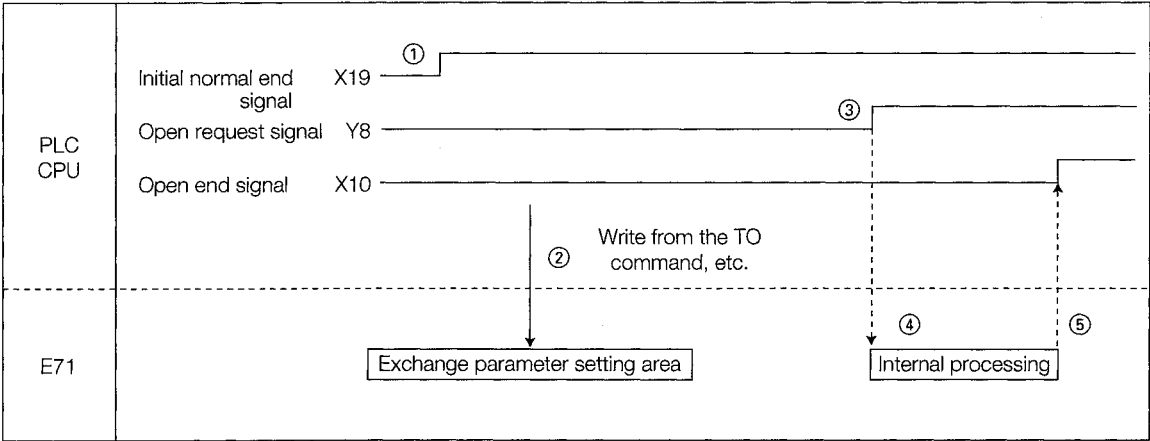
This section explains the open processing procedure for connecting a communication line from the E71 to a remote node using an example for connection No.1.

To conduct open processing, initial processing must be completed.

1 Open processing procedure using TCP



2 Open processing procedure using UDP



- ① Initial processing is conducted by the initial request signal (Y19). Initial normal end signal (X19) turns on. (Refer to Item 5.3)
 - ② The TO command, etc., causes the sequence program to write the setting values (parameters) in the buffer memory exchange parameter settings area.
 - ③ The open request signal (Y8) is turned on by the sequence program.
 - ④ The E71 executes the open processing. (*1)
 - (For TCP)
 - For active open Open request (SYN) is transmitted. (*2)
 - For passive open An open request from the partner remote node is waited. (*3)
 - (For UDP)
 - Executes internal processing.
 - ⑤ The open end signal (X10) is turned on when the open processing/internal processing is normal end.

The open error code is stored in the buffer memory, and the open error detection signal (X18) is turned on when the open processing/internal processing is error end. (*4) (The open end signal (X10) does not turn on.)

When the open request signal is turned OFF while an open error is being generated, the open error detection signal will be turned OFF if no open errors are generated in other line connections.

(If the open request signal (Y8 to YF) is off for all connections in which an open error is currently occurring, the open error detection signal (X18) is turned off.)
- *1 When the initial request signal (Y19) is off or the open request signal (Y8) is turned off during open processing, closed processing and end processing are performed after the open processing end.
- *2 When an active open is performed and an ABORT (RST) instruction is received from the partner remote node, the open processing will complete abnormally and the open error detection signal (X18) will turn on.
- *3 When a passive open is performed and the open request signal is turned off prior to completing the open processing, the open request will be invalid.
- *4 The open state and error codes during error end are checked by the next buffer memory.
- The exchange state storage areas (information storage area by connection: address 89 to 168) open error code area.
 - Error log area (address 169 to 179).
- The error code stored in the open error code area will be cleared ($n \rightarrow 0$) when the open request signal is turned ON again.

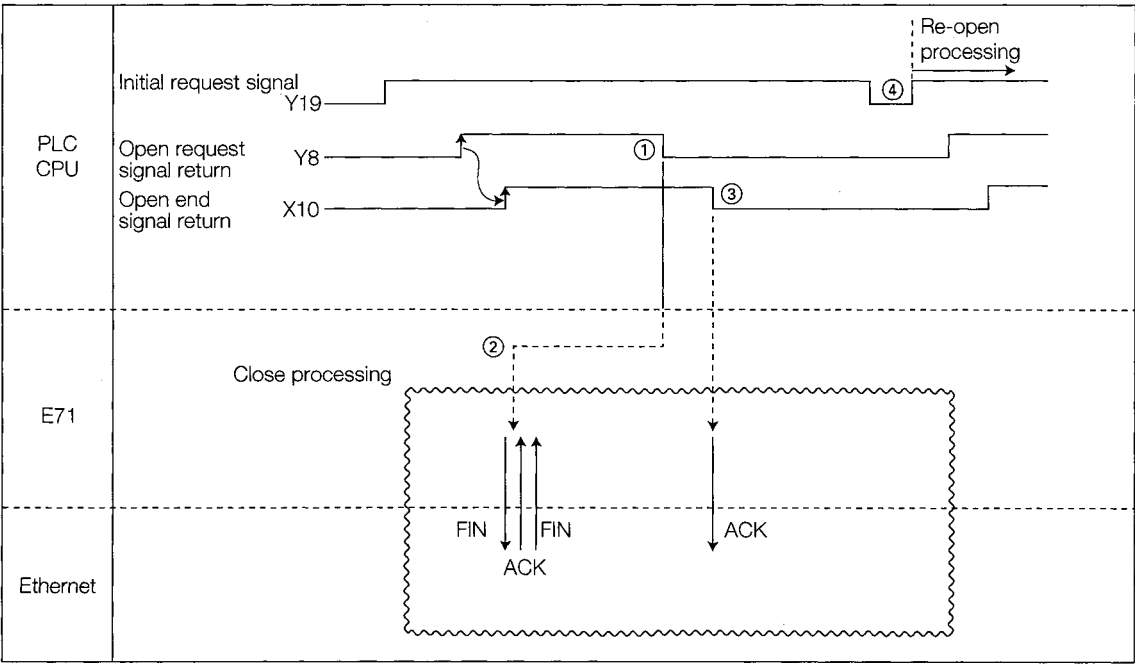
5.4.3 Communication Line Close Processing Procedure

This section explains the close processing for closing (disconnecting) the communication line that was connected between the E71 and the remote node by open processing using an example for connection No.1.

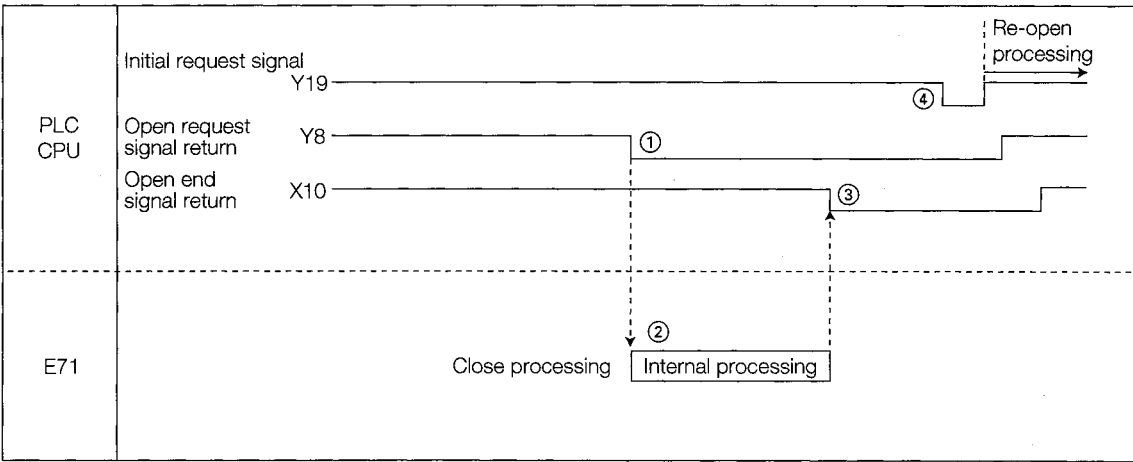
The close processing timing must be conducted by making arrangements with the partner remote node.

1 For closing from the E71 end

(a) Close processing procedures for TCP



(b) Close processing procedures for UDP



- ① The open request signal (Y8) is turned off by the sequence program.
- ② The E71 executes closed processing.
 - * If the transmission request signal/reception completion confirmation signal (Y0, Y1...) for the fixed buffer communication of the corresponding connection is off when the open request signal (Y8, Y9...) is turned off, the E71 turns off the corresponding input signal (X) listed below:
 - Transmission normal completion signal/reception completion signal (X0, X2...)
 - Transmission error detection signal/reception error detection signal (X1, X3...)
 - (Example: For connection 1)
If the transmission request signal/reception completion confirmation signal (Y0) for connection 1 is off when the open request signal (Y8) is turned off, the E71 turns off the following input signals (X).
 - Transmission normal completion signal/reception completion signal (X0)
 - Transmission error detection signal/reception error detection signal (X1)
- ③ When close processing ends the open end signal (X10) turns off for either normal close or error close. (The open request signal (Y8) can be turned on immediately after the open end signal (X10) turns off.)
- ④ Initial request signal (Y19) is turned off by the sequence program.

Point

The open end signal (X10 to X17) is automatically turned off and the communication line will be closed in the following cases in addition to a close request. To reopen, first turn off the open request signal (Y8 to YF) once, and then conduct open processing.

- (1) When the exchange condition setting switch (SW1: line processing selection when TCP ULP timeout error) is set to off (close), the open end signal will turn off when the following timeout occurs.

- ① Timeout during TCP transmission
- ② Partner remote node existence check function timeout

At this time the open error detection signal (X18) turns on. (*1)

* When the exchange condition setting switch SW1 is set to on (does not close), the line will not close even when the above timeout occurs. (The open error detection signal (X18) will not turn on.) However, the error code will be stored in the buffer memory described in *1 below.

- (2) The open end signal will turn off when a close or ABORT (RST) command is received from the partner remote node.

- (3) The open end signal will turn off when transmission of the ABORT (RST) command is conducted. At this time, the open error detection signal (X18) will turn on. (*1)

- (4) When the active open request is received again from the partner remote node with the TCP connection completely open

The operation varies depending on the E71 version.

- (a) When the software version of the E71 is "Version E" or later

The E71 closes the connection when it receives the RST command from the partner remote node after returning ACK to the partner remote node.

- (b) When the software version of the E71 is "Version D" or earlier

The E71 closes the connection after sending the RST command.

However, the E71 only sends the RST command when it receives the active open request again from the partner remote node with a different IP address or port No. (Does not close the connection.)

- (5) The open end signal will turn off when the E71's abort command is transmitted including that described above (Refer to Item 3.5.3).

- *1 There error code during the open status for the error end can be checked using the buffer memory exchange status storage (addresses 89 to 168) and error log area (addresses 169 to 179).

- Error during open processing : Open error code area
- Error during data exchange

When conducting transmission using the fixed buffer:

Set buffer transmission error code area

When conducting transmission using any other than the fixed buffer :

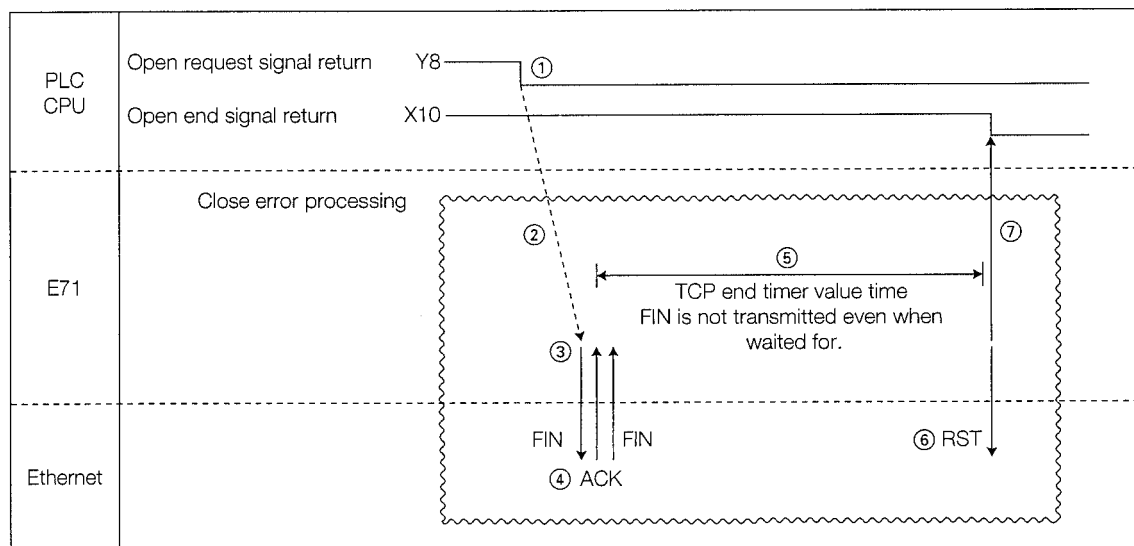
Error log area

The error code stored in the open error code area will be cleared (n → 0) when the open request signal is turned ON again.

(c) Processing (TCP) when error end is done by close

Normally, when closed by the E71, the E71 transmits an FIN and then ACK, FIN are returned from the partner remote node. However, when ACK, FIN are not returned because of a partner remote node error, the E71 forcefully disconnects the connection (transmission of ABORT (RST) command).

Following is an explanation that uses an example of processing for connection No. 1.

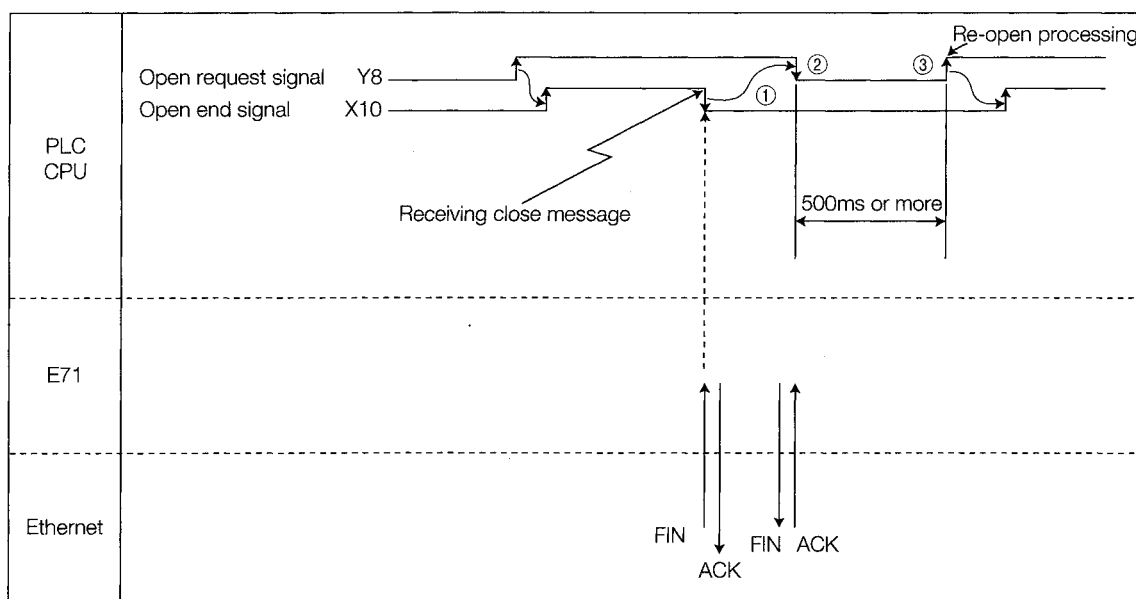


- ① The open request signal (Y8) is turned off by the sequence program.
- ② The E71 begins close processing.
- ③ The E71 transmits FIN to the partner remote node.
- ④ The partner remote node returns ACK, FIN in response to the FIN sent by the QE71.
(If it is not returned, the E71 retransmits the FIN.)
In response to the FIN sent by the E71, if an ACK RST is sent back from the other node, the E71 determines that the close processing is completed and turns OFF the open completion signal.
- ⑤ The E71 waits for ACK, FIN to be transmitted by the partner remote node.
(The wait time is the TCP end value timer time.)
At this time, if ACK, FIN is transmitted, an ACK will be returned as normal processing.
- ⑥ If the ACK, FIN is not transmitted within the TCP end timer value time, ABORT (RST) command is transmitted to the partner remote node.
- ⑦ The E71 determines that close processing has ended regardless of the state of the partner remote node, and turns the open end signal (X10) to off.

Remarks

- (1) When the above processing is conducted, the E71 determines that the partner remote node processing was conducted normally, so the close results are not stored in the error log area.
- (2) The above processing is a unique function of the E71, and is not part of the general TCP/IP protocol.

2 When closing from the partner remote node end



- ① The open end signal (X10) is turned off when the close/ABORT (RST) command is received from the partner remote node.
- ② The open request signal (Y8) is turned off by the sequence program when the open end signal is turned off.
- ③ When reopening the open request signal (Y8) is turned on by the sequence program after a minimum of 500ms.

Point

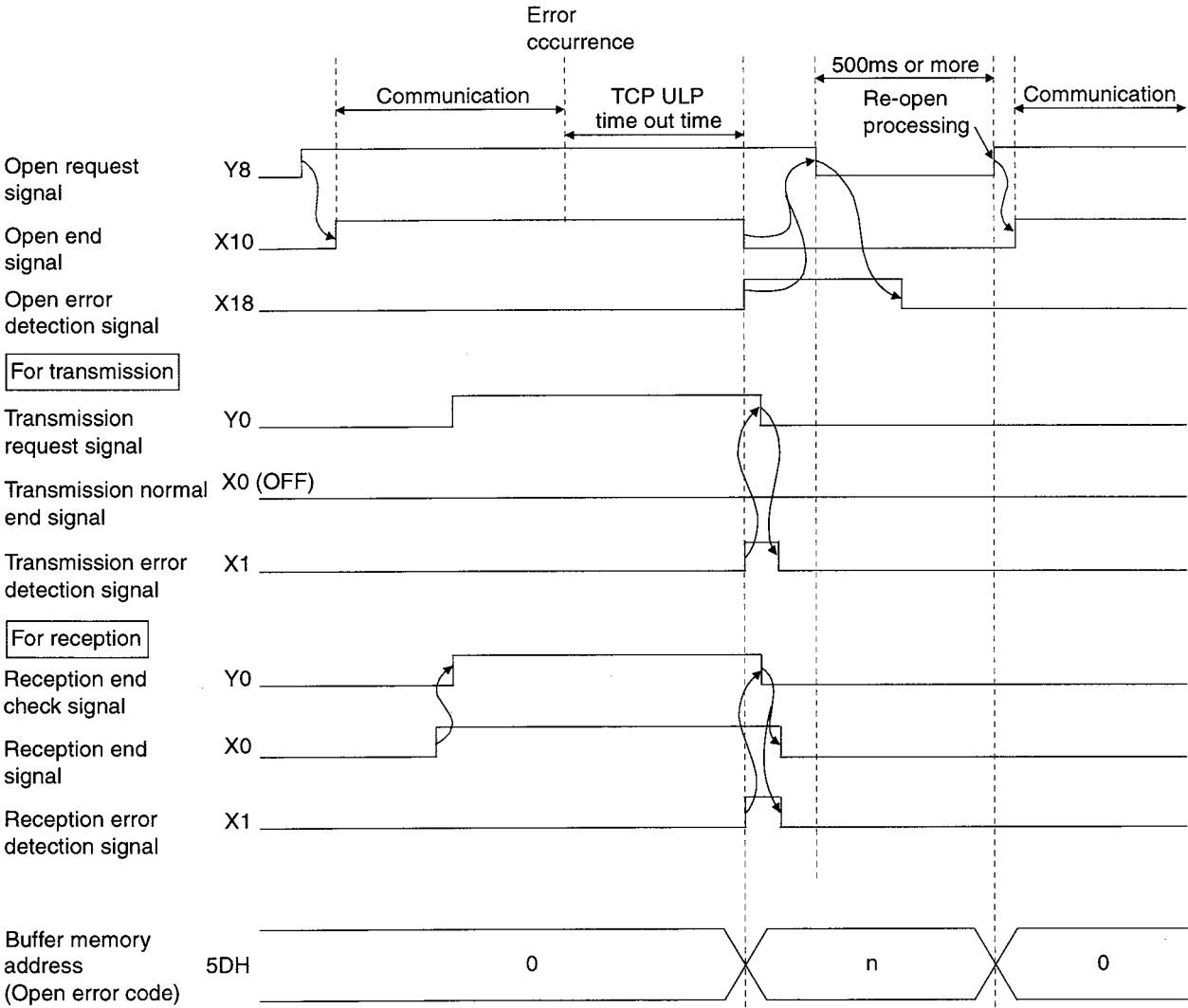
For the sequence program to recognize the open end from the remote node side the open end signal (X10 to X17) on time must be longer than the PLC CPU's scan time. Even if there is an open end, if a close message is received that is shorter than the ACPU scan time, the sequence program may not recognize the open end.

Remarks

With communication in the connection 1 as an example, the following shows the timing chart for performing reopen processing when an error has occurred in fixed buffer data exchange via TCP/IP communication and the open completion signal turns off.

1 When all input signals for data exchange turn off normally

* A program example is shown in Item 5.4.5.



2

When all input signals for data exchange do not turn off

The E71 performs close processing even if the following signals for the fixed buffer data exchange for the corresponding connection are on when the reception of Close/Abort (RST) has occurred from the partner remote node prior to the completion of data communication performed immediately before.

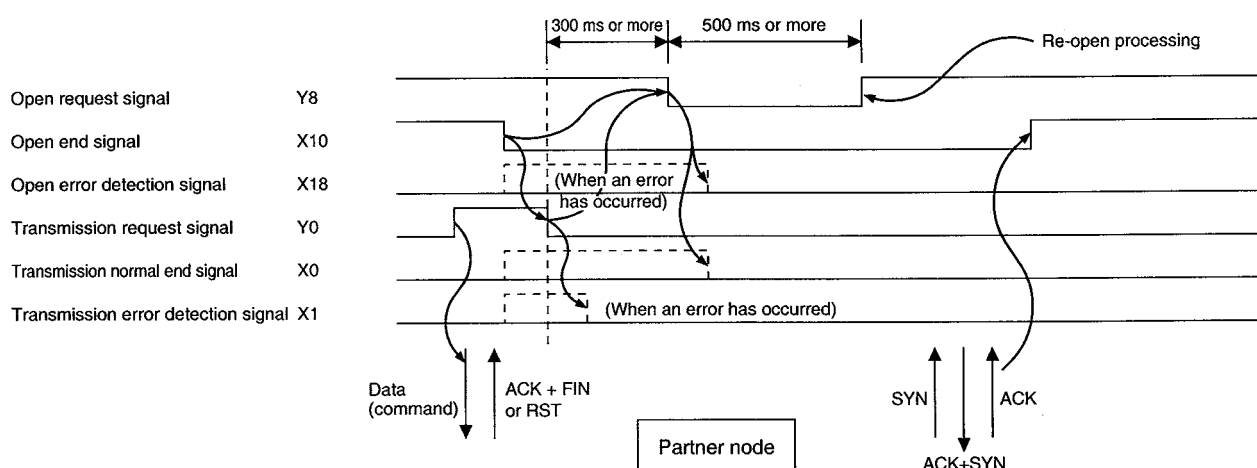
- Transmission request signal/reception completion confirmation signal (Y0 to Y8)
- Transmission normal completion signal/reception completion signal (X0, X2...)
- Transmission error detection signal/reception error detection signal (X1, X3...)

If close processing is performed for the corresponding connection while the above signals are on, turn off the open request signal (Y8 to YF) after turning off the transmission request signal/reception completion confirmation signal at the timing shown below. The E71 turns off the above input signals of the corresponding connection.

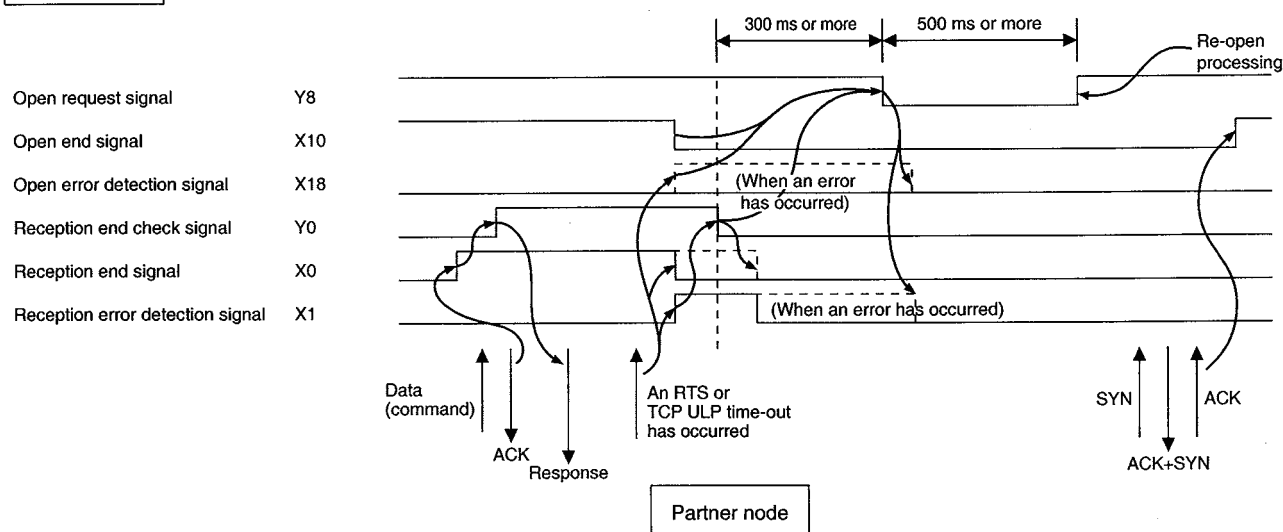
* A program examples are shown in Item 7.2 of Appendix.

* If pairing open has been performed for the corresponding connection, the signal that was specified when opening will be the target open request signal for the input/output signals shown in the figure below.

For transmission (when closed by the partner node)



For reception (when closed by the partner node)

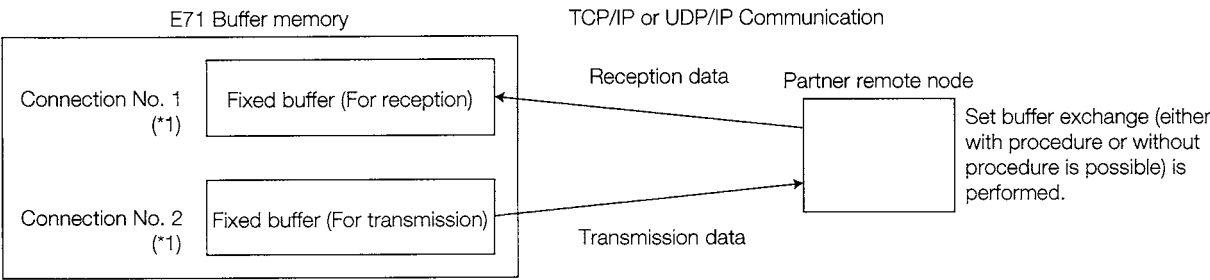


5.4.4 Pairing Open Communication Line Open Processing and Close Processing Procedures

This section explains the open processing and close processing procedures when connecting a communication line to one partner remote node port when the E71's reception connection and transmission connections are made into one pair.

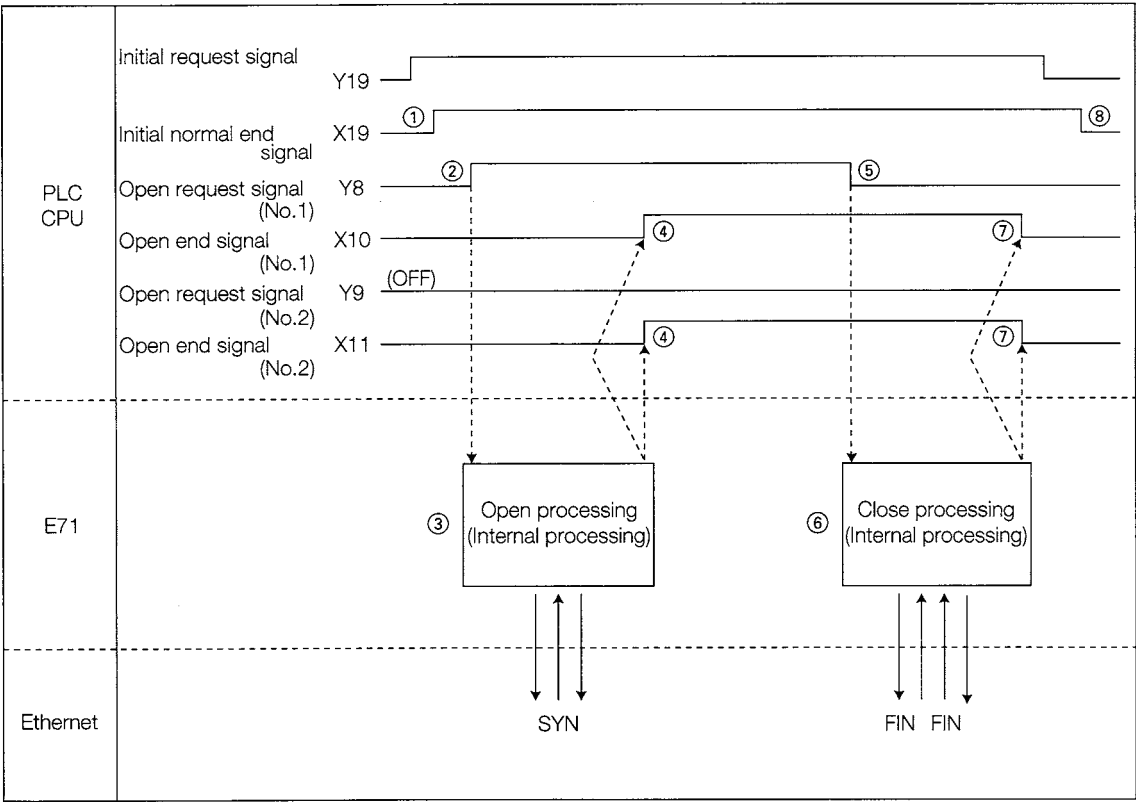
Fixed buffer exchange (either with procedure or without procedure is possible) is performed by the connection which the pairing open was processed.

(Example)



- *1 Connection No.1 and No.2's exchange parameter settings (Address 10H to 11H)
- Connection No.1 exchange parameter setting (Address 10H) : 0081H
 - Connection No.2 exchange parameter setting (Address 11H) : Setting not required

(When the Pairing is Connection No. 1 and Connection No. 2)



Open processing

- ① The initial normal end signal (X19) is turned on by the initial processing normal end which is produced when the initial request signal (Y19) is turned on. (Refer to Item 5.3)
- ② The setting values (parameters) are written to the buffer memory exchange parameter settings area by the sequence program in response to a TO command, etc., which turns on the open request signal (Y8).
- ③ The E71 executes open processing for connection No.1 and connection No.2. (*1)

(For TCP)

For active open: Transmits open request (SYN).

For passive open: Waits for an open request from the partner remote node.

(For UDP)

Internal processing is executed.

- ④ Open end signal (X10, X11) is turned on when the open processing/internal processing is normal end.

The open error code is stored in the buffer memory, and the open error detection signal (X18) is turned on when the open processing/internal processing is error end.

(The open end signal (X10, X11) is not turned on.) Refer to Item 5.4.2 *4

If the open request signal is turned off when an open error occurs, the open error detection signal turns off unless an open error has occurred in other line connection.

(If the open request signal (Y8 to YF) for all connections in which an open error is currently occurring is off, the open error detection signal (X18) is turned off.)

- *1 If the initial request signal (Y19) is turned off or the open request signal (Y8) is turned off when data sending or receiving has not been performed after open processing, closed processing or end processing is performed after open processing ends.

Close processing

- ⑤ The open request signal (Y8) is turned off by the sequence program.
- ⑥ The E71 executes close processing for connection No.1 and connection No.2.
- ⑦ The open end signal (X10, X11) is turned off when the close processing ends.
- ⑧ The initial request signal (Y19) is turned off by the sequence program.

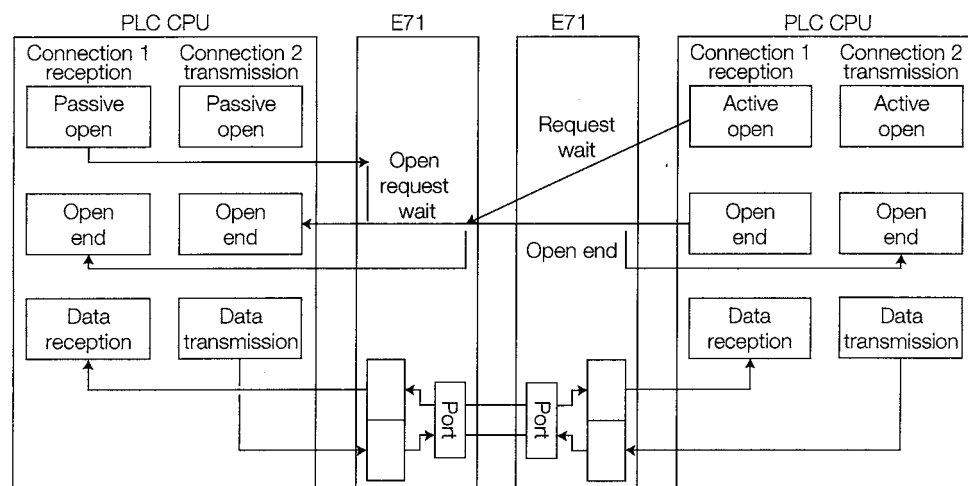
Point

- (1) It is not necessary to set the exchange parameter that corresponds to the connection No. following the connection No. for which pairing open is to be specified. (Ignored.)
For information regarding the exchange parameter setting (usage application setting and exchange address setting) refer to Item 5.4.1 1 (b) ③.
- (2) This is the remote node that is connected by the remote node in the Ethernet that is connected by the E71 or by the router relay function (Refer to Chapter 12) that sets the remote node range with which exchange can be done using pairing open.

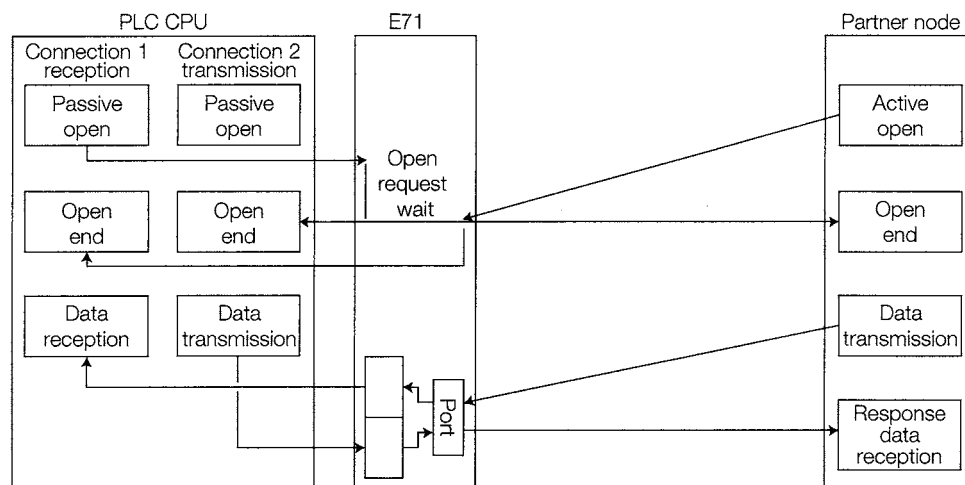
Remarks

(1) Following is shown the open processing image when pairing is set.

<Example 1> Pairing between E71s

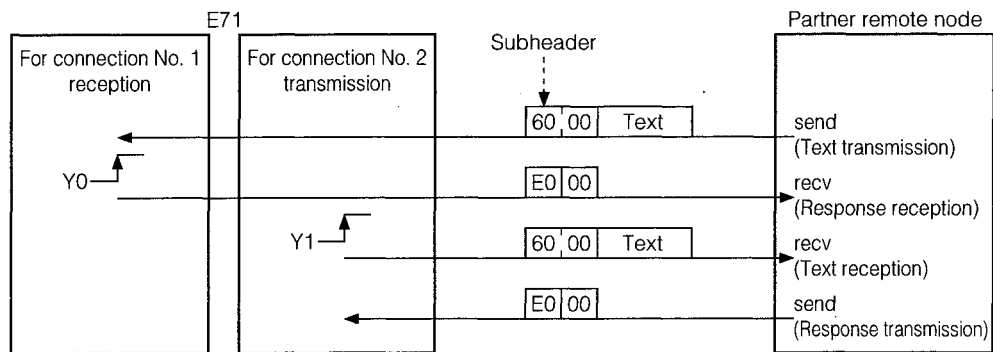


<Example 2> Connection between E71 and partner node



- (2) When conducting fixed buffer exchange using pairing open, use one port each for the E71 and the partner remote node. The E71 uses the receive data subheader to determine whether the reception data from the remote node is text or response. The remote node also uses the subheader to determine whether the received data is text or response.

(Example) For fixed buffer exchange (with procedure)



- (3) For more details on cases when the reception of Close/Abort (RST) has occurred from the partner remote node prior to the completion of the data communication performed immediately before, refer to Remarks in 2 of Item 5.4.3.

5.4.5 Example Program

This section explains an example sequence program used to do the connection open processing for the E71 and a remote node.

(Example) The following is an example program.

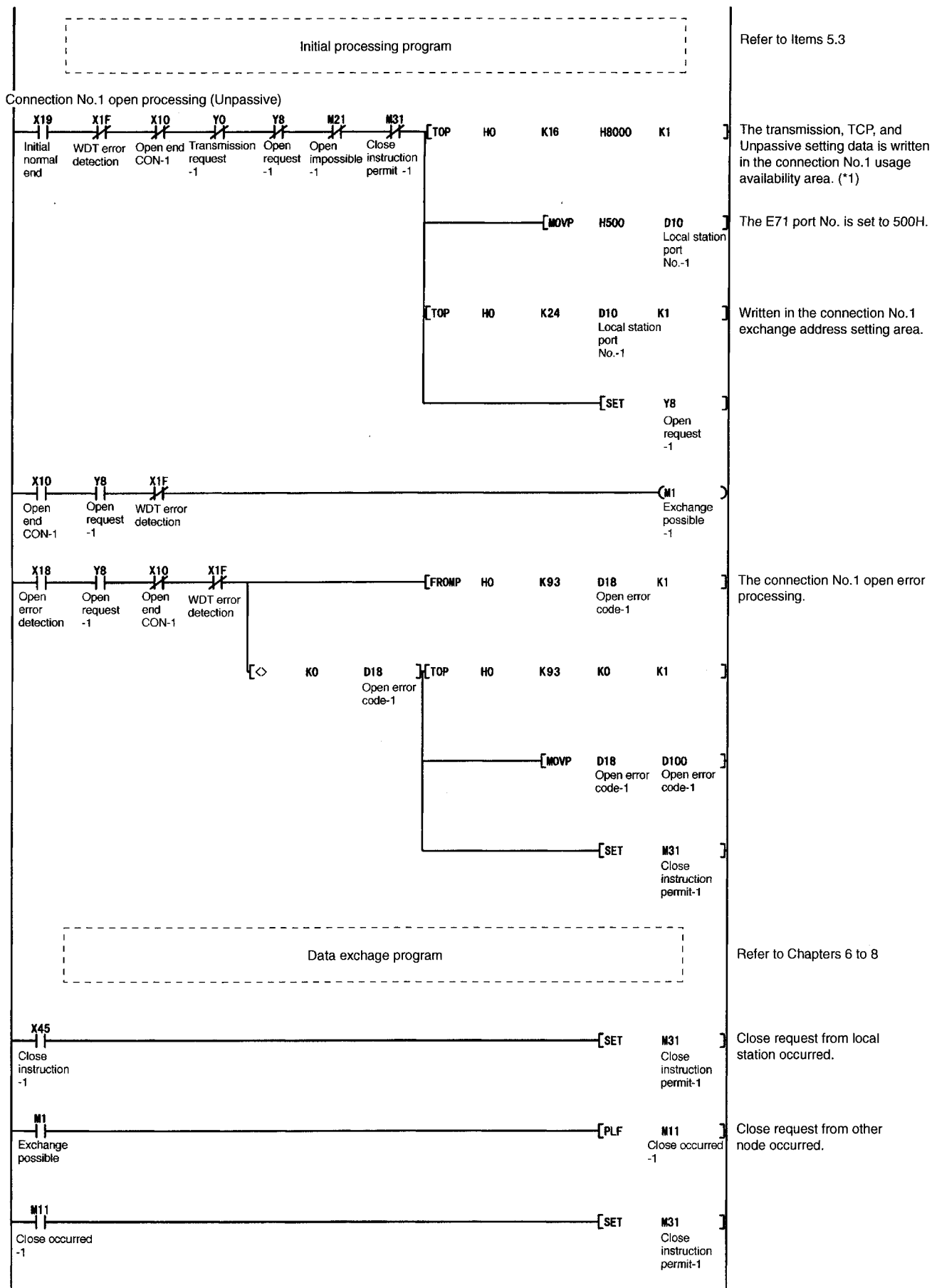
(1) The E71 is installed in the basic bases "0" slot.

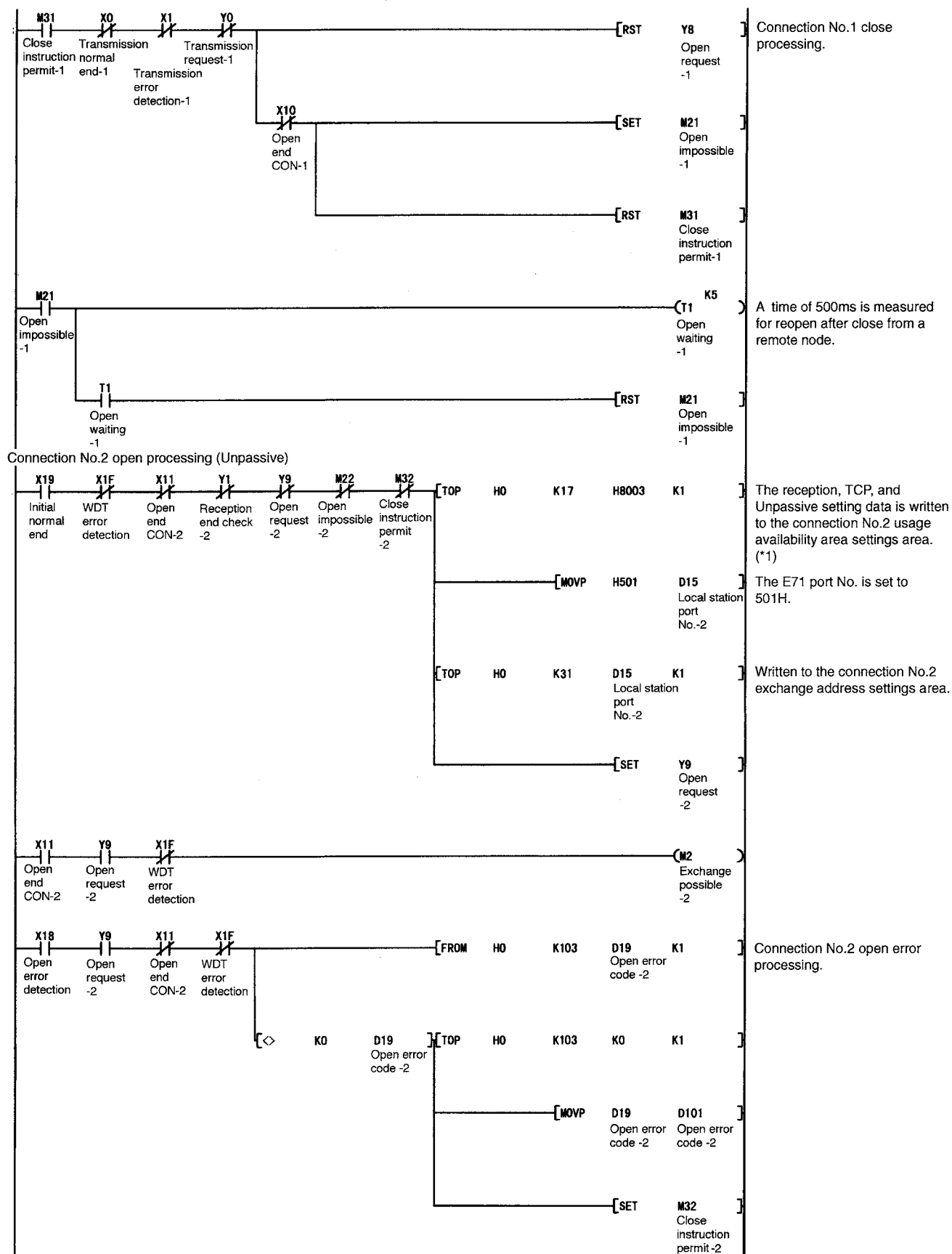
(2) Exchange parameters are shown in the table below.

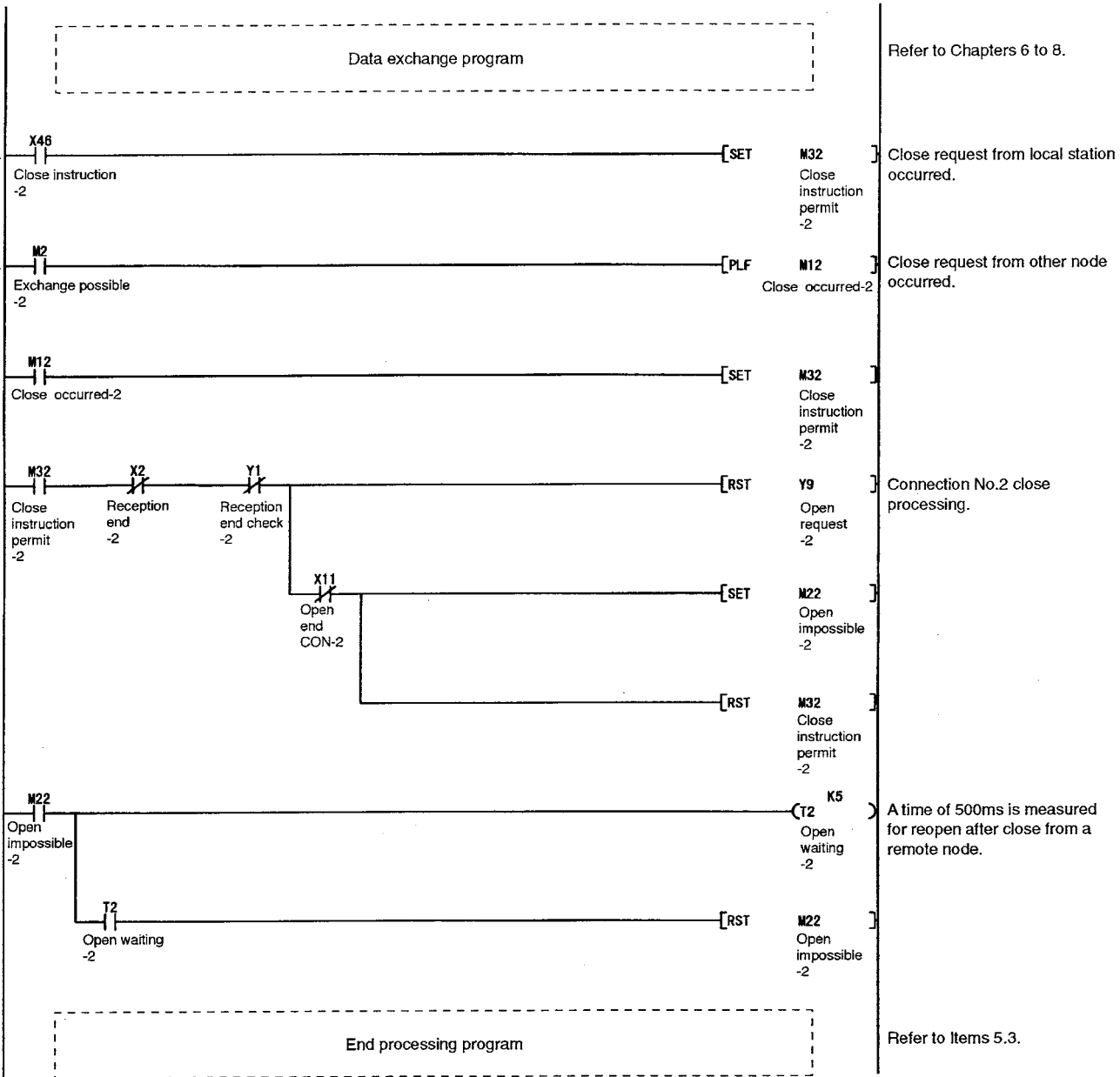
Exchange Parameter Name (When does not opening pairing)			Connection No.1	Connection No.2
Usage availability setting	Fixed buffer usage availability	Bit 0	0 : For transmission	1 : For reception
	Destination existence check	Bit 1	0 : Not performed	1 : Performed
	Pairing open	Bit 7	0 : Not performed	0 : Not performed
	Communication format	Bit 8	0 : TCP	0 : TCP
	Fixed buffer exchange procedure existence	Bit 9	0 : With procedure	0 : With procedure
	Open method	Bit 14 Bit 15	10 : Unpassive	10 : Unpassive
Exchange address setting	E71 port No.		500H	501H
	Remote node IP address		(Setting not required)	(Setting not required)
	Remote node port No.		(Setting not required)	(Setting not required)
	Remote node Ethernet address		(Default value)	(Default value)

Exchange Parameter Name (When opening pairing)			Connection No.1	Connection No.2
Usage availability setting	Fixed buffer usage availability	Bit 0	1 : For reception	(Setting not required)
	Destination existence check	Bit 1	1 : Performed	
	Pairing open	Bit 7	1 : Performed	
	Communication format	Bit 8	0 : TCP	
	Fixed buffer exchange procedure existence	Bit 9	0 : With procedure	
	Open method	Bit 14 Bit 15	10 : Unpassive	
Exchange address setting	QE71 port No.		500H	
	Remote node IP address		(Setting not required)	
	Remote node port No.		(Setting not required)	
	Remote node Ethernet address		(Default value)	

1 When not opening pairing







*1 The data exchange shown below can be conducted after setting of usage availability shown in the program.

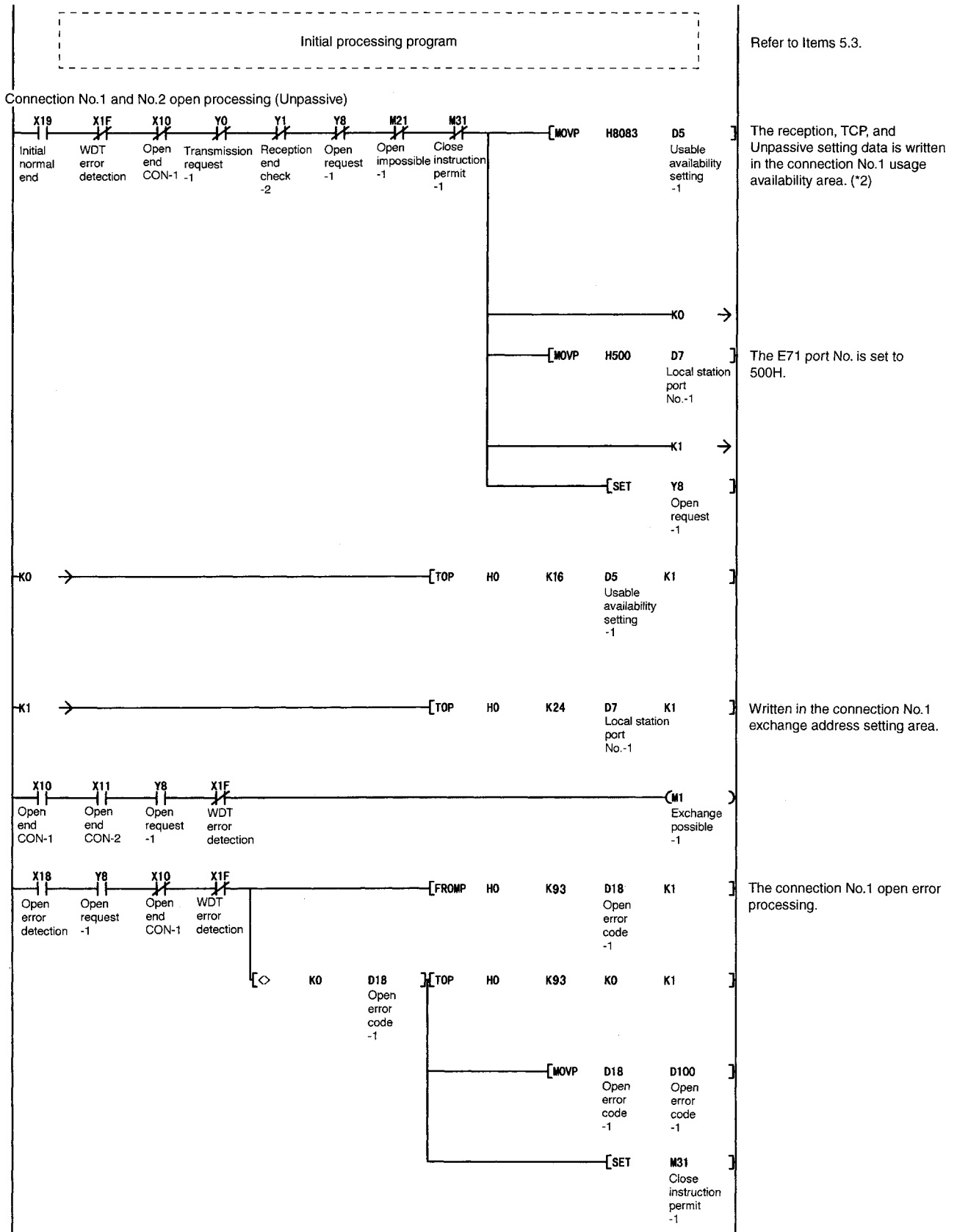
	Fixed buffer exchange		Random access buffer exchange	Read/write data in the PLC CPU
	With procedure	Without procedure		
Connection No.1	Exchange enabled (Transmission only)	Exchange disabled	Exchange enabled	Exchange enabled
Connection No.2	Exchange enabled (Reception only)	Exchange disabled	Exchange enabled	Exchange enabled

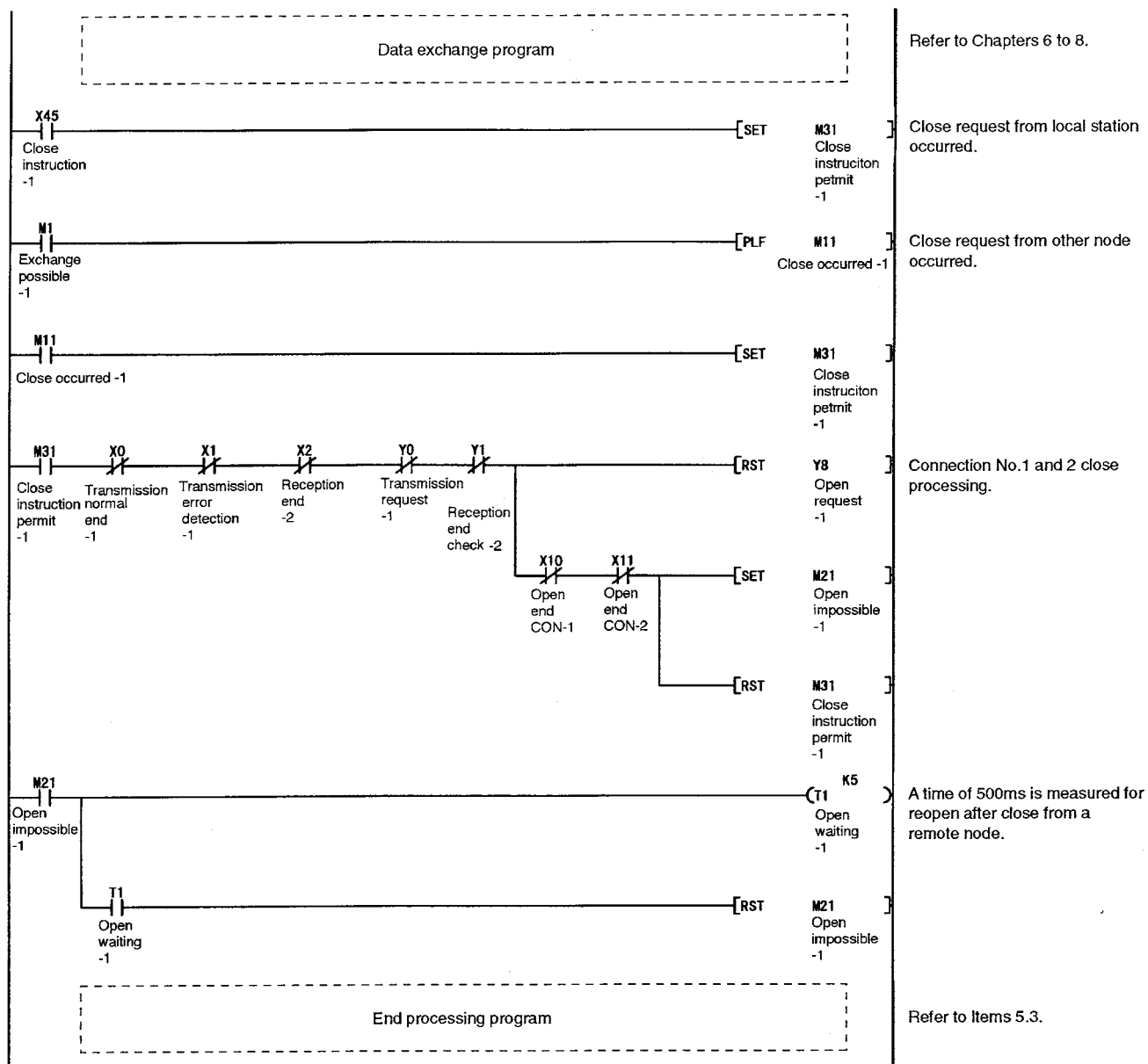
Point

The close processing part of the program example shown in this section indicates only when close processing is performed while all the input signals from the E71 for data exchange are off. For an example of a program when close processing is performed while all the input signals from the E71 for data exchange are not off, see Appendix 7.2.

2

When opening pairing





*2 The data exchange shown below can be conducted after setting of usage availability shown in the program.

	Fixed buffer exchange		Random access buffer exchange	Read/write data in the PLC CPU
	With procedure	Without procedure		
Connection No.1	Exchange enabled (Reception only)	Exchange disabled	Exchange enabled	Exchange enabled
Connection No.2	Exchange enabled (Transmission only)	Exchange disabled	Exchange enabled	Exchange enabled

5.5 Exchange State Storage Area

This section explains about the exchange state storage area where the initial processing state, open processing state, exchange state for each connection, and error log information are stored. Each processing result, exchange state for each connection, and error information can be checked by reading from this area.

5.5.1 Initial Processing State Storage Area

		Buffer memory	
(Address)		Initial Processing State Storage Area (6 words)	Default Value
50H (80)		Initial error code (1 word)	0H (0)
51 to 52H (81 to 82)		Local station E71's IP address (2 words)	0H (0)
53 to 55H (83 to 85)		Local station E71's Ethernet address (3 words)	0H (0)

* After initial process end the corresponding values are stored in order.

1 Initial error code (Default value = 0H) Address 50H (80)

- (a) Stores the error codes generated during initial processing execution.
- (b) Please refer to Chapter 13 for details regarding initial processing error codes.
- (c) Error codes are stored as binary values when the initial error detection signal (X1A) is on.
- (d) The error codes are cleared when the initial normal end signal is on, but the following process can also be used to clear them.
 - ① PLC CPU reset operation, or turning off the PLC power.
 - ② Using the sequence program to write (0) in the initial error code storage error

2 Local station E71's IP address (Default value = 0H) Address 51H to 52H....(81 to 82)

- (a) Stores the E71's IP address set during the initial processing execution.
- (b) The E71's IP address is stored as a binary value.

Example: The data storage condition when the IP address is A20009C0H (162.0.9.192) is shown below.

Address	Buffer memory
51H(81)	09C0H
52H(82)	A200H

3 Local station E71's Ethernet address (Default value = 0H) Address 53H to 55H....(83 to 85)

- (a) After initial processing, the E71's physical address is read from the ROM and stored. The Ethernet's physical address cannot be changed.
- (b) The E71's Ethernet address is stored from the newest address in the L to H order.

5.5.2 Exchange State Storage Area

		Buffer memory	
(Address)		Exchange State Storage Area (119 words)	Default Value
59H (89)	Local station E71's port No.	Information by Connection (10 words for connection No.1)	0H (0)
5A to 5BH (90 to 91)	Remote node IP address		0H (0)
5CH (92)	Remote node port No.		0H (0)
5DH (93)	Open error code		0H (0)
5EH (94)	Fixed buffer transmission/reception error code		0H (0)
5FH (95)	Fixed buffer exchange end code		0H (0)
60H (96)	Maximum value		0H (0)
61H (97)	Minimum value		0H (0)
62H (98)	Current value	Fixed buffer exchange's ex- change time	0H (0)
63 to 6CH (99 to 108)	Local station E71's port No. to	Information by connection (For connection No.2)	(Same as above)
6D to 76H (109 to 118)	Local station E71's port No. to	Information by connection (For connection No.3)	(Same as above)
77 to 80H (119 to 128)	Local station E71's port No. to	Information by connection (For connection No.4)	(Same as above)
81 to 8AH (129 to 138)	Local station E71's port No. to	Information by connection (For connection No.5)	(Same as above)
8B to 94H (139 to 148)	Local station E71's port No. to	Information by connection (For connection No.6)	(Same as above)
95 to 9EH (149 to 158)	Local station E71's port No. to	Information by connection (For connection No.7)	(Same as above)
9F to A8H (159 to 168)	Local station E71's port No. to	Information by connection (For connection No.8)	(Same as above)

* After processes end from initial processing the corresponding values are stored in order.

1 Local station E71's port No. (Default value = 0H) Address 59H....(89...)

- (a) Stores the port No. when the subject communication line was connected by open processing.
- (b) The storage values are not set during the closed state.

2 Remote node IP address (Default value = 0H) Address 5AH to 5BH....(90 to 91...)

- (a) Stores the partner remote node's IP address for when the subject communication line was connected using open processing.

(Example) The following is the data that is stored when the IP address is "A20009C0H."

Address	Buffer memory
5AH(90)	09C0H
5BH(91)	A200H

- (b) The stored values are not set that are in the closed state.

- 3 Remote node port No. (Default value = 0H) Address 5CH....(92...)**
- (a) Stores the partner remote node port No. for the communication line connected by open processing.
 - (b) The stored value is not set in the closed state.
- 4 Open error code (Default value = 0H) Address 5DH....(93...)**
- (a) Stores the open processing results of the subject communication line.
 - (b) The open processing results are stored as binary values.
 - 0 : Normal end
 - Other than 0 : Error end (refer to Chapter 13 for information regarding error control items.)
 - (c) Conducting the operation shown below will clear the error code.
 - ① When reopening the connection for which the open error occurred. (When the open request signal is turned OFF to ON)
 - ② When a PLC CPU reset operation was conducted or when the PLC power supply was turned off
- 5 Fixed buffer transmission/reception error code (Default Value = 0H) Address 5EH....(94...)**
- (a) Stores the error code (refer to Chapter 13 for details regarding error code items) when the error detection signal (X1, etc.) is turned on by the data transmission/reception with the remote node during fixed buffer exchange on the corresponding communication line.
 - (b) The transmission error codes are cleared under the following conditions.
 - The fixed buffer transmission request/reception end check signal for the connection for which an error occurred is turned off.
 - A reset operation is conducted for the PLC CPU or the PLC's power supply is turned off.
- 6 Fixed buffer exchange end code (Default value = 0H) Address 5FH....(95...)**
- (a) The error codes that are returned as a response from the remote node during fixed buffer exchange on the subject communication line are stored as binary values.
 - (b) The action to be taken by the end code in the response is conducted in accordance with the arrangements with the remote node.
 - (c) The end code can be cleared by conducting the following operations.
 - A reset operation is conducted for the PLC CPU or the PLC's power supply is turned off.
- 7 Fixed buffer exchange's exchange time (Each default value = 0H)60H to 62H....(96 to 98...)**
- (a) Each of the fixed buffer exchange processing times (maximum value, minimum value, current value) are stored.
 - ① Fixed buffer transmission processing time
 - The time from when the transmission request signal turns on to when the E71 conducts a transmission end.
 - (The processing time is not stored when the transmission error occurred.)
 - ② Fixed buffer reception processing time
 - The time from when the reception end signal turns on to when the E71 ends in response to the response return processing from the remote node.
 - (b) The processing time is stored as a binary value in 10ms units.
 - (c) Each exchange time is changed to "0," when the subject communication line's open request signal (Y8 to YF) is changed from off to on.

5.5.3 Error Log Area

Buffer memory		
(Address)	Error log area (11 words)	Default Value
A9H (169)	Error log	Area-1 0H (0)
AAH (170)		Area-2 0H (0)
ABH (171)		Area-3 0H (0)
ACH (172)		Area-4 0H (0)
ADH (173)		Area-5 0H (0)
AEH (174)		Area-6 0H (0)
AFH (175)		Area-7 0H (0)
B0H (176)		Area-8 0H (0)
B1H (177)		Area-9 0H (0)
B2H (178)		Area-10 0H (0)
B3H (179)		Area-11 0H (0)

- (1) The following two areas are storage areas in which the codes that show the error contents are stored.

(Refer to Chapter 13)

- ① When the occurrence origin of the error that occurred cannot be checked
IP level error, reception data sum check error (TCP/UDP/IP check sum error).
- ② An error that occurred during random access buffer exchange or read/write of data in the PLC CPU.

Remarks

The error that occurred during the fixed buffer transmission is stored in the fixed buffer transmission error code (buffer memory addresses 94, 104, 114, ... 164) area.

- (2) This error area has an 11 word data area and is configured of ring buffers that can store up to 10 units of error information. The next area to be set is normally set to 0000H and this makes it possible to determine what data is the newest.
- (3) The value stored in this area is cleared when the power to the station installed in the E71 is turned on or when a reset operation is conducted. In addition, it can be cleared if the user writes a 0. It cannot be cleared using initial processing.
- (4) It is not normally necessary to read this area, so read it when necessary when conducting maintenance.

5.5.4 Protocol Status Storage Area

This is the area that is used to store the number of times of a protocol level that is used during exchange between the E71 and a remote node. (This is the count value controlled by the E71)

When the count value exceeds one word, the count is stopped at FFFFH (65535).

The storage values for all the protocol status storage areas can be cleared by the user writing a 0. In addition, they are cleared when the power is turned on to the station installed in the E71 and when a reset operation is conducted. (They are not cleared during initial processing.)

Normally it is not necessary to read this area, so read it when necessary during maintenance.

		Buffer memory	
(Address)		Exchange status storage area (80 words)	Default Value
170H (368)	Number of times IP packets received (1 word)	0H (0) *
171H (369)	Number of times received IP packet discarded because of check sum error (1 word)	0H (0) *
172H (370)	Total number of transmitted IP packets (1 word)	0H (0) *
173H (371)	System area (Use prohibited) (1 word)	——
174H (372)	Number of received ARP packets (1 word)	0H (0) *
175H (373)	Number of responses to ARP packets (1 word)	0H (0) *
176H (374)	Number of received IP packets that are not broadcasted to local station (1 word)	0H (0) *
177H (375)	System area (Use prohibited) (1 word)	0H (0) *
178H (376)	Number of transmitted ARP packets (1 word)	0H (0) *
179H (377)	Number of transmitted broadcast packets (1 word)	0H (0) *
17A to 17FH (378 to 383)	System area (Use prohibited) (6 words)	——
180H (384)	Total number of received ICMP (1 word)	0H (0) *
181H (385)	Total number of times received ICMP packet discarded because of check sum error (1 word)	0H (0) *
182H (386)	Total number of transmitted ICMP packets (1 word)	0H (0) *
183H (387)	Total number of received ICMP echo request packets (1 word)	0H (0) *
184H (388)	Total number of transmitted ICMP echo reply packets (1 word)	0H (0) *
185H (389)	Total number of transmitted ICMP echo request packets (1 word)	0H (0) *
186H (390)	Total number of received ICMP echo reply packets (1 word)	0H (0) *
187 to 18FH (391 to 399)	Total number of received TCP packets (1 word)	——
190H (400)	System area (Use prohibited) (9 words)	0H (0) *
191H (401)	Total number of times received TCP packet discarded because of check sum error (1 word)	0H (0) *
192H (402)	Total number of transmitted TCP packets (1 word)	0H (0) *
193H (403)	Number of ZERO-Window time-outs (1 word)	0H (0) *
194 to 19FH (404 to 415)	System area (Use prohibited) (12 words)	——
1A0H (416)	Total number of received UDP packets (1 word)	0H (0) *
1A1H (417)	Total number of times received UDP packet discarded because of check sum error (1 word)	0H (0) *
1A2H (418)	Total number of transmitted UDP packets (1 word)	0H (0) *
1A3H (419)	Number of times received UDP packets discarded because addressed to other than the host (1 word)	0H (0) *
1A4 to 1BFH (420 to 447)	System area (Use prohibited) (28 words)	——

* This shows the areas that can be cleared with a 0 from the sequence program.

5.6 Data Exchange during the PLC CPU is Stopped

This section explains the settings etc., that make it possible to continue data exchange from remote nodes to the E71 even after the PLC CPU in the station installed in the E71 enters the STOP status and the E71 open request signals (Y8 to YF) are turned off.

Data exchange while the PLC CPU is stopped can be conducted using the settings shown in Item 5.6.1 after the initial processing and open processing from the PLC CPU are completed.

Point

Be sure to match the system specifications when conducting data exchange while the PLC CPU is stopped.

5.6.1 Settings for Continuing Data Exchange

This section explains the settings for using the data exchange function while the PLC CPU is stopped.

The relationship between the data exchange when the PLC CPU is stopped and this setting is shown in Item 5.6.3.

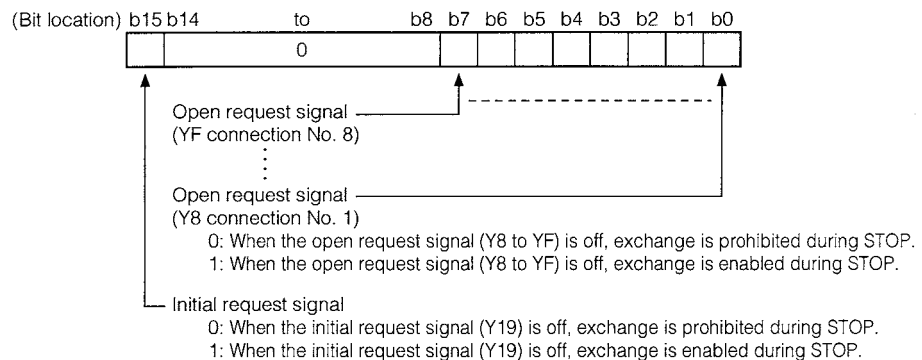
1

Setting method

The setting that uses the data exchange function while the PLC CPU is stopped is done using the buffer memory exchange specification during STOP area (address 1F0H).

2

Setting value for the exchange specification during STOP area



3

Specification of the setting value to the exchange instruction area during STOP

To continue exchange after the initial request signal (Y19) and open request signals (Y8 to YF) are turned off because the PLC CPU is stopped, etc., turn on the bit 15 of the exchange instruction area during STOP.

(Example) To continue exchange between connections No.1 and No.2 after the initial request signal (Y19) and open request signal (Y8, Y9) are turned off, set "8003H".

Point

- (1) When continuing the exchange with remote nodes while the PLC CPU is at the stop state, always set the bit 15 of the above exchange instruction area during STOP to "1 (on)".
- (2) When continuing the exchange when the initial request signal (Y19) is on and the open request signal (Y8 to YF) is turned from on to off, turn on the bit for corresponding connection No. of the exchange instruction area during STOP.

(Example) To continue the exchange between connections No. 1 and No. 2 when the initial request signal (Y19) is on and the open request signals (Y8, Y9) are off, set "0003H".

5.6.2 Functions for Which Continuing Data Exchange is Possible

When conducting the setting to exchange data while the PLC CPU is stopped, the following shows the functions that make it possible to continue data exchange between a remote node and the E71 even after the PLC CPU of the station installed in the E71 enters the STOP status and the E71 open request (Y8 to YF) and initial request signal (Y19) have turned off.

Data exchange function	Data exchange while the PLC CPU is stopped
Fixed buffer exchange (with procedure, without procedure)	Not possible
Random access buffer exchange	Possible
Exchange for reading/writing data in the PLC CPU	Possible

5.6.3 Relationship between the Setting and Data Exchange during the PLC CPU is Stopped

The setting that performs the data exchange while the PLC CPU is stopped after the initial processing and open processing from the PLC CPU is completed is valid.

After the setting for data exchange while the PLC CPU is stopped is valid, the PLC CPU enters the stop status, and the E71 initial request signal (Y19) and open request signals (Y8 to YF) are turned off, it is possible to continue data exchange from the remote node to the E71 while the PLC CPU is stopped.

The setting used for the data exchange function while the PLC CPU is stopped, the I/O signal (initial request signal and open request signal) with the PLC CPU, and the relationship for data exchange while the PLC CPU is stopped are shown below.

A concrete example is shown on the next page.

- ① Data exchange is possible while the PLC CPU is stopped when exchange enable during STOP is set using the buffer memory's exchange specification during STOP area (address 1F0H).
- ② When exchange enable during STOP is set, the change from on to off of the initial request signal (Y19) and the open request signals (Y8 to YF) are ignored.

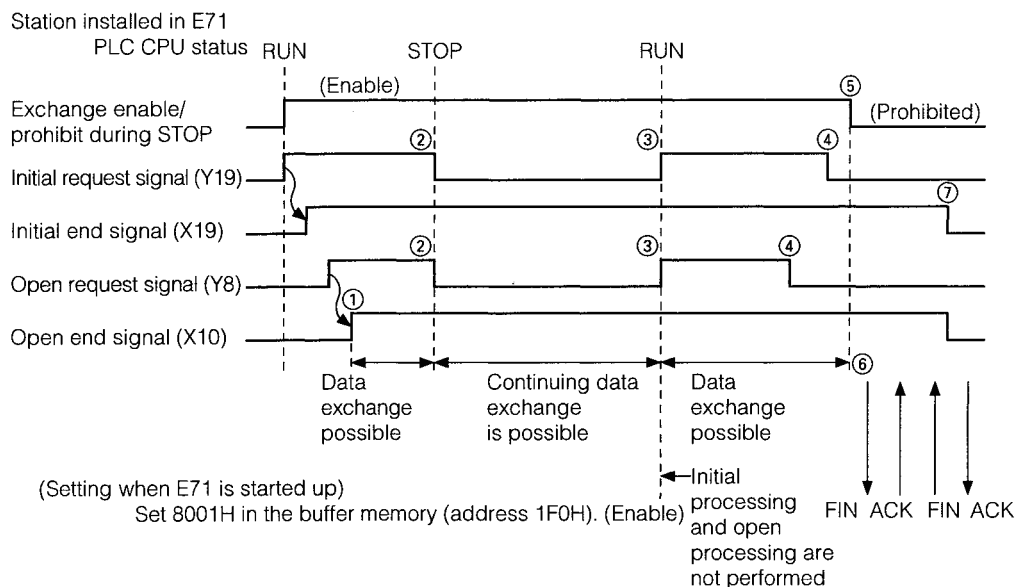
	QE71 processing	
	OFF → ON	ON → OFF
Initial request signal (Y19)	Initial processing is conducted. (*1)	End processing is not conducted.
Open request signal (Y8 to YF)	Open processing is conducted. (*1)	Close processing is not conducted.

*1 Only when the signal is turned on first.

Point

When the buffer memory's exchange specification during STOP area setting is the default value, then data exchange cannot be conducted when the PLC CPU is stopped. Conduct data exchange by conducting initial processing, open processing, close processing, and end processing in accordance with the procedure shown in Items 5.1 to 5.4.

1 When the communication circuit is opened by a setting to allow data exchange through connection No. 1 while the PLC CPU is stopped

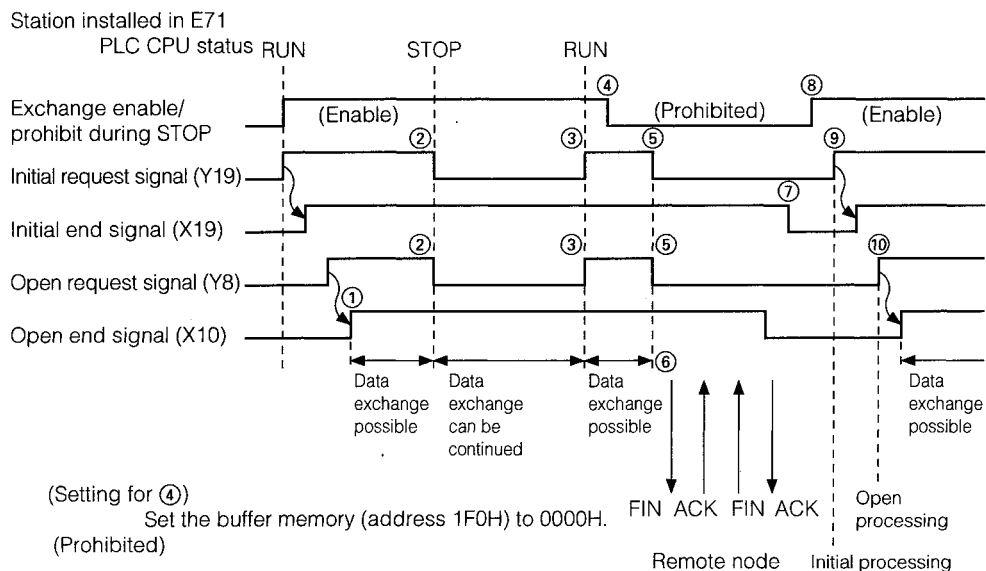


- ① Data exchange with a remote node is possible by turning on the open end signal (X10). (Fixed buffer exchange, random access buffer exchange, and exchange of read/write data in the PLC CPU are possible.)
- ② The PLC CPU of the station installed in the E71 enters the STOP status and the initial signal (Y19) and open request signal (Y8) turn off. Close processing and end processing are not performed because exchange enable during STOP is set. Data exchange can be continued. (Random access buffer exchange and exchange of read/write data in the PLC CPU are possible.)
- ③ The PLC CPU of the station installed in the E71 enters the RUN status and the initial request signal (Y19) and open request signal (Y8) turn on. Initial processing and open processing are not conducted because exchange enable during STOP is set. Data exchange can be continued. (Fixed buffer exchange, random access buffer exchange, and exchange of read/write data in the PLC CPU are possible.)
- ④ The open request signal (Y8) and initial request signal (Y19) are turned off. Close processing and end processing are not conducted because exchange enable during STOP is set. Data exchange can be continued. (Random access buffer exchange and exchange of read/write data in the PLC CPU are possible.)
- ⑤ Change the setting to exchange prohibited during STOP. Set the buffer memory (address 1F0H) to 0000H.
- ⑥ Exchange circuit close processing is conducted after the open request signal (Y8) is turned off after the setting has been changed to exchange prohibit during STOP.
- ⑦ End processing is conducted after the initial request signal (Y19) is turned off after the setting is changed to exchange prohibit during STOP.

2 When initial processing and open processing are reformed when the setting for data exchange while the PLC CPU is stopped is changed midway for connection No. 1

(Changed from exchange data while the PLC CPU is stopped to do not exchange data while the PLC CPU is stopped.)

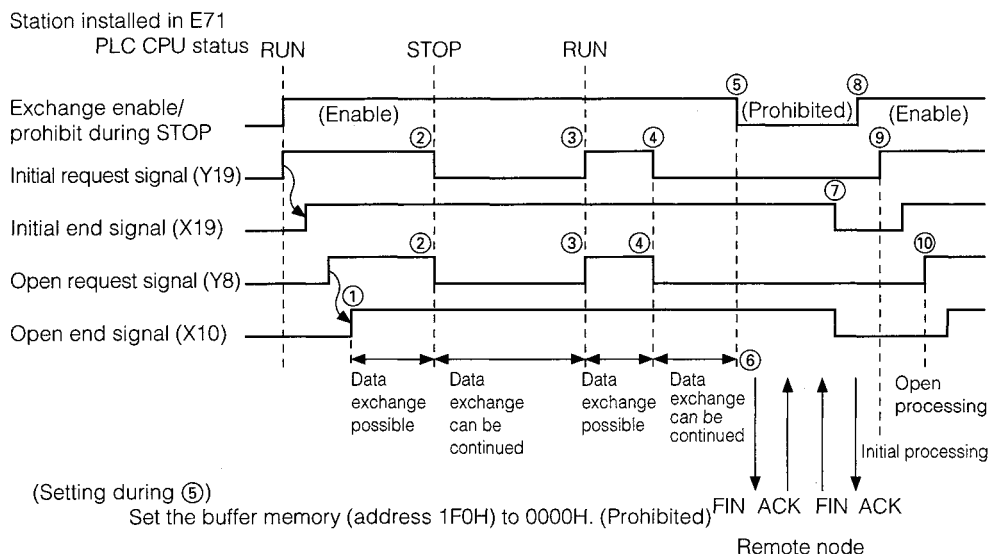
- (a) When the open request signal (Y8) and the initial request signal (Y19) are turned off after the setting is changed to exchange prohibited



- ① Data exchange to a remote node becomes possible by turning the open end signal (X10) on. (Fixed buffer exchange, random access buffer exchange, and exchange of read/write data in the PLC CPU are possible.)
- ② The PLC CPU in the station installed in the E71 enters the STOP status and the initial request signal (Y19) and the open request signal (Y8) turn off. Close processing and end processing are not conducted because exchange enable during STOP is set. Data exchange can be continued. (Random access buffer exchange and exchange of read/write data in the PLC CPU are possible.)
- ③ The PLC CPU in the station installed in the E71 enters the RUN state and the initial request signal (Y19) and open request signal (Y8) are turned on. Initial processing and open processing are not conducted because the exchange enable during STOP is set. Data exchange can be continued. (Fixed buffer exchange, random access exchange, and exchange of read/write data in the PLC CPU are possible.)
- ④ Change the setting to exchange prohibited during STOP. Set the buffer memory (address 1F0H) to 0000H. Close processing and end processing are not conducted because the initial request signal (Y19) and open request signal (Y8) are turned on. Data exchange can be continued. (Fixed buffer exchange, random access buffer exchange, and exchange of read/write data in the PLC CPU are possible.)

- ⑤ The initial request signal (Y19) and open request signal (Y8) are turned off.
- ⑥ Close processing is conducted because the exchange prohibited during STOP is set.
- ⑦ End processing is conducted for the same reason as in ⑥ above.
- ⑧ Change the setting to exchange enable during STOP. Set the buffer memory (address 1F0h) to 8001H. Initial processing and open processing are not conducted because the initial request signal (Y19) and open request signal (Y8) are turned off.
- ⑨ The initial request signal (Y19) is turned on to reconduct initial processing. E71 initial processing is conducted.
- ⑩ The request signal (Y8) is turned on to reconduct open processing. Open processing of the exchange circuit with the remote node is conducted. (Fixed buffer exchange, random access buffer exchange, and exchange of read/write data in the PLC CPU are possible.)

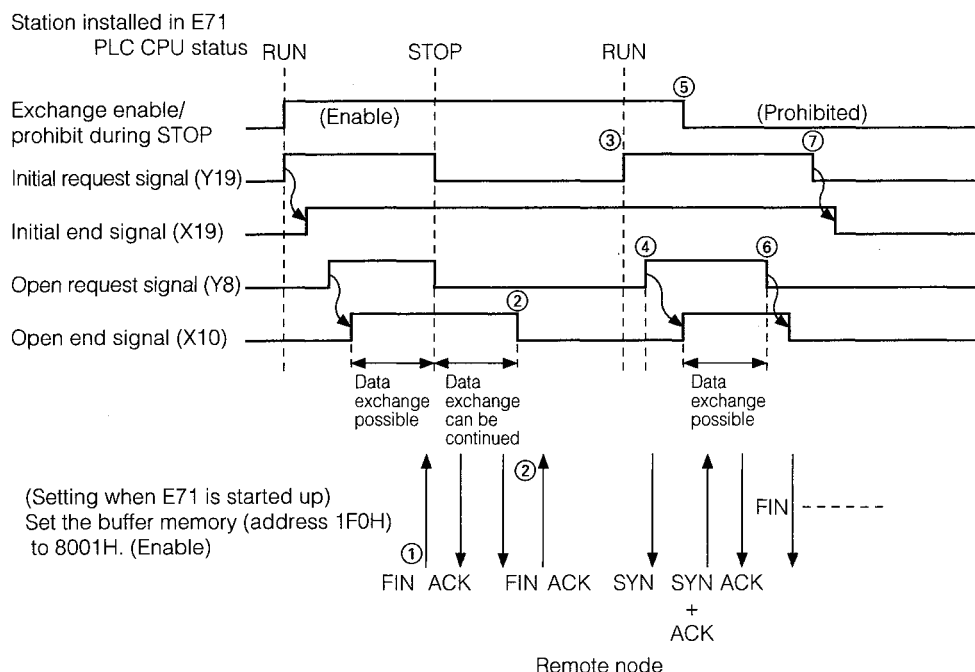
- (b) When changing the setting to exchange prohibited after turning the open request signal (Y8) and initial request signal (Y19) off



- ① Data exchange with the remote node can be conducted by turning the open end signal (X10) on. (Fixed buffer exchange, random access buffer exchange, and exchange of read/write data in the PLC CPU are possible.)
- ② The PLC CPU of the station installed in the E71 enters the STOP status, and the initial request signal (Y19) and open request signal (Y8) are turned off. Close processing and end processing are not conducted because exchange enable during STOP is set. Data exchange can be continued. (Random access buffer exchange and exchange of read/write data in the PLC CPU are possible.)
- ③ The PLC CPU in the station installed in the E71 enters the RUN status and the initial request signal (Y19) and open request signal (Y8) are turned on. Initial processing and open processing are not conducted because the exchange enable during STOP is set. Data exchange can be continued. (Fixed buffer exchange, random access buffer exchange, and exchange of read/write data in the PLC CPU are possible.)
- ④ The initial request signal (Y19) and open request signal (Y8) are turned off. Close processing and end processing are not conducted because exchange enable during STOP is set. Data exchange can be continued. (Random access buffer exchange and exchange of read/write data in the PLC CPU are possible.)
- ⑤ Change the setting to exchange prohibited during STOP. Set the buffer memory (address 1F0H) to 0000H.
- ⑥ Communication line close processing is conducted after the setting is changed to exchange prohibit during STOP because the open request signal (Y8) is turned off.

- ⑦ End processing is conducted because the initial request signal (Y19) is turned off after the setting is changed to exchange prohibited during stop.
- ⑧ Change the setting to exchange enable during STOP. Set the buffer memory (address 1F0H) to 8001H.
- ⑨ The initial request signal (Y19) is turned on to reconduct initial processing. E71 initial processing is conducted.
- ⑩ The open request signal (Y8) is turned on to reconduct open processing. Open processing of the communication line with the remote node is conducted. (Fixed buffer exchange, random access buffer exchange, and exchange of read/write data in the PLC CPU are possible.)

- 3** When a close request is received from the partner remote node when data is being exchange because the setting allows data exchange through connection No. 1 while the PLC CPU is stopped.



- ① A close request is received from the partner remote node when data is exchanged with the remote node using the data exchange function when the PLC CPU is stopped.
 - ② Close processing is conducted in the open end signal (X10) is turned off. Data exchange cannot be conducted.
- * FIN is transmitted even if the open request signal (Y8) is turned off.
- ③ The PLC CPU of the station installed in E71 enters the RUN status and the initial request signal (Y19) is turned on. Initial processing is not conducted because the exchange enable during STOP is set.
 - ④ Open request signal (Y8) turns on and open processing is conducted and then the open end signal (X10) is turned on. Data exchange with the remote node becomes possible. (Fixed buffer exchange, random access buffer exchange, and exchange of read/write data in the PLC CPU are possible.)
 - ⑤ Change the setting to exchange prohibited during STOP. Set the buffer memory (1F0H) to 0000H. Close processing and end processing are not conducted because the initial request signal (Y19) and open request signal (Y8) are turned on. Data exchange can be continued.
 - ⑥ Communication circuit close processing is conducted because the open request signal (Y8) turns off after the setting is changed to exchange prohibited during STOP.
 - ⑦ End processing is conducted when the initial request signal (Y19) turns off after the setting is changed to exchange prohibited during STOP.

5.7 Precautions for Programming

This section describes the precautions for programming to perform data exchange between the PLC CPU and remote nodes using the E71.

1

Countermeasures for data exchange problems during communication

Perform the following processing if the connection being used is closed due to a data exchange problem after open processing has completed normally.

- ① Turn off the transmission request signal/reception completion confirmation signal (Y0 to Y7).
- ② After the following input signals corresponding to the above signals are set to off, perform open processing again.
 - Transmission normal completion signal, reception completion signal (X0, X2...)
 - Transmission error detection signal, reception error detection signal (X1, X3...)

As for the timing to reopen the connection being used, see Item 5.4.3.

Program examples are shown in Item 5.4.5 as well as Item 7.2 of Appendix.

Remarks

When the existence check function of the E71 is used (see Remark at the end of Item 5.4.3)

- If communication is disabled due to the occurrence of a sudden problem (down, etc.) on the remote node side, the E71 will close (disconnect the line) the applicable connection if it detects an error using the existence check function.

2

Data exchange using a single connection

In order to prevent complications with application programs, it is recommended to open a connection (port) for each exchange function, and then perform data exchange.

(Example) To perform transmission/reception of fixed buffer data exchange and read/write exchange of data in the PLC CPU with the PLC side, open the following connections for data exchange, and then exchange data.

- For transmission of fixed buffer data exchange
- For reception of fixed buffer data exchange
- For read/write exchange of data in the PLC CPU

3

Capacity of data to be sent/received

To use the data exchange function using fixed buffer (with procedure) and data exchange function using random access buffer of the E71, specify the data length in the application data section of the message to be sent from a remote node to the E71 using the actual data size of the text section.

If the data length is not correct, a response of abnormal completion will be returned.

4

Code for exchange data

Specify either ASCII code or binary code according to the setting value of the E71's exchange condition setting switch (SW2: data code setting) in order to perform data exchange.

When SW2 is off: Data is exchanged using binary code

When SW2 is on: Data is exchanged using ASCII code

Referring to the reference section of the function to be used, check the arrangement of data for exchange.

5

Precautions for creating sequence programs for the E71

- (a) Be sure to follow the procedure below when performing various processing such as initial/open/data transmission and reception.

- ① Load the data (such as exchange conditions) for the applicable processing into the buffer memory.
- ② Change the request signal (output (Y)) for the applicable processing from off to on.
- ③ Check whether the normal completion signal (input (X)) for the applicable processing has changed from off to on.

* In the case of abnormal completion, read the error information stored in the buffer memory, change the request signal (output (Y)) of the applicable processing from on to off, and then check whether the abnormal completion signal (input (X)) for the applicable processing has changed from on to off.

After error processing completes, repeat from ① again.

- ④ Change the request signal (output (Y)) for the applicable processing from on to off.
- ⑤ Check whether the normal completion signal (input (X)) for the applicable processing has changed from on to off.

- (b) When performing TCP/IP communication, if the open processing of a connection from a remote node completes normally, but the same connection must be closed immediately, perform close processing after the open normal completion signal (X10 to X17) to the PLC CPU stays on for at least one scan time.

* This is necessary so that the PLC CPU can recognize the normal completion of open processing.

- (c) When performing TCP/IP communication, be sure to follow the procedure below to perform reopen processing on the E71 side when close processing is performed from the remote node side after open processing has completed normally. (See Item 5.4.3 [2].)

- ① Close from a remote node (open normal completion signal of the E71 changes from on to off)
- ② Close processing on the E71 side (open request signal changes from on to off)
- ③ Minimum 500 ms wait (wait time for the E71)

* If close processing is performed from the E71 side, it is not necessary to set up the wait time.

- ④ Open processing on both the E71 and remote node sides (E71 side: Open request signal changes from off to on)

- (d) The detection of an open error for the eight connections of the E71 is notified to the PLC CPU side with a single open error detection signal (X18).

If open processing for any of the E71's eight connections ends with an error, read all open error code storage areas of the buffer memory of each connection for which the open request signal is on, and then perform error processing for the error code.

6**Precautions for creating application programs on the remote node side****(a) Open/close processing of connections with the E71 side**

Because the reopen processing time indicated in (b) below is required, reduce the number of connection open/close processing with the E71 side.

- * Open the connection when starting data exchange, and close the connection when a series of data exchange (multiple number of data exchanges) is completed.

(b) Open processing of connections with the E71 side

When performing TCP/IP communication, take the time required on the E71 side shown below into account.

- If the open processing of a connection with the E71 (*1) completes normally, but the same connection must be closed immediately, perform close processing after at least one scan time has elapsed on the PLC CPU side following the normal completion of the open processing of the connection.

*1 This is the same when the open processing on the remote node side with which data is to be exchanged is either active or passive open.

- To perform an active open again after the close processing of the connection with the E71 has completed, wait for at least 500 ms following the normal completion of the close processing on the E71 side, and then perform open processing.

(c) Retry processing for data exchange errors

As for data exchange errors that may occur in application programs, create application programs so that data exchange processing that causes an error can be retried in order to assure the quality of communication (retry at least once).

However, be sure to perform retry only after normal completion/abnormal completion for the previous processing request is returned.

(d) Checking the specifications of the E71

Check the specifications of the E71 indicated in sections explaining the exchange function to be used.

① Data section code and exchange data capacity (see Item 3.3)

- * For the E71 side, specify the code during exchange using the exchange condition setting switch (SW2: data code setting) on the main unit.

② Division and data length of exchange messages (see Item 3.5.1)**③ Exchange procedure (see Item 3.5.2)**

- * While in TCP/IP communication, monitor the response reception with an application program.

Set the monitoring time larger than the TCP retransmission timer value.

Also, it is recommended to make the monitoring time larger than the TCP ULP timeout time.

④ Forced disconnection condition of connections (see Item 3.5.3)

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FIXED BUFFER EXCHANGE SECTION

The fixed buffer exchange section explains the with procedure data exchange methods and the without procedure data exchange method when data is exchanged with remote node external equipment that uses the Ethernet interface module's fixed buffer and with the PLC CPU.

Fixed buffer exchange is begun after initial processing and open processing that is described in Chapter 5 connects the communication line.

In addition, conduct close processing and end processing during data exchange end on the subject communication line.

When conducting fixed buffer exchange with procedures, read Chapter 6.

When conducting fixed buffer exchange without procedures, read Chapter 7.

6. FIXED BUFFER EXCHANGE WITH PROCEDURE

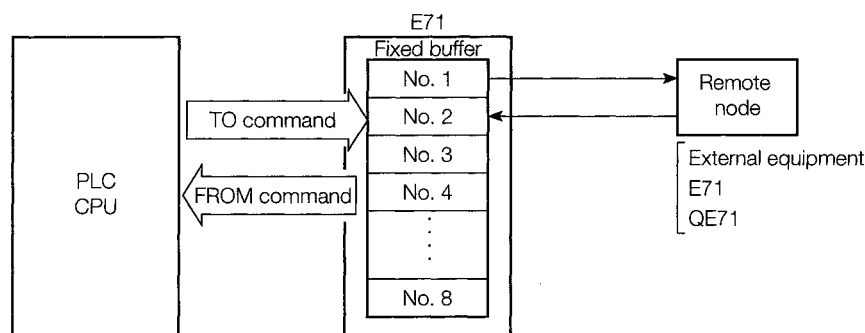
This section explains the method used to conduct exchange with a remote node with procedures using the E71's fixed buffer.

6.1 Control Format

This section explains the control format used for fixed buffer exchange with procedure.

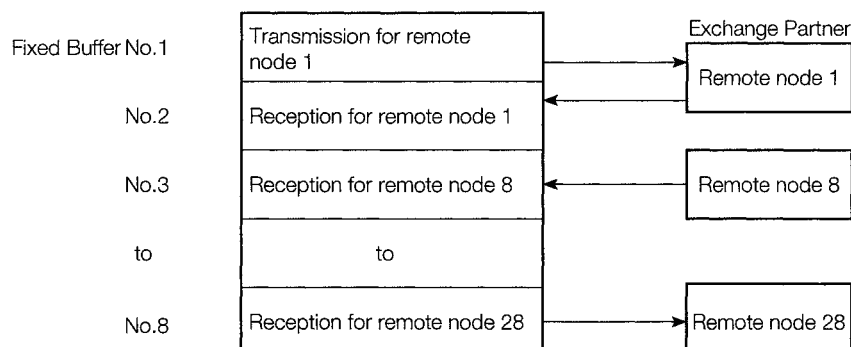
Remote node exchange processing using the fixed buffer is conducted during the handshake with the remote node for data transmission from the PLC CPU and data reception from the remote node.

(1) The exchange processing data flow is as follows.



(2) Data exchange can be conducted with remote nodes in the Ethernet to which E71 is connected and with remote nodes that are connected by a router relay function (Refer to Chapter 12). As is shown in the diagram below, the various fixed buffers (No. 1 to No. 8) are used to set the remote node with which to exchange and the usage availability (for transmission and for reception, with procedures and without procedures, etc.) using the E71's communication line open (Refer to Item 5.5) to set the exchange partner for each buffer.

- ① When TCP/IP is used, a fixed buffer exchange partner setting using the parameter settings becomes valid when the E71's open end signal changes from OFF to ON during boot-up. The exchange partner cannot be changed while the open end signal is on.
- ② When UDP/IP is used, the fixed buffer exchange partner can be changed after open processing. (It is possible to change the exchange parameter's remote node IP address and remote node port No., but is not possible to change the local station's E71's port No.)



Point

- (1) When with procedures is selected during opening, random access buffer exchange, and reading and writing data to the PLC CPU exchange can be conducted at the same time as fixed buffer exchange (transmission or reception) with procedure for the subject connection. (Refer to Item 5.1 (*1))
- (2) When changing the exchange partner in the UDP/IP communication, do not conduct pairing setting (Refer to Item 5.4.1 in 1 (b) ③) and existence check setting (Refer to *2 at the end of Item 5.3.1). If these settings are made the E71 will not operate correctly.

(3) The transmission and reception processing during data transmission and reception is given below.

① During transmission

When the transmission request signal (Y0 to Y7) is ON, the E71 transfers the subject fixed buffer data to the remote node set in the subject area with a buffer memory address of 18H to 4FH (24 to 79). (*1)

② During reception

If there is reception from the remote node set in the subject area for the buffer memory addresses 18H to 4FH (24 to 79), the E71 will conduct reception processing. (*1)

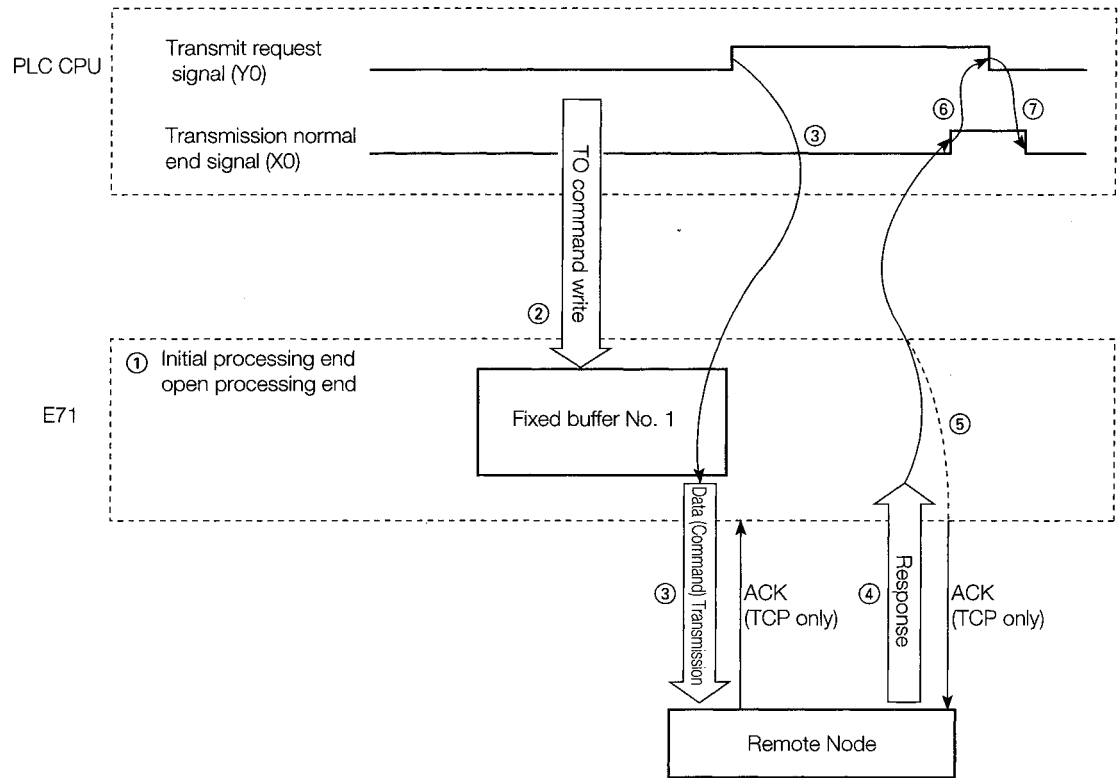
In addition, when the reception data is stored in the subject fixed buffer during reception processing, the E71 updates the subject connection's remote node IP address and remote node port No. for the buffer memory addresses 59H to A8H (89 to 168).

If there is a reception from a remote node that is not set in the buffer memory addresses 18H to 4FH (24 to 79), the E71 will ignore the reception data.

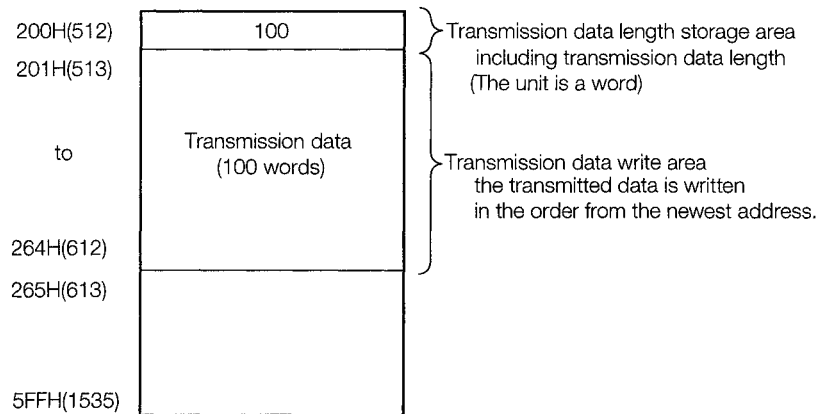
*1 During TCP/IP unpassive open, data is transmitted to and received from the remote nodes stored in the subject area for the buffer memory addresses 59H to A8H (89 to 168).

6.1.1 Transmission Control Method

This section explains about the control method when data is transmitted to the remote node by the E71 using an example where the fixed buffer No. 1's data is transmitted to a remote node.



- ① The E71 initial processing is conducted. (Refer to Items 5.2 to 5.3)
Line open processing with a remote node is conducted. (Refer to Item 5.4)
- ② The sequence program's TO command writes the transmission data length and transmission data in the E71's fixed buffer.
The transmission data length is written to the subject fixed buffer's first address (512). The transmission data is written to the subject fixed buffer's first address + 1.
The following diagram shows an example of a 100-word transmission using fixed buffer No. 1.



- ③ Changing the transmission request signal (Y0) to ON using the sequence program transmits the data to the node (from the parameter settings) that is specified by the fixed buffer (No.1).
- ④ When data is received from the E71 by the specified remote node, a response is returned to the E71.
- ⑤ The E71 turns ON the transmission normal end signal (X0) when the response is received from the remote node.
- ⑥ When the transmission normal end signal turns on, the sequence program turns OFF the transmission request signal (Y0).
- ⑦ The transmission normal end signal is automatically turned OFF when the transmission request signal is turned to OFF.

Point

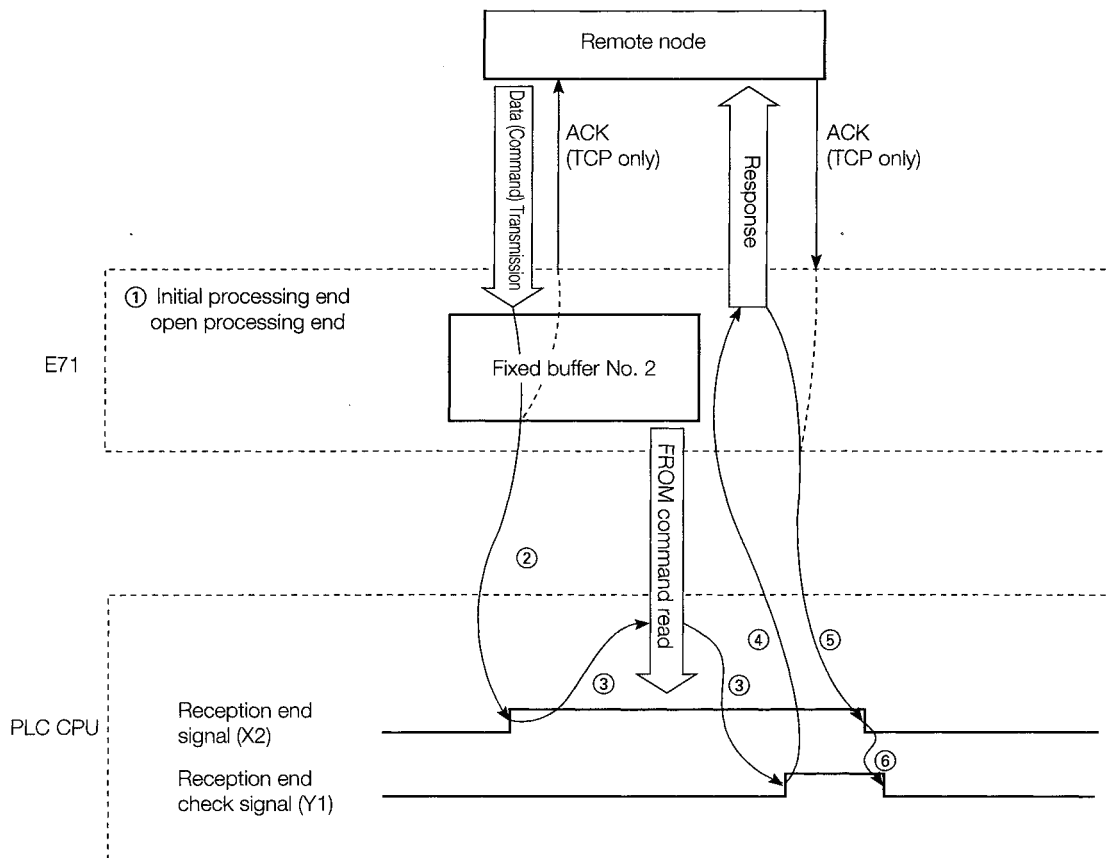
- (1) At the following times the transmission error detection signal (X1) turns ON so conduct retransmission processing when the transmission request signal turns from OFF to ON after the transmission error processing has ended.
 - ① When a response is not received within the response monitoring timer value.
 - ② When the response end code is anything other than "00H."
- (2) This shows the processing when the open request signal and initial request signal are turned off during transmission when the E71 "Function for Data Exchange during the PLC CPU is Stopped" (Item 5.6) is not used.
 - ① The E71 conducts close processing after the transmission processing end when the open request signal (Y8) turns OFF during transmission.
 - ② The E71 conducts close processing and end processing after transmission processing end when the initial request signal (Y19) turns OFF during transmission.

Remarks

For more details on cases when the reception of Close/Abort (RST) has occurred from the partner remote node prior to the completion of the data communication performed immediately before, refer to Remarks in 2 of Item 5.4.3.

6.1.2 Reception Control Method

This section explains the control method that the E71 receives the data from the remote node receiving data from the remote node to the fixed buffer No.2 as an example.



- ① The E71 initial processing is conducted. (Refer to Items 5.2 to 5.3)

The remote node and line opening processing. (Refer to Item 5.4)

To conduct fixed buffer exchange, initial processing and open processing must be completed.

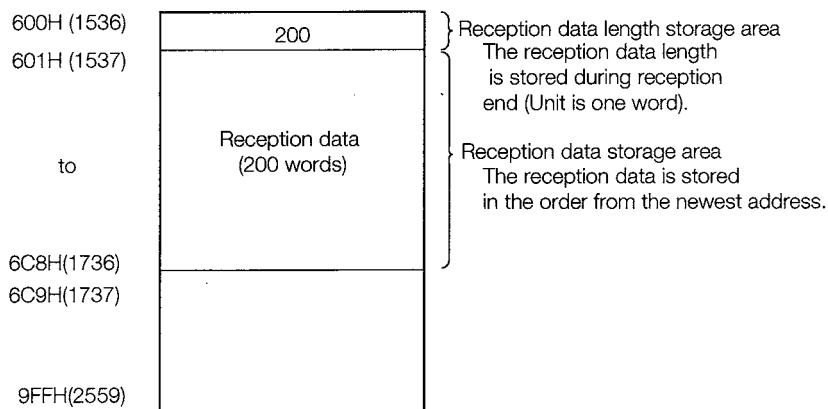
- ② When the data received from the remote node by parameter settings is stored in the fixed buffer (No.2), the QE71 turns the reception end signal (X2) ON.

The reception data length and reception data are stored in the fixed buffer.

The reception data length is stored in the subject fixed buffer's first address (1536).

The reception data is stored in the subject fixed buffer's first address + 1 order.

The following diagram shows an example of a 200-word reception using fixed buffer No. 2.



- ③ The reception data length and reception data stored in the fixed buffer are read by the sequence program's FROM command when the reception end signal is turned ON.
- At the same time the reception end check signal (Y1) is turned ON by the sequence program.
- ④ The E71 returns a response to the remote node (by the parameter settings) when the reception end check signal is turned ON.
- ⑤ When the response returned is ended, the E71 automatically turns OFF the reception end signal.
- ⑥ The reception end check signal is turned OFF by the sequence program when the reception end signal is turned OFF.

Point

- (1) The reception end signal (X2) does not turn ON during error data reception.
In addition, the data is not stored in fixed buffer No. 2.
- (2) This shows the processing when the open request signal and initial request signal are turned off during reception when the E71 "Function for Data Exchange during the PLC CPU is Stopped" (Item 5.6) is not used.
 - ① When the open request signal (Y9) turns OFF during reception, the E71 immediately performs close processing.
 - ② When the initial request signal (Y19) turns OFF during reception, the E71 immediately conducts close processing and end processing.

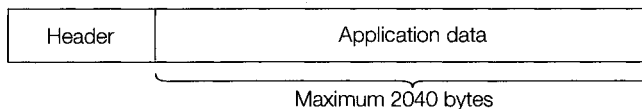
Remarks

For more details on cases when the reception of Close/Abort (RST) has occurred from the partner remote node prior to the completion of the data communication performed immediately before, refer to Remarks in 2 of Item 5.4.3.

6.2 Data Format

This shows the data item order and contents for exchange data (commands and responses) between the E71 and a remote node when conducting fixed buffer exchange with procedures.

As shown below, the exchange data consists of a header and application data.



As shown below, the data code of the application data can be expressed as either binary or ASCII code.

The exchange condition setting switches (SW2: data code setting) on the E71 is used to set either binary or ASCII. (For details regarding the setting method refer to Item 4.3.2.)

6.2.1 Format When Exchanging with Binary Code

The command response data item order for when conducting fixed buffer exchange with procedures when exchanging the application data portion of the exchange data as binary code data are shown below.

1 Transmission/reception data order when exchanging using TCP/IP

① Order when transmitting/receiving commands

Header			Application data		
Ethernet	IP	TCP	Subheader	Data length setting (L) (H)	Text (command)
(14 bytes)	(20 bytes)	(20 bytes)	60H 00H (2 bytes)	(2 bytes)	(Maximum 1017 words)

② Order when transmitting/receiving responses

Header			Application data	
Ethernet	IP	TCP	Subheader	End code
(14 bytes)	(20 bytes)	(20 bytes)	E0H (1 byte)	(1 byte)

2 Transmission/reception data order when exchanging using UDP/IP

① Order when transmitting/receiving commands

Header			Application data		
Ethernet	IP	UDP	Subheader	Data length setting (L) (H)	Text (command)
(14 bytes)	(20 bytes)	(8 bytes)	60H 00H (2 bytes)	(2 bytes)	(Maximum 1017 words)

② Order when transmitting/receiving responses

Header			Application data	
Ethernet	IP	UDP	Subheader	End code
(14 bytes)	(20 bytes)	(8 bytes)	E0H (1 byte)	(1 byte)

6.2.2 Format When Exchanging with ASCII Code

The command and response data item order when conducting fixed buffer exchange with procedure when exchanging the application data portion of exchange data using ASCII code data is shown below.

1

Transmission/reception data order when exchanging using TCP/IP

① Order when transmitting/receiving commands

Header			Application data						
Ethernet	IP	TCP	Subheader				Data length setting		Text (command)
"6"	"0"	"0"	"0"				(H)	(L)	
36H	30H	30H	30H						
(14 bytes)	(20 bytes)	(20 bytes)	(4 bytes)				(4 bytes)		(Maximum 1016 words)

② Order when transmitting/receiving responses

Header			Application data			
Ethernet	IP	TCP	Subheader		End code	
"E"	"0"		(H)	(L)		
45H	30H					
(14 bytes)	(20 bytes)	(20 bytes)	(2 bytes)		(2 bytes)	

2

Transmission/reception data order when exchanging using UDP/IP

① Order when transmitting/receiving commands

Header			Application data						
Ethernet	IP	UDP	Subheader				Data length setting		Text (command)
			"6"	"0"	"0"	"0"	(H)	(L)	
			36H	30H	30H	30H			
(14 bytes)	(20 bytes)	(8 bytes)	(4 bytes)				(4 bytes)		(Maximum 1016 words)

② Order when transmitting/receiving responses

Header			Application data			
Ethernet	IP	UDP	Subheader		End code	
"E"	"0"		(H)	(L)		
45H	30H					
(14 bytes)	(20 bytes)	(8 bytes)	(2 bytes)		(2 bytes)	

6.2.3 Exchange Data Item Contents

The command and response data item contents when fixed buffer exchange with procedure is conducted is shown below.

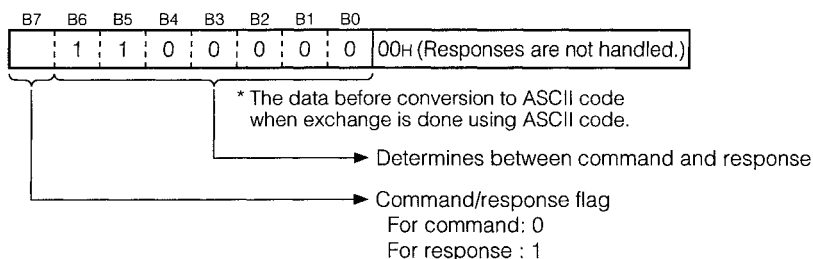
1 Header

The header is the header that is used for TCP/IP and UDP/IP. For the E71, since the E71 can be added or removed the user does not need to make the setting.

2 Subheader

The subheader format is as shown below.

For E71, since the E71 can be added or removed the user does not need to make the setting.



The subheader data code order when conducting fixed buffer exchange is as follows.

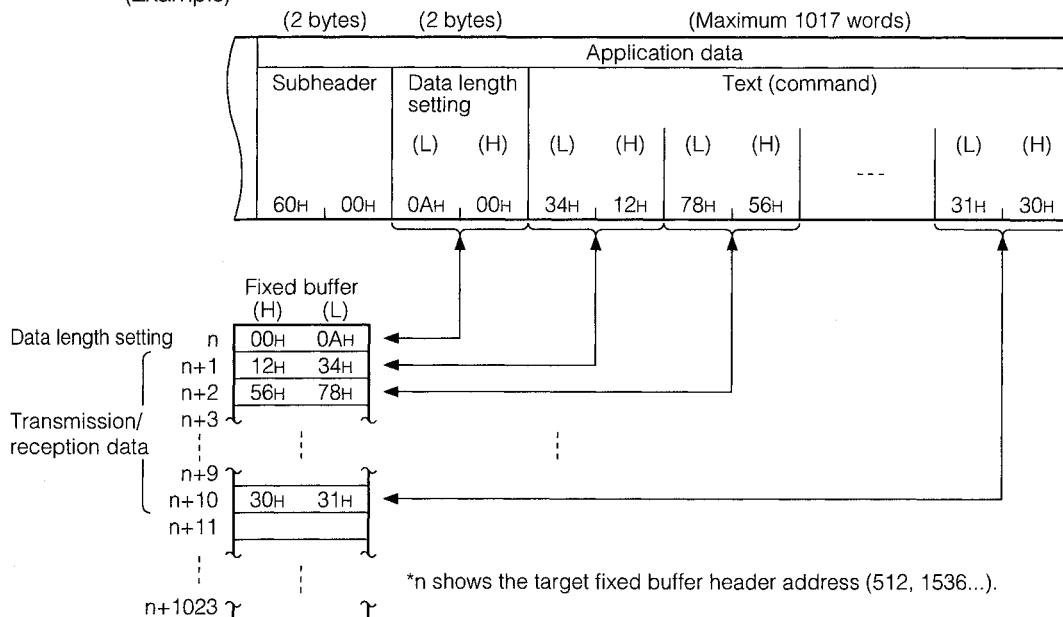
	Subheader code during exchange	
	For command exchange	For response exchange
When exchanging binary code	60H 00H	E0H
When exchanging using ASCII code	36H 30H 30H 30H	45H 30H

3 Data length setting and text (command)

The data length setting shows the text data capacity in number of words.

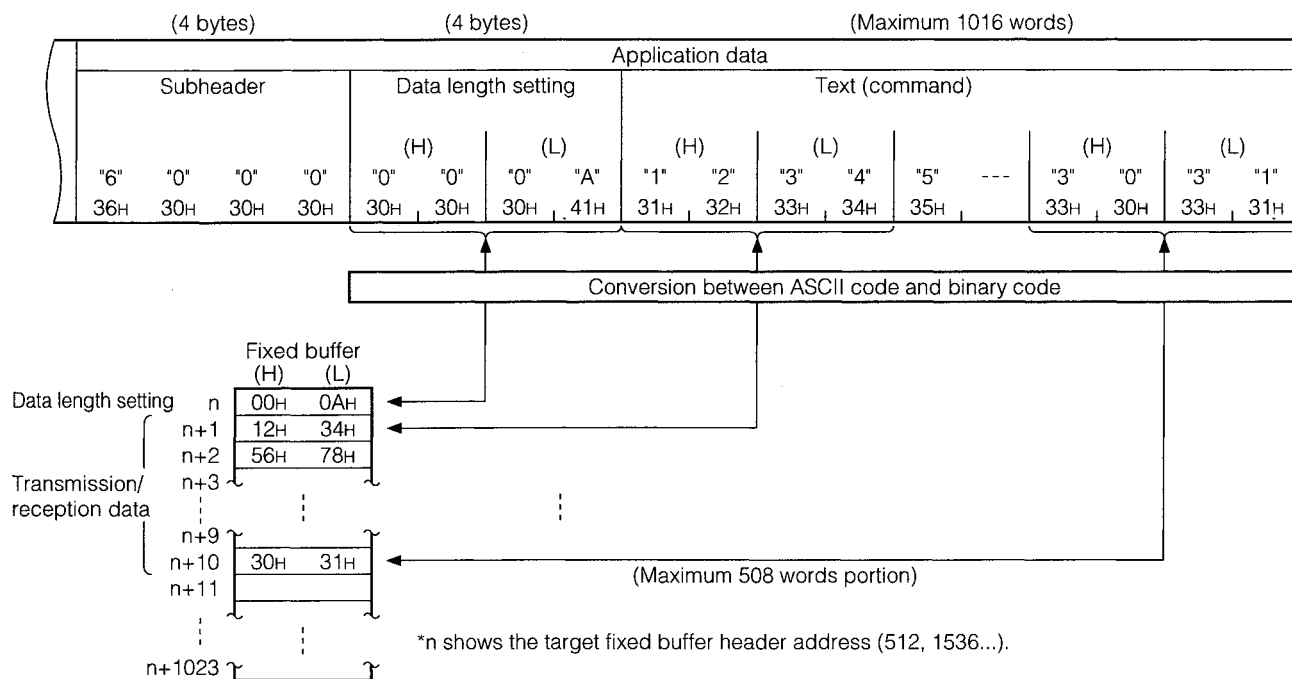
The text (command) shows the data for the data length setting portion that is sent to the exchange partner node.

- ① Data length setting and text (command) portion format when exchanging with binary code (Example)



- ② Data length setting and text (command) portion format when exchanging with ASCII code

(Example)



Point

- (1) The maximum exchange data capacity that can be handled by the PLC CPU when binary code is specified is 1017 words. The data length setting range is 1 to 1017. The unit is a word.
- (2) The maximum exchange data capacity that can be handled by the PLC CPU when ASCII code is specified is 508 words. This is an exchange data amount that is approximately one-half that of when binary code is specified. The data length is communicated using ASCII code ("0001" to "01FC") when the number of words is expressed in hexadecimal notation. The setting range is 1 to 508. The unit is a word.

4

End code

This shows the end code that is added to the response when conducting fixed buffer exchange. The end code is stored in the buffer memory's exchange status storage area.

When binary code is specified		When ASCII code is specified	
00H	Normal end	30H30H	Normal end
50H	Command/response classification undefined error	35H30H	Command/response classification undefined error
52H	Number of data words error	35H32H	Number of data word error
—	—	35H34H	ASCII conversion error

For details regarding error codes refer to Chapter 13.

6.3 Programming

This section explains programming method for using the fixed buffer to conduct exchange between the E71 and a remote node with procedures.

6.3.1 Programming Creation Precautions

- (1) Fixed buffer exchange can only be conducted when the open request signal (Y8 to YF) and the open end of signal (X10 to X17) is turned ON. Initial processing and communication line open processing must be completed. (Refer to Chapter 5)
- (2) The parameter settings are entered into the E71 when the open request signal (Y8 to YF) turns from OFF to ON during boot up. Except for those cases shown in the following diagram (3), the control contents cannot be changed even if the parameter contents are written over while the open end signal (X10 to X17) is ON.
- (3) When using a connection opened by UDP, the exchange parameters setting area's exchange address setting area setting values can be changed before data is transmitted or received, and the exchange partner remote node can be switched. Therefore, data can be transmitted in order to multiple remote nodes, so to prevent exchange trouble from occurring, switch the partner remote node and conduct transmission and reception.
- (4) The data link that is specified (stored) in the buffer memory when exchange with procedures is conducted, uses units of one word. If the buffer memory transmission data link exceeds the range during transmission, there will be an exchange error and the transmission will not be conducted.
- (5) When data is received from a fixed buffer, be sure the reception end check signal (Y0 to Y7) is ON during reception end (the point at which the reception end signal turns ON).

A response is returned to the remote node when the reception end check signal turns ON, and the following reception data is stored in the subject fixed buffer. If the reception end check signal does not come ON, a response is not returned to the remote node, so an exchange error occurs at the remote node end.

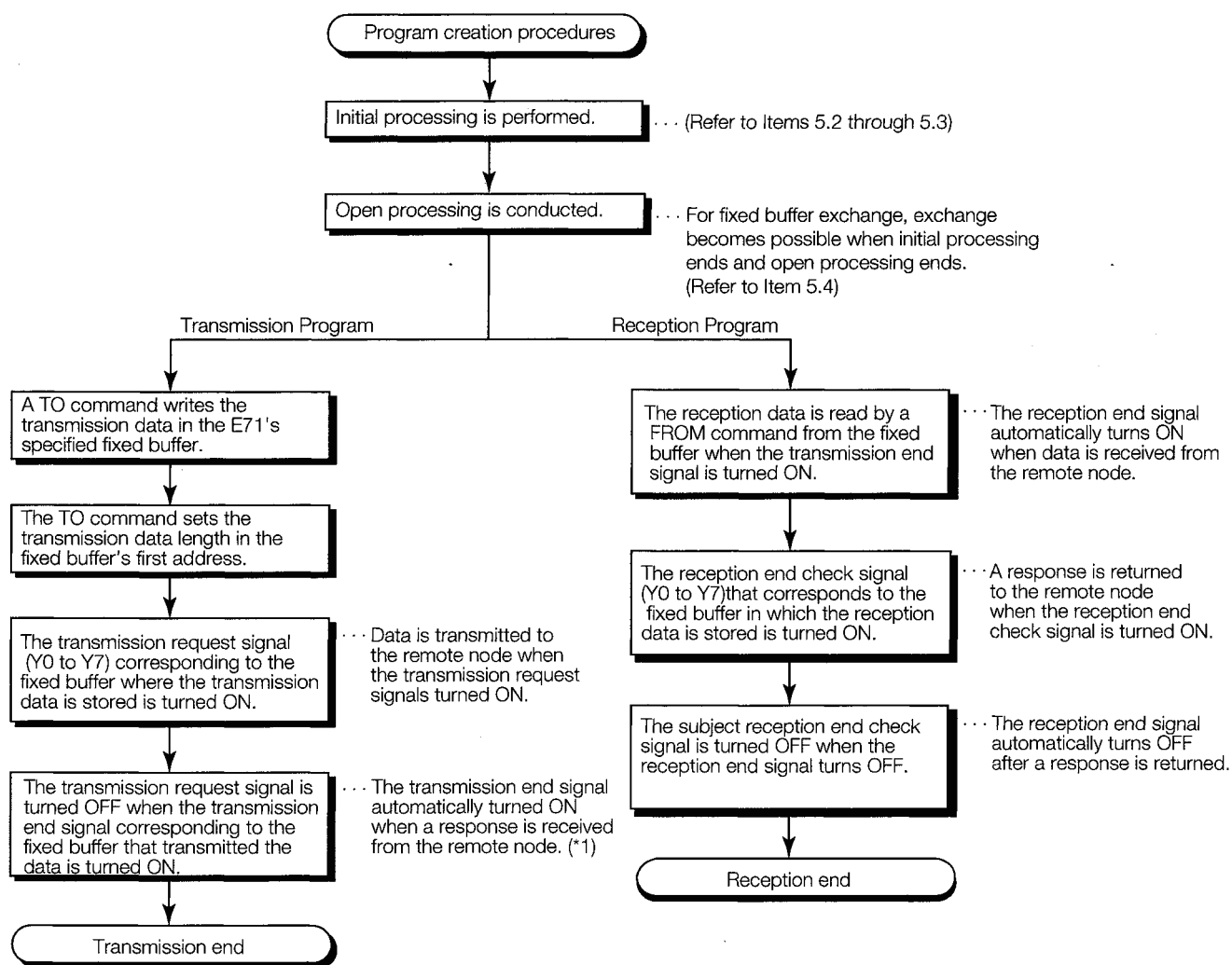
- (6) When receiving data from remote nodes the data length in the command being transmitted from the remote node must not exceed the range shown in Item 6.2.3 3 Point.

When the E71 receives a command with a data length that exceeds the settable range, it might conduct closed processing for the corresponding connection without transmitting a response. Check using the I/O signal's open end signal (X10 to X17) or the open error code area (address 5DH, 67H, ..., error code 71H) in the buffer memory's exchange status storage area.

- (7) For data (command) transmission, the next data (command) should be sent after the completion of data communication (such as after the reception of a response) for the transmission of the previous data (command).

6.3.2 Program Creation Procedure

This section explains the fixed buffer data transmission and reception program creation procedures.



*1 When the transmission error detection signal turns ON, the information for individual connections in the transmission state storage area (transmission error code, end code) is handled as described in Chapter 13.

6.3.3 Example Fixed Buffer Exchange Program (With Procedure)

This section explains the programming method for conducting data exchange with a remote node using a fixed buffer.

(Program Conditions)

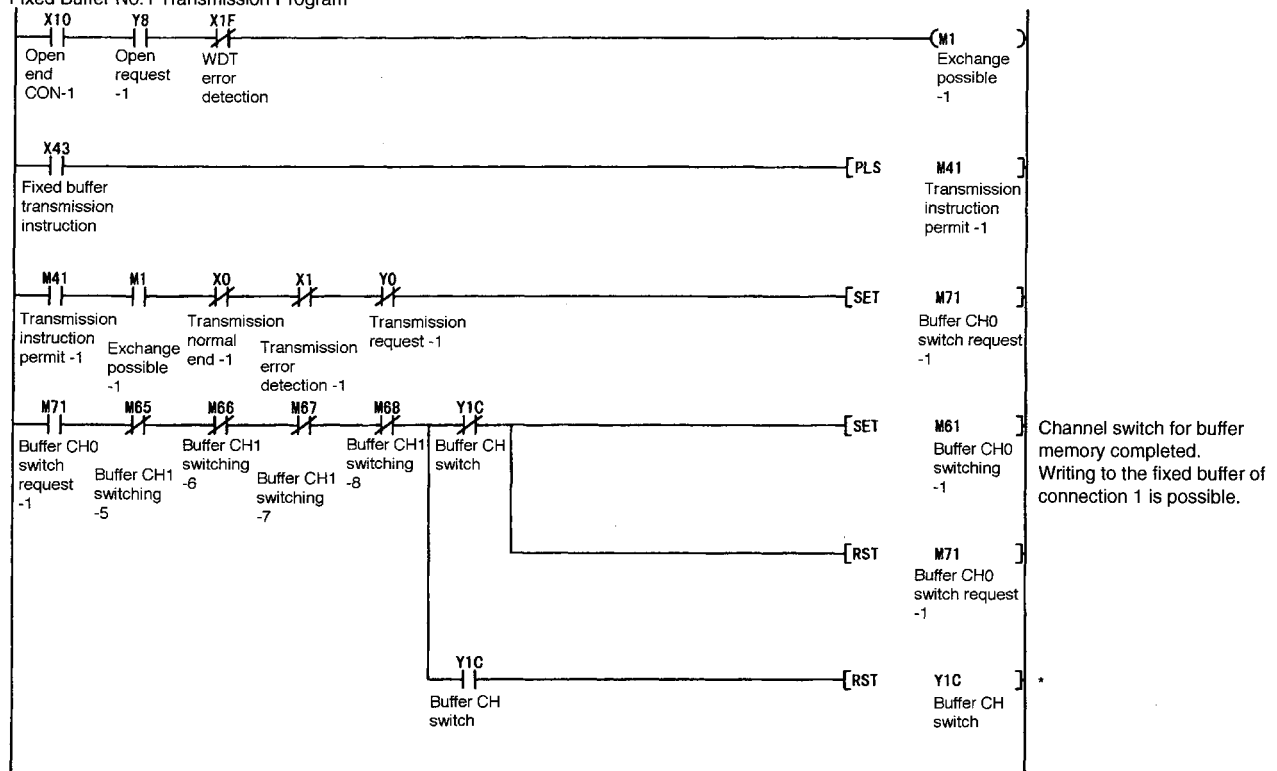
- (1) All connection exchange parameters are set to those parameters specified in Item 5.4.5.
- (2) The fixed buffer transmission data is set to D300 to D303.
- (3) The fixed buffer reception data is stored in D500 to D503.
- (4) The error code and end code storage destination is allotted as follows.

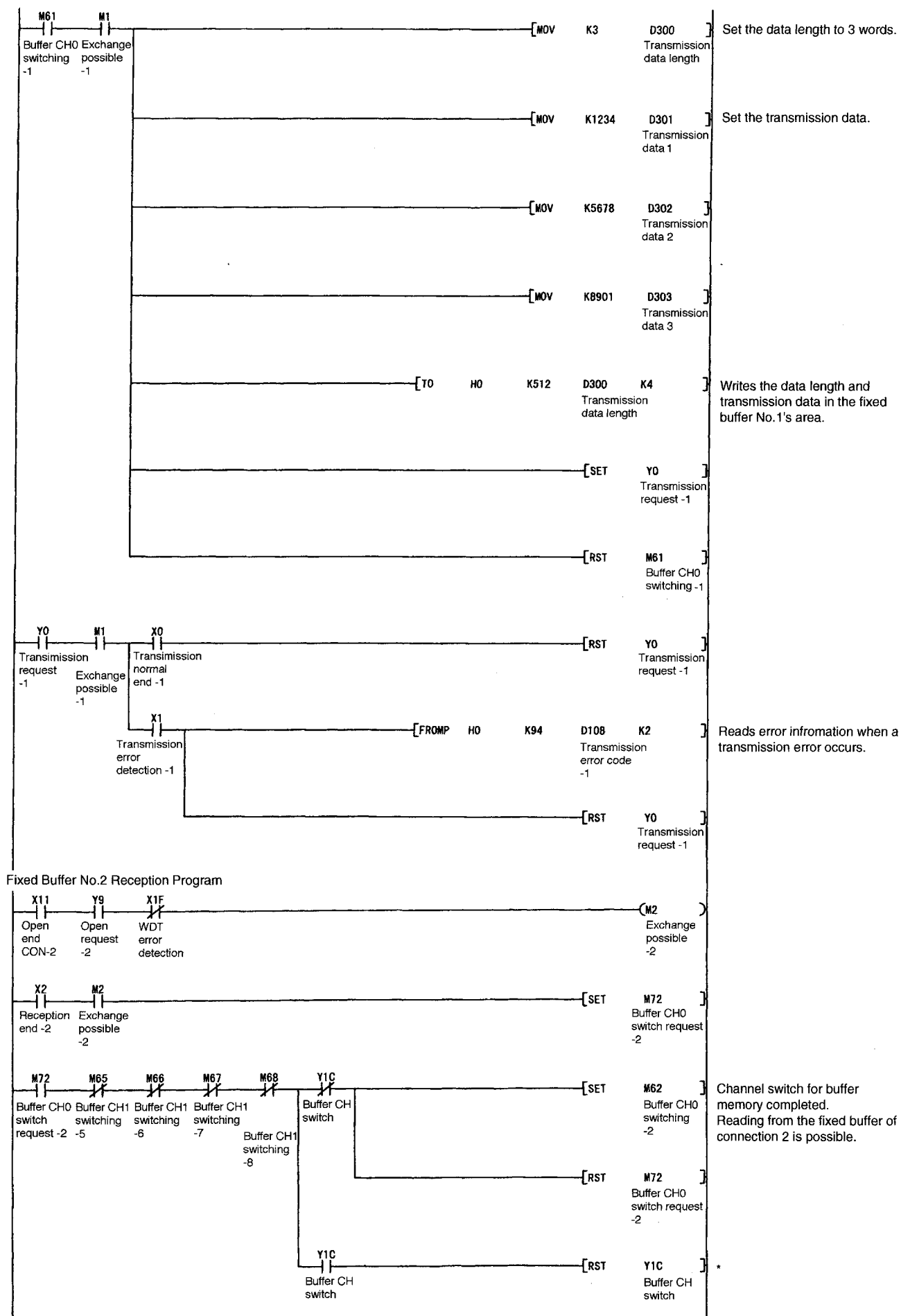
D108 Transmission error code

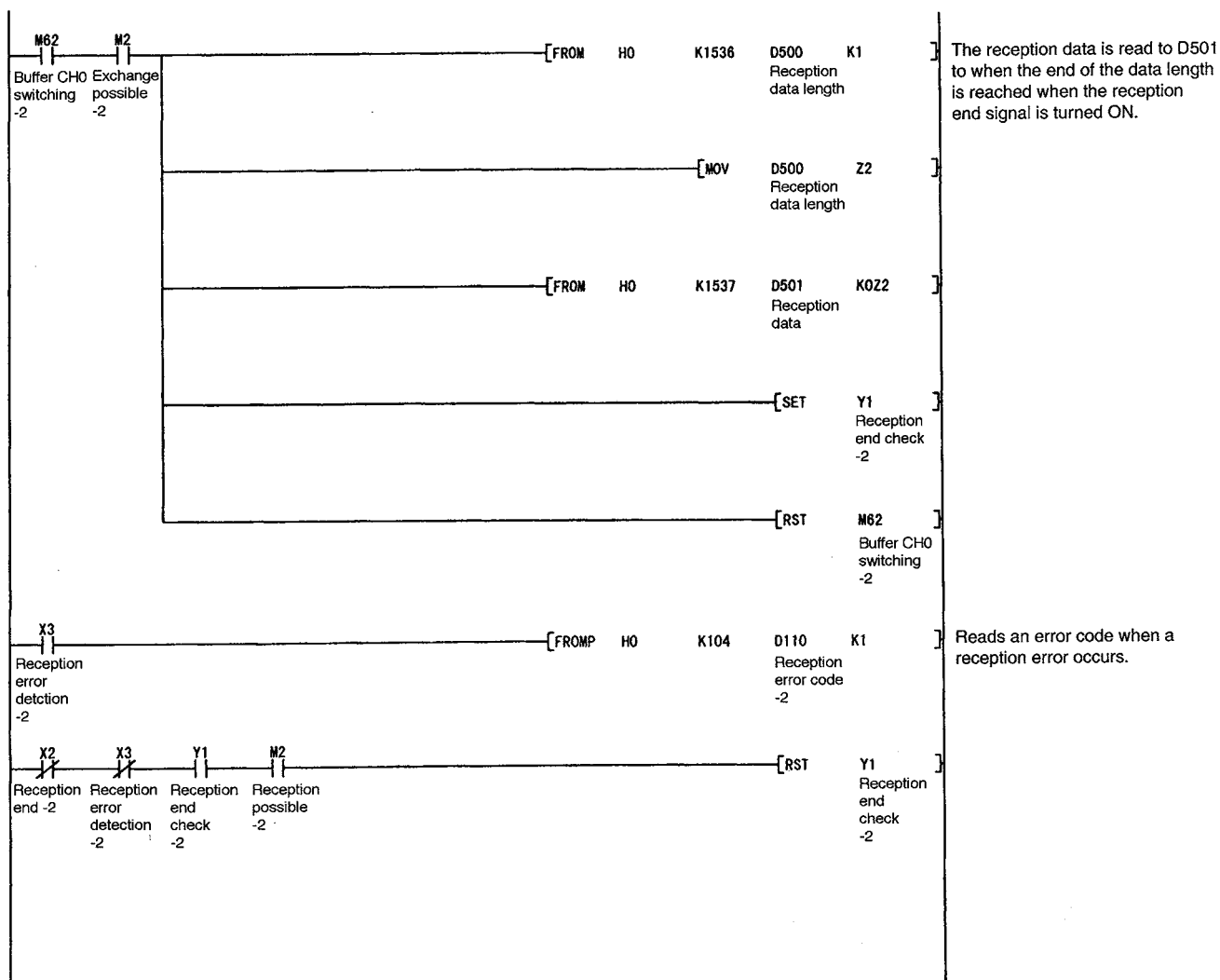
D109 Reception end code

D110 Reception error code

Fixed Buffer No.1 Transmission Program







* When the I/O control method of the PLC CPU of the station installed in the E71 is the refresh method and a fixed buffer read/write is performed after the buffer memory channel switching signal (Y001C) is switched from on → off/off → on, a read/write must be performed after the channel switching signal (Y001C) is output to the E71. Output the channel switching signal (Y001C) to the E71 using the tail's *1 of Item 3.6.2.

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7. FIXED BUFFER EXCHANGE WITHOUT PROCEDURE

This section explains the method for exchanging with a remote node without procedure using the E71's fixed buffer.

Point

Following is an overview of the difference from fixed buffer exchange using with procedure.

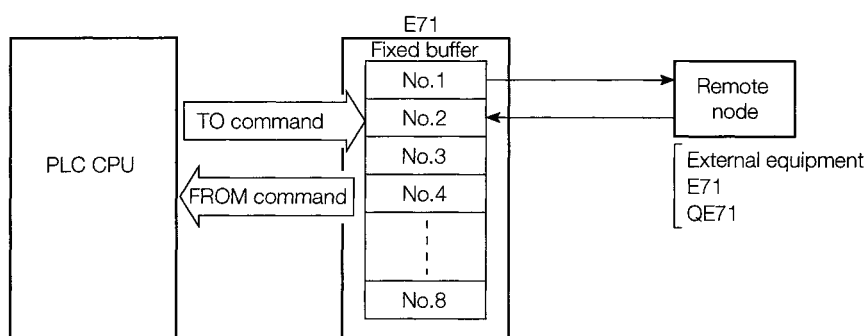
- ① During data transmission, the data is transmitted to the fixed buffer without the message application adding a subheader or a data length. During data reception, the header is removed from the received message and all the data is stored in the fixed buffer.
- ② A response to the data reception is not transmitted.
- ③ Conducts exchange in binary code regardless of the exchange condition setting switch (SW2: data code setting) setting of the E71.
- ④ The application data portion that can be handled by one exchange is 1 to 2046 bytes.
- ⑤ The subject connection becomes for fixed buffer exchange without procedure special use. Fixed buffer exchange with procedure, random access buffer exchange, and reading and writing data to the PLC CPU exchange are not conducted at the same time as fixed buffer exchange without procedure.

7.1 Control Format

This section explains the control format used to conduct fixed buffer exchange without procedure.

Fixed buffer remote node exchange processing can be conducted without procedure for data transmission from the PLC CPU and data reception from a remote node.

- (1) The exchange processing data flow is shown below.
- (2) Data exchange can be conducted for remote nodes connected in the Ethernet by the E71, and for remote nodes connected by router relay functions (Refer to Chapter 12).

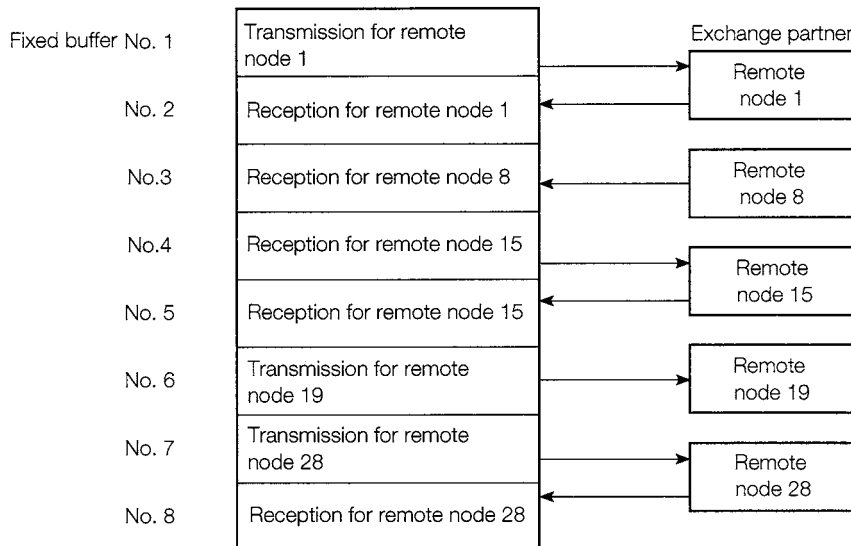


As shown in the diagram on the following page, the fixed buffers (No.1 to No.8) are used to set the remote terminals to which exchange will be conducted and the usage availability (for transmission/reception, without procedure/with procedure, etc.) to be opened in the E71's communication line (Refer to Item 5.5), and to set the exchange partners for the buffers.

- ① The parameter settings fixed buffer exchange partner setting when TCP/IP is used, is valid when the E71's open end signal turns from OFF to ON during boot up. The exchange partner cannot be changed while the open end signal is ON.

- ② The fixed file exchange partner can be changed after open processing when UDP/IP is used. (The exchange parameter's remote node IP address and remote node port No. can be changed, but the local station E71's port No. cannot be changed.)

(Example)



Point

- ① Select without procedure and during open the subject connection will be changed to special use for fixed buffer transmission/reception without procedure. (Refer to Item 5.1 (*1))
Fixed buffer exchange with procedure, random access buffer exchange, and reading and writing data to the PLC CPU exchange cannot be conducted at the same time as fixed buffer exchange without procedure.
- ② When changing the exchange partner in the UDP/IP communication, do not perform pairing setting (Refer to Item 5.4.1 1 (b)(③), or existence check setting (Refer to *2 at the end of Item 5.3.1).
If these are set, the E71 will not operate normally.

- (3) The transmission and reception processing during data transmission and reception is as follows.

① During transmission

When the transmission request signal (Y0 to Y7) is ON, the E71 transmits the subject fixed buffer's data to the remote node set in the buffer memory address 18H to 4FH (24 to 79) subject area. (*1)

② During reception

If there is reception from a remote node set in the buffer memory address 18H to 4FH (24 to 79) subject area, the E71 will perform reception processing. (*1) In addition, when reception data is stored in the subject fixed buffer by reception processing, the E71 updates the buffer memory address 59H to A8H (89 to 168) subject connection's remote node IP address and remote node port No.

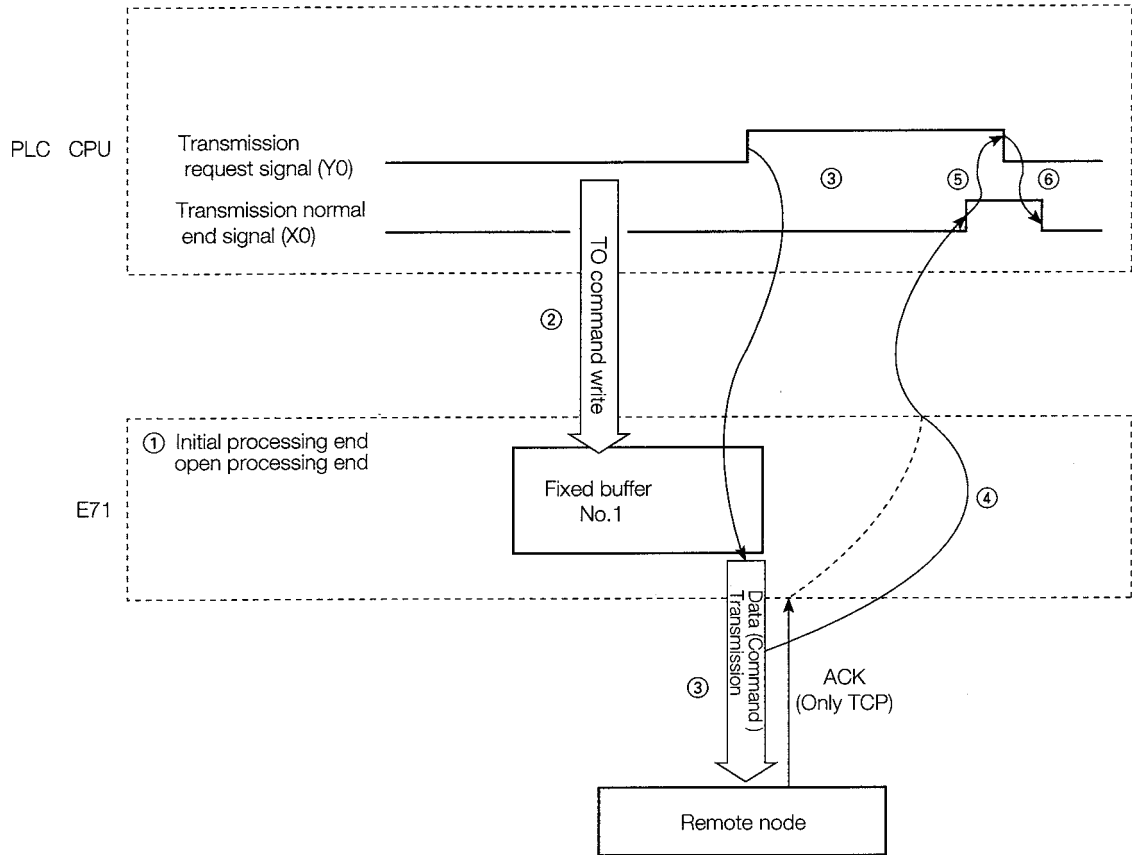
If there is a reception from remote nodes that are not set in the buffer memory address 18H to 4FH (24 to 79), the E71 will ignore the reception data.

For details refer to Item 7.3.2 remarks.

*1 When the TCP/IP unpassive open, data is transmitted to and received from the remote node stored in the buffer memory address 59H to A8H (89 to 168) subject area.

7.1.1 Transmission Control Method

This section explains the control method for transmitting data to the remote node from the E71 using transmission of the fixed buffer No.1 data to a remote node.

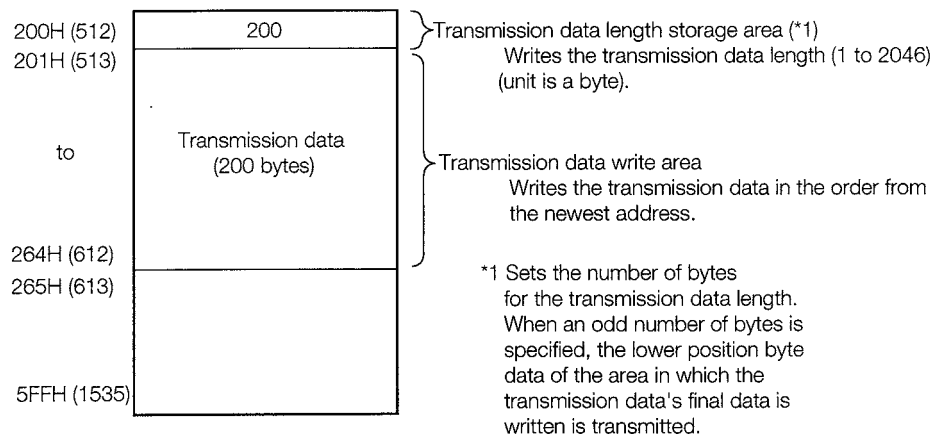


- ① Performs E71 initial processing. (Refer to Items 5.2 through 5.3)
Performs line open processing with the remote node. (Refer to Item 5.4)
- ② The sequence program's TO command writes the transmission data length and transmission data in the E71's fixed buffer.

The transmission data link is written to the subject fixed buffer's first address (512).

The transmission data is written starting from the subject fixed buffer's first address +1.

The following figure shows an example of a 200-byte transmission using the fixed buffer No.1.



- ③ When the transmission request signal (Y0) is turned ON by the sequence program, the data is transmitted as it is to the specified node (by the parameter settings) from the fixed buffer (No.1).
- ④ The E71 turns the transmission normal end signal (X0) ON when the data transmission ends.
- ⑤ The turning ON of the transmission normal end signal causes the sequence program to turn OFF the transmission request signal (Y0).
- ⑥ When the transmission request signal turns OFF, the transmission normal end signal automatically turns OFF.

Point

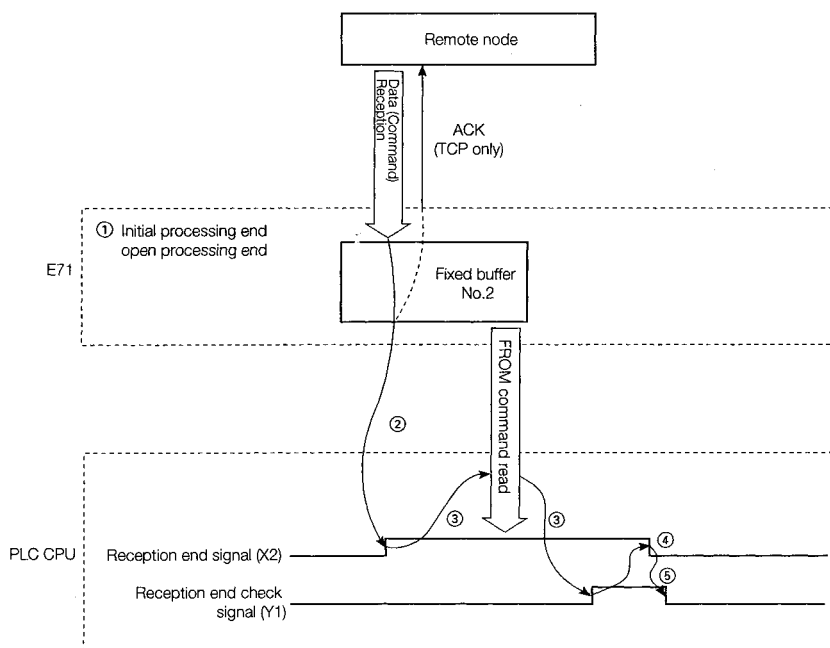
- (1) When communicating by UDP/IP, need attention for the items described below.
 - ① When the PLC CPU and the remote node are not connected by a communication line due to disconnection of the connection cable, the transmission normal end signal (X0...) and transmission error detection signal (X1...) are not turned ON if data was transmitted to the remote node by the PLC CPU.
 - ② When data is transmitted to the remote node from the PLC CPU, conduct the time out check until transmission is ended using the PLC CPU. (The user can adjust the time out time) When a time out occurs, turn OFF the transmission request signal (Y0...), check the communication line with the partner remote node, and conduct connection processing for the troubled location.
- (2) Except (1) in above, when the transmission does not end normally, the transmission error detection signal (X1) turns ON. (The exchange normal end signal (X0) does not turn ON.) In this case, reconduct transmission processing by turning the transmission request signal from off to on after the transmission error processing is completed.
- (3) This shows the processing when the open request signal and initial request signal are turned off during transmission when the E71 "Function for Data exchange during the PLC CPU is Stopped"(Item 5.6) is not used.
 - ① When the open request signal (Y8) turns off during transmission, the E71 conducts closed processing after the transmission processing end.
 - ② When the initial request signal (Y19) turns off during transmission, the E71 conducts closed processing and end processing after transmission processing end.

Remarks

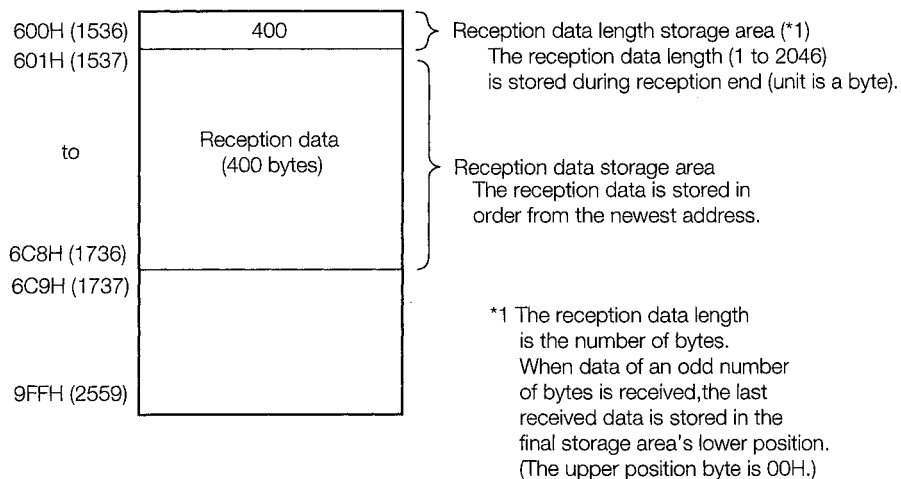
For more details on cases when the reception of Close/Abort (RST) has occurred from the partner remote node prior to the completion of the data communication performed immediately before, refer to Remarks in 2 of Item 5.4.3.

7.1.2 Reception Control Method

This section explains the control method that the E71 receives the data from the remote node receiving to the fixed buffer No.2 as an example.



- ① Conducts E71 initial processing. (Refer to Items 5.2 through 5.3)
Conducts line open processing to the remote node. (Refer to Item 5.4)
To conduct fixed buffer exchange, initial processing and open processing must be completed.
- ② The E71 will turn on the receive end signal (X2) when the reception data is stored as is from the remote node by parameter setting into the fixed buffer (No.2).
The reception data length and reception data are stored in the fixed buffer.
The reception data length is stored in the subject fixed buffer first address (1536).
The reception data is stored in order starting from the fixed buffers No.1 to 8 first address +1.
The following figure shows an example of a 400-byte reception using fixed buffer No.2.



- ③ The received data length and received data stored in the fixed buffer is read by the sequence program's FROM command when the reception end signal turns on. At the same time, the sequence program turns on the reception end check signal (Y1).
- ④ The E71 automatically turns off the reception end signal.
- ⑤ The sequence program turns off the reception end check signal when the reception end signal turns off.

Point

- (1) The reception end signal (X2) does not turn on during error data reception. In addition, the data is not stored in the fixed buffer No.2.
- (2) This shows the processing when the open request signal and initial request signal are turned off during reception when the E71 "Function for Data Exchange during the PLC CPU is Stopped" (Item 5.6) is not used.
 - ① If the open request signal (Y9) turns off during reception, the E71 immediately conducts close processing.
 - ② If the initial request signal (Y19) turns off during reception, the E71 immediately conducts close processing and end processing.

Remarks

For more details on cases when the reception of Close/Abort (RST) has occurred from the partner remote node prior to the completion of the data communication performed immediately before, refer to Remarks in 2 of Item 5.4.3.

7.2 Data Format

Following shows the exchange data (command) data item order and contents for when fixed buffer exchange is performed without procedure between the E71 and a remote node.

As shown below, the exchange data consists of a header and application data.

7.2.1 Format during Exchange

Following shows the command data item order when fixed buffer communication without procedure is conducted.

1 Transmission/reception data order when exchanging with TCP/IP

Header			Application data
Ethernet	IP	TCP	Text (command)
(14 bytes)	(20 bytes)	(20 bytes)	(Maximum 2046 bytes)

2 Transmission/reception order when exchanging with UDP/IP

Header			Application data
Ethernet	IP	UDP	Text (command)
(14 bytes)	(20 bytes)	(8 bytes)	(Maximum 2046 bytes)

7.2.2 Exchange Data Item Contents

Following shows the command data item contents when conducting fixed buffer exchange without procedures.

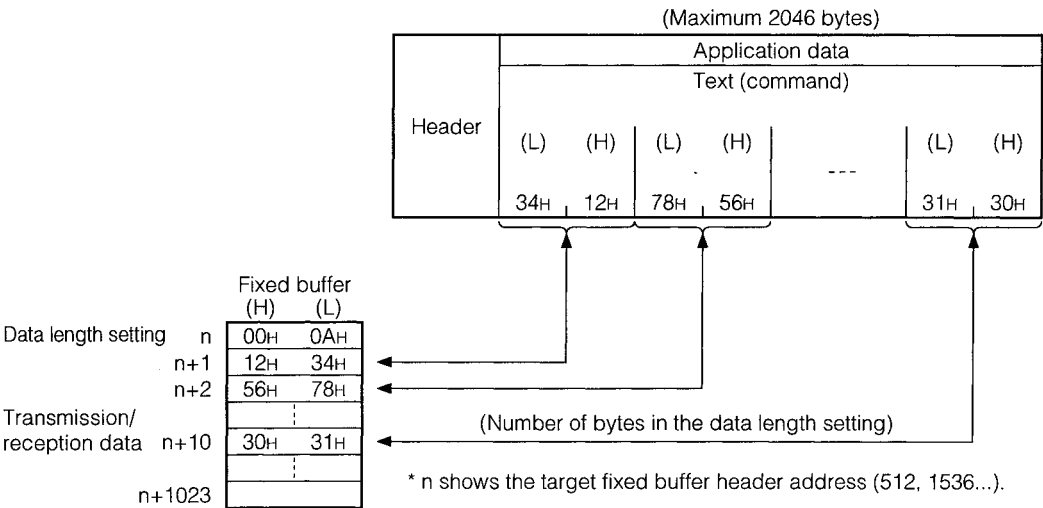
1 Header

The header is the header used for TCP/IP or UDP/IP. For the E71, since the E71 is added or removed the user is not required to make the setting.

2 Text (command)

The text (command) shows the data that is sent to the exchange partner node. The data code is expressed in binary code. The binary and ASCII setting made using the exchange condition setting switch (SW2: data code setting) of the E71 is ignored.

(Example) Text (command) portion format during exchange



Remarks

This subheader and data length added to the fixed buffer exchange with procedures does not exist when without procedure. All the data is handled as valid text.

7.3 Simultaneous Broadcast Communication When Using UDP/IP

When UDP/IP is used to conduct fixed buffer exchange without procedure, a simultaneous broadcast to all E71 installation stations connected to the same Ethernet as the E71.

This makes it possible to write, etc., the same data to all stations.

Point

- (1) For simultaneous broadcast communication, the necessity of the reception message must be determined, and if unnecessary, read and discard processing must be conducted for remote nodes connected to the same Ethernet.
- (2) For simultaneous broadcast communication, the user arranges the simultaneous broadcast transmission, reception special port No., and specifies the port No.

7.3.1 Simultaneous Broadcast Communication Transmission

When conducting open processing for the partner remote node IP address to which data will be sent as FFFFFFFFH, simultaneous broadcast communication transmission can be conducted. During simultaneous broadcast communication transmission, the E71 changes the request destination IP address to FFFFFFFFH, and transmits the data on the Ethernet.

(Example) When connection 1 is used

Address	Buffer memory	
10H(16)	0300H	Set without procedure, UDP, and for transmission.
to	to	
18H(24)	E71 Port No.	Makes the remote node IP address to a simultaneous broadcast address.
19H(25)	FFFFH	
1AH(26)	FFFFH	Makes the remote node port No. the simultaneous broadcast port No. (arranged by the user)
1BH(27)	Remote node port No.	

During the situation shown in the figure above, the E71 makes the request destination IP addresses equal FF. FF. FF. FFH, makes the request destination port No. equal the remote node port No., and transmits the fixed buffer's data.

7.3.2 Simultaneous Broadcast Communication Reception

Making the partner remote nodes IP address to which the data will be received FFFFFFFH and the port No. to FFFFH / User arrangement No. and conducting open processing will receive process all of the corresponding reception data as simultaneous broadcast communication data.

(Example) When connection 1 is used

Address	Buffer memory	
10H(16)	0301H	Set without procedure, UDP, and for Reception
to	to	
18H(24)	E71 Port No.	
19H(25)	FFFFH	
1AH(26)	FFFFH	Makes the remote node IP address to a simultaneous broadcast address.
1BH(27)	FFFFH	Makes the remote node port No. the simultaneous broadcast port No. (FFFFH/ User arrangement No.)

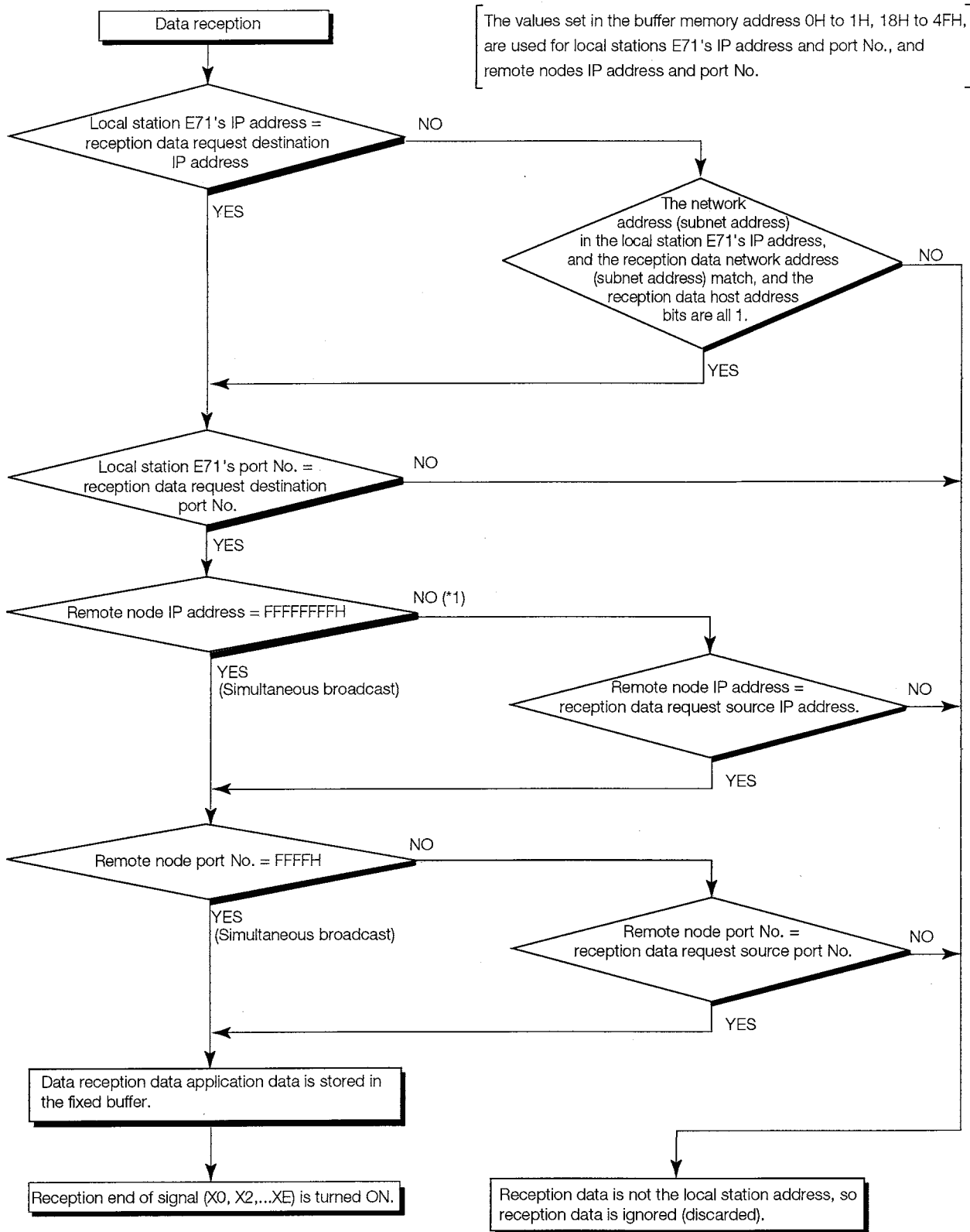
During the situation shown in the above figure, the E71 changes all of the bits in the reception data request destination IP addresses local station class host address range to 1, and if the request destination port No. equals the E71's port No., the reception data is stored in the fixed buffer and the reception end signal (X0) is turned on.

In addition, when the reception data is stored in the subject fixed buffer, the E71 updates the buffer memory address 59H to A8H (89-168) of the subject area's remote node IP address and the remote node's port No.

When checking the data transmission origin, read the above buffer memory (exchange state storage areas information area by connection).

Remarks

Following shows an overview of the E71's internal processing when there is reception using without procedure and reception using simultaneous broadcast communication.



7.3.3 Precautions When Using Simultaneous Broadcast Communications Functions

Following are precautions when conducting simultaneous broadcast communication with fixed buffer exchange without procedures.

1 For simultaneous broadcast communication, the user will arrange the simultaneous broadcast transmission/reception special port No. and specify the port No. for it.

2 The simultaneous broadcast communication transmission message is set to all the nodes on the Ethernet to which the E71 is connected.

All of the nodes connected to the same Ethernet determine whether the received message broadcast by simultaneous broadcast communication is necessary, and when unnecessary, must conduct read and discard processing.

* When the transmitted message is unrelated to a particular node, the particular node will discard the received message. In addition, even if a particular node is the subject station, do not return a response. The E71 will automatically perform this processing.

3 The application data amount that can be handled at one time for transmission or reception is a maximum of 2046 bytes. If data of 2047 bytes or more must be transmitted or received, divide it at the transmission origin.

4 When the reception data is read to the PLC CPU when the reception end signal (X0, X2...XE) is turned on, be sure to turn on the corresponding reception end check signal (Y0 to Y7).

Turning on the reception end check signal (Y0 to Y7) makes it possible for the E71 to store the received data in the subject fixed buffer.

Not turning on the reception end check signal (Y0 to Y7) could cause the reception data to be discarded.

5 When performing simultaneous broadcast, set up "Destination does not check existence" during the open processing for the corresponding connection.

Remark

Until the previous reception processing is completed, the E71 temporarily stores the data received after that in its internal buffer for the OS.

Data received in excess of the internal buffer capacity (approximately 40k bytes) by simultaneous broadcast are discarded.

For communication using the fixed buffer (with procedure), etc., it is not necessary to consider the above-mentioned internal buffer since the E71 waits for a response message after transmitting a command message to the remote node, and upon reception of the response message, it transmits the next command message.

7.4 Programming

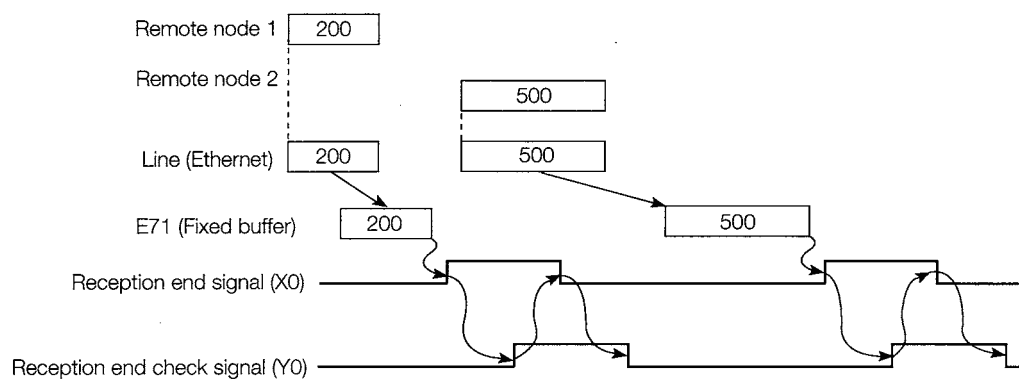
This section explains the programming method for conducting exchange between the E71 and the remote node using the fixed buffer and without procedure.

7.4.1 Precautions When Creating Programs

- 1** Fixed buffer exchange can be conducted when the open request signal(Y8 to YF) and the open end signal (X10 to X17) are turned on. The initial processing and communication line open processing must be completed. (Refer to Chapter 5)
- 2** The parameter setting contents are taken into the E71 when the open request signal (Y8 to YF) is turned from off to on at the boot up.
 Except for that in **3** below, the control contents cannot be changed even if the parameter contents are written over while the open end signal (X10 to X17) is on.
- 3** When using a connection that is UDP open,
 - (a) The settings values of the exchange parameter setting area's exchange address setting area can be changed before transmission or reception, and the exchange partner remote node can be changed.
 Therefore, data can be transmitted in order to multiple nodes, so conduct transmission and reception by switching the partner remote nodes to prevent exchange trouble from occurring.
 - (b) When transmission data, need attention for the items described below.
 - ① When the PLC CPU and the remote node are not connected by a communication line due to disconnection of the connection cable, the transmission normal end signal (X0...) and transmission error detection signal (X1...) are not turned ON if data was transmitted to the remote node by the PLC CPU.
 - ② When data is transmitted to the remote node from the PLC CPU, conduct the time out check until transmission is ended using the PLC CPU. (The user can adjust the time out time) When a time out occurs, turn OFF the transmission request signal (Y0...), check the communication line with the partner remote node, and conduct connection processing for the troubled location.
- 4** When opened by selecting without procedure, the subject connection becomes a fixed buffer transmission/reception special use without procedure, so fixed buffer exchange with procedure, random access buffer exchange, and reading and writing data to the PLC CPU exchange, cannot be conducted at the same time as fixed buffer exchange without procedure.
- 5** The data length specified (stored) in the buffer memory during exchange without procedure is byte units. (The data length during exchange with procedure is in word units.)
 When the buffer memory transmission data length exceeds the range (1 to 2046) during data transmission, an exchange error will occur and transmission will not be conducted.
- 6** When receiving data using the fixed buffer, be sure to turn on the reception end check signal (Y0 to Y7) during reception end (at the point the reception end signal turns on).
 Turning on the reception end check signal stores the following reception data in the subject fixed buffer.

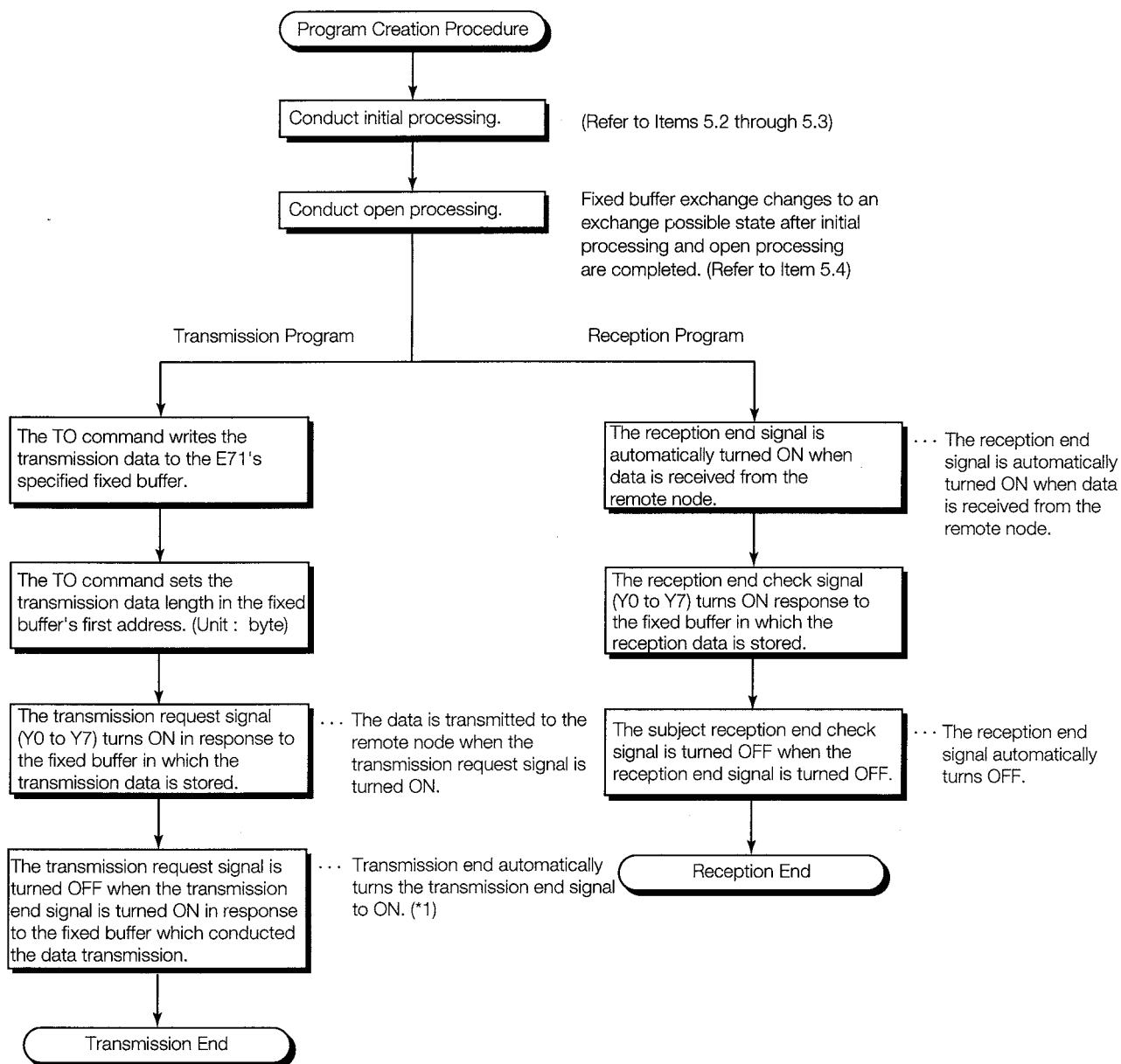
- 7** When using without procedure, the message does not have a data length.
- The E71 turns on the reception end signal (X0, X2,... XE) after the received message (packet) size is stored in the reception data length storage area.
- It is recommended that check steps, such as including data length and data type codes in the message application data, be used to allow the receiving end to identify the number of application data bytes and the data types.

(Example) For continuous reception of messages from remote nodes 1 and 2.



7.4.2 Program Creation Procedures

This section explains the data transmission and reception program creation procedures using fixed buffer without procedure.



*1 When the transmission error detection signal is on, handle the exchange state storage areas information for individual connection (transmission error code) as specified in Chapter 13.

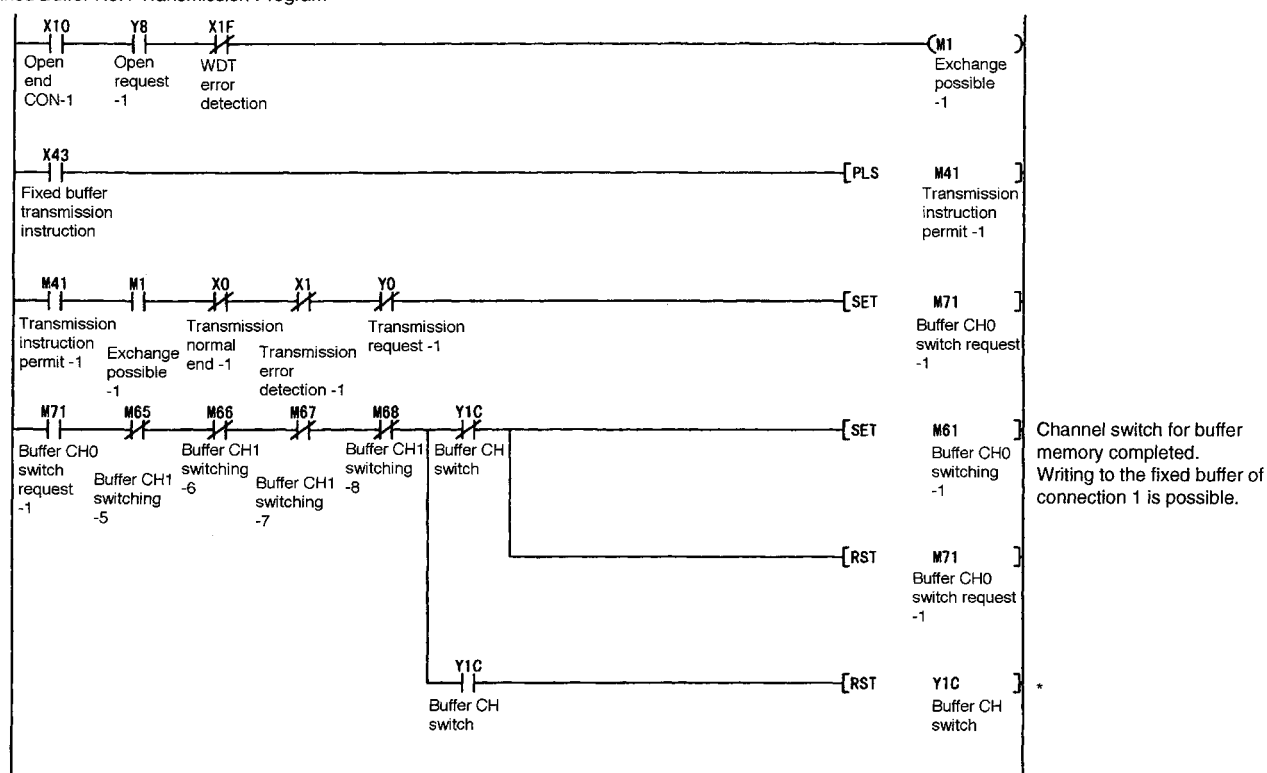
7.4.3 Example Fixed Buffer Exchange Program (Without Procedure)

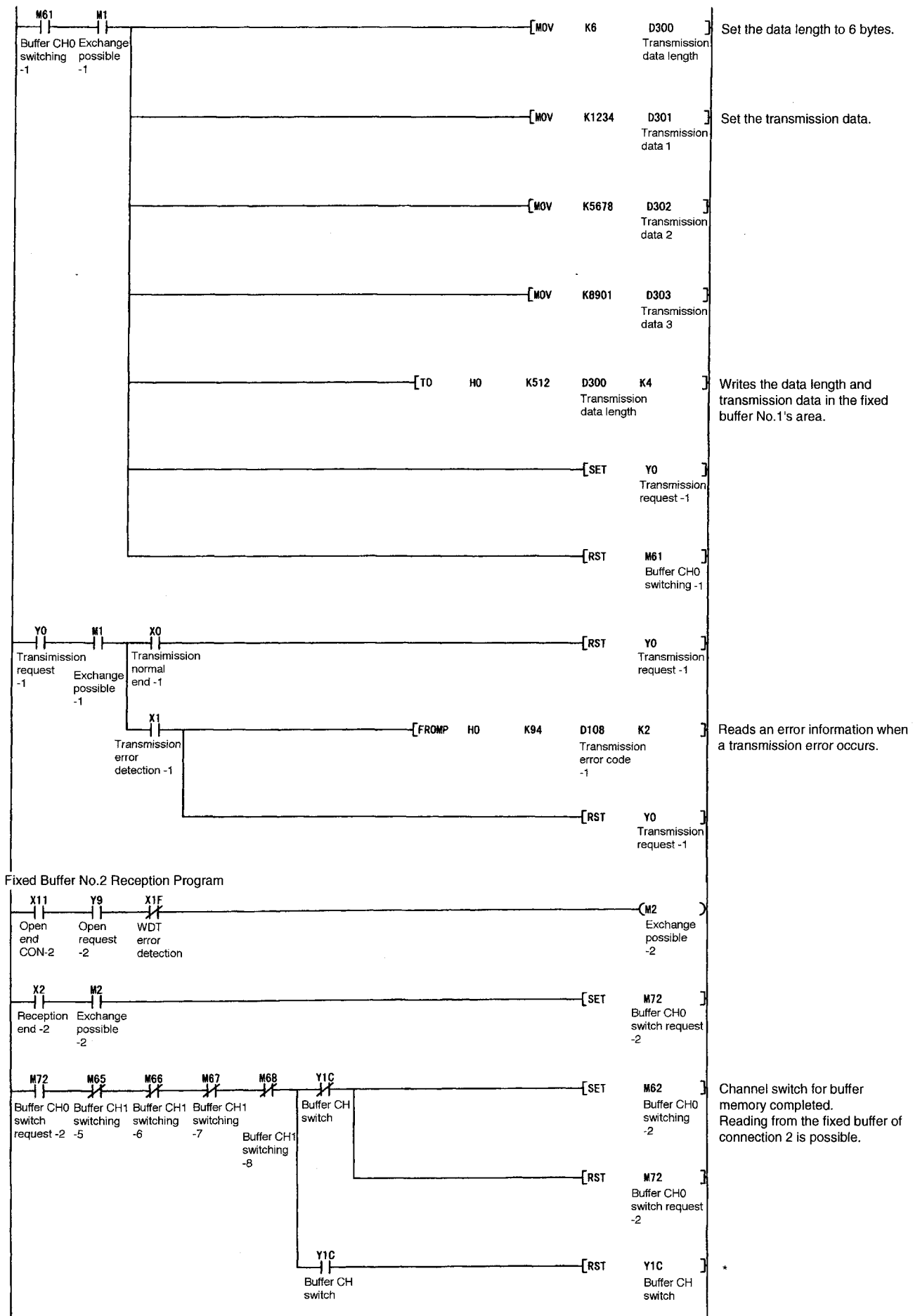
This section explains the programming method for performing data exchange with a remote node using fixed buffer No.1 with procedure.

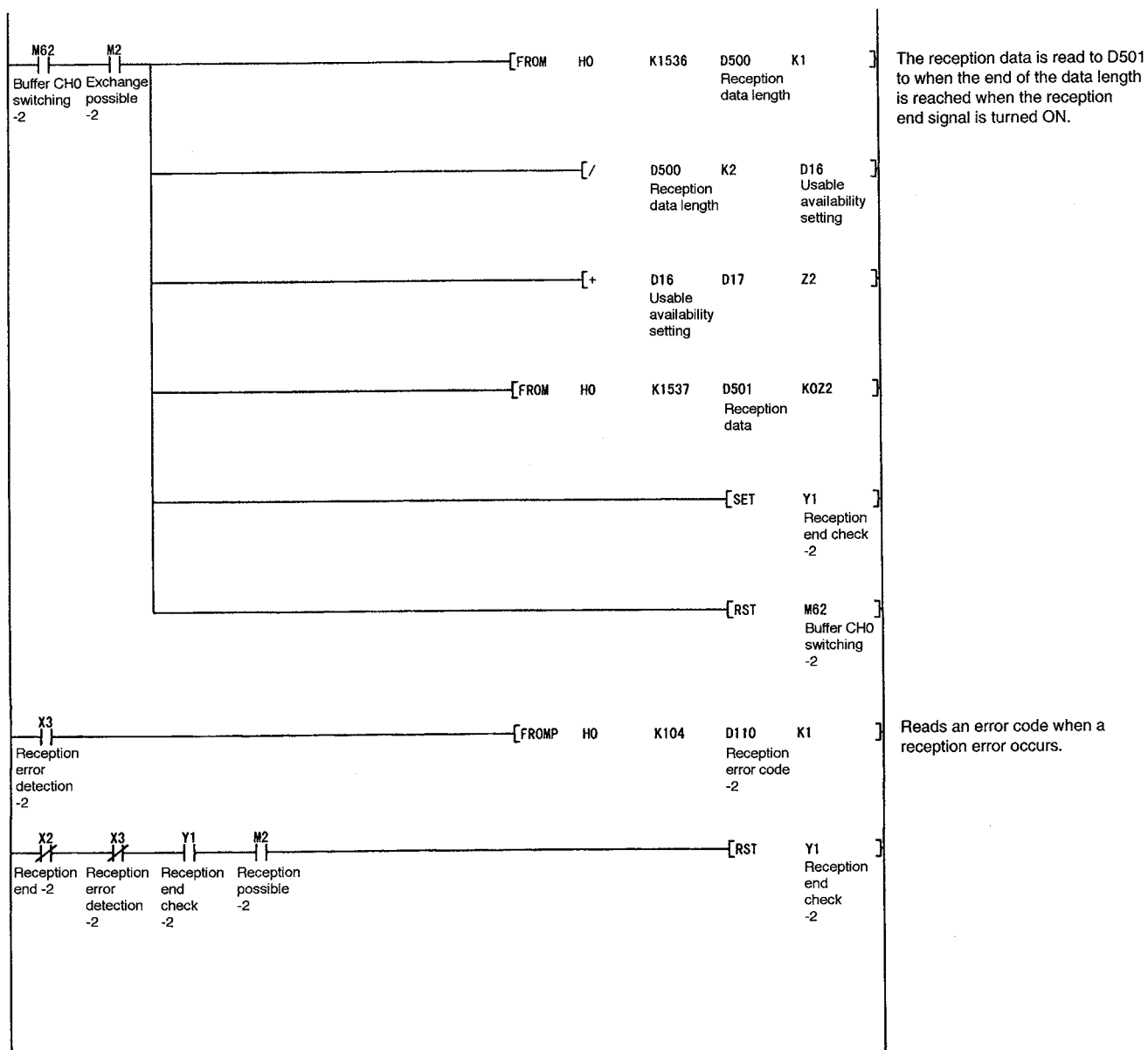
(Program Conditions)

- Set the exchange parameters for each connection as specified in Item 5.4.5.
- Set the fixed buffer transmission data to D300 to D303.
- Store the fixed buffer reception data in D500 to D503.
- The storage destination for the error code and end code are allotted as follows.
D108 Transmission error code
D109 Exchange end code
D110 Reception error code

Fixed Buffer No.1 Transmission Program







* When the I/O control method of the PLC CPU of the station installed in the E71 is the refresh method and a fixed buffer read/write is performed after the buffer memory channel switching signal (Y001C) is switched from on → off/off → on, a read/write must be performed after the channel switching signal (Y001C) is output to the E71. Output the channel switching signal (Y001C) to the E71 using the tail's *1 of Item 3.6.2.

RANDOM ACCESS BUFFER EXCHANGE SECTION

The random access buffer exchange section explains the method for exchanging data between the remote node's external equipment and the PLC CPU using the Ethernet interface module's random access buffer.

Random access buffer exchange begins after the initial processing and open processing that connects the communication line as described in Chapter 5.

In addition, perform close processing and end processing when data exchange is completed for the subject communication line.

8. RANDOM ACCESS BUFFER EXCHANGE

This section explains the method for conducting exchange with remote nodes using the E71's random access buffer.

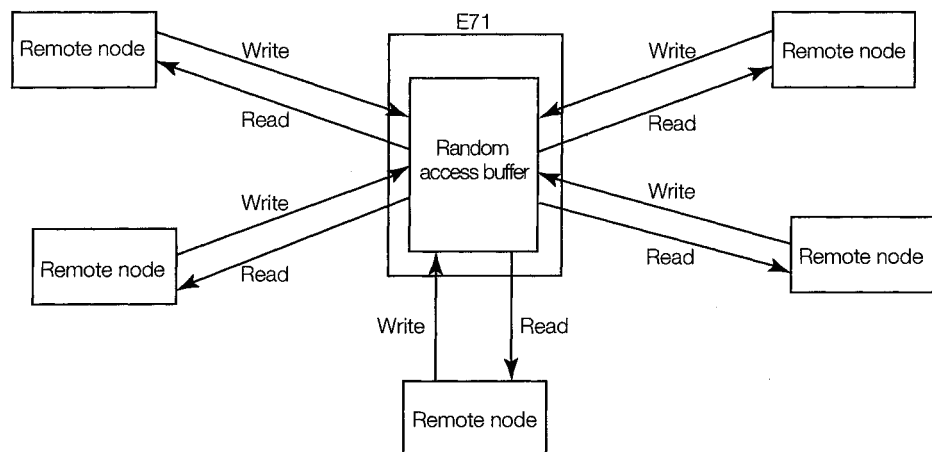
8.1 Control Format

This section explains the exchange processing control format using the random access buffer.

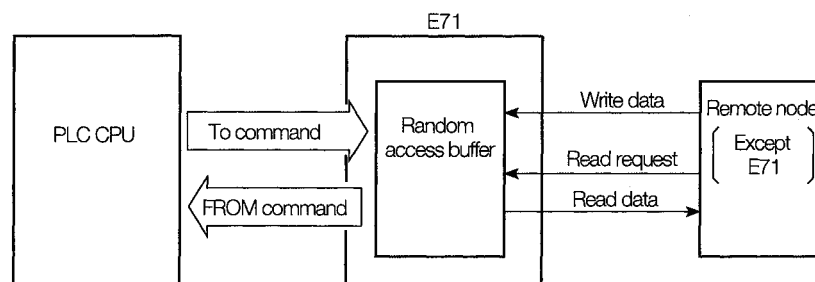
For random access buffer exchange, data is written to the random access buffers and read from the random access buffer using commands (requests) from the remote nodes.

Writing to and reading from the E71's random access buffer from the remote nodes is conducted asynchronously with the PLC CPU's sequence program.

- 1** The random access buffer is not fixed to the remote node to which exchange is done, but writing and reading can be freely conducted from any remote node (except E71). Therefore, a common buffer area can be used for all nodes connected to the Ethernet.



- 2** The data flow for exchange processing using the random access buffer is shown below.

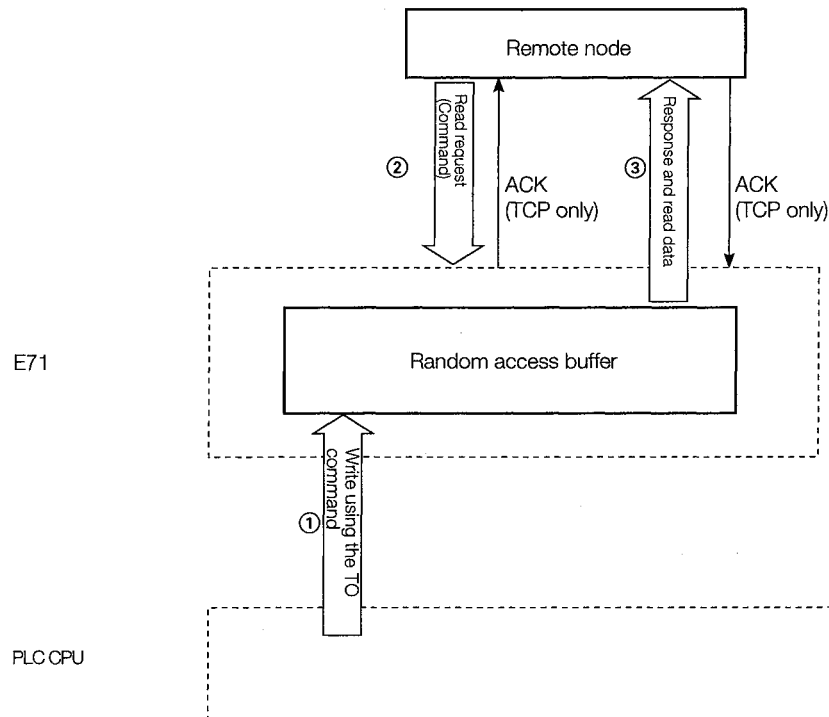


- 3** Random access buffer exchange can be conducted from the remote node shown below except for E71, AJ71E71, and QE71. (Exchange cannot be done between E71 and E71, AJ71E71, or QE71.)

- ① Remote nodes on the Ethernet to which the E71 is connected.
- ② Remote nodes on the Ethernet that are connected using router relay functions (Refer to Chapter 12).

8.1.1 Control Method When there is a Read Request from a Remote Node

This section explains the control method when data is transmitted from the E71 by a read request from a remote node.



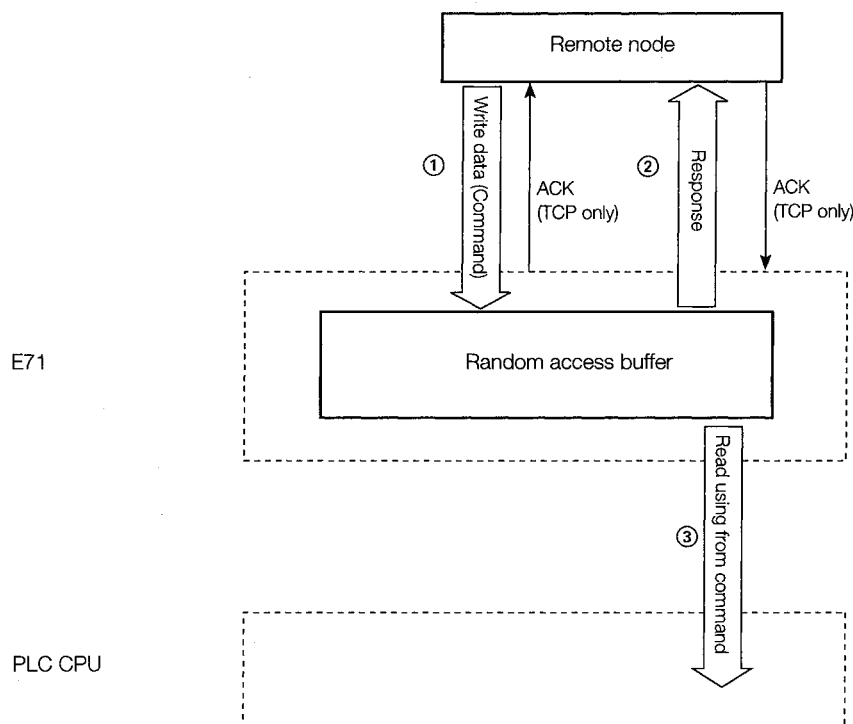
- ① Data is written to the E71's random access buffer using the sequence program's TO command.
In addition, data is written to the E71's random access buffer from a remote node.
- ② A read request is transmitted from the remote node that will read the E71's random access buffer contents to the E71.
- ③ When a read request is received from a remote node, the E71 will send the data written in the random access buffer to the node from which the request was received as a response.

Point

- (1) With random access buffer exchange, exchange can only be conducted with the remote node for which the E71's open end signal (X10 to X17) is on.
- (2) Random access buffer exchange is conducted asynchronously with the sequence program. When synchronous exchange is required, conduct exchange by putting a free protocol between the partner remote node to which communication is being done and the PLC CPU.

8.1.2 Control Method When there is a Write Request from a Remote Node

This section explains the control mode when data is written by the remote node to the E71's random access buffer.



- ① Data is written from the remote node to the E71's random access buffer.
- ② When the E71 receives data from the remote node, a response is returned to the remote node that conducted the transmission.
- ③ The data received by the random access buffer using the sequence program's FROM command is read.

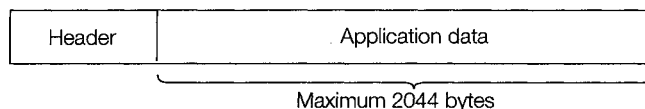
In addition, the data being received to the E71's random access buffer can be read by the separate remote node.

Point

- (1) For random access buffer exchange, exchange can only be conducted with remote nodes for which the E71's open end signal (X10 to X17) is on.
- (2) Random access buffer exchange is conducted asynchronously with the sequence program. When synchronous is required, conduct exchange by adding a free protocol between the partner remote node to which exchange is being conducted and the PLC CPU.

8.2 Data Format

This shows the exchange data (command and response) data item order and contents when random access buffer exchange is conducted between the E71 and a remote node. As shown below, the exchange data consists of a header and application data.



As shown below, the data code of the application data can be shown as either binary or ASCII code.

Use the exchange condition setting switches (SW2: data code setting) of the E71 to set either binary or ASCII. (For details regarding the setting method refer to Item 4.3.2.)

8.2.1 Format When Exchanging with Binary Code

Following shows the command in response data item order when exchanging the exchange data application data portion in binary code data when conducting random access buffer exchange.

1

Transmission/reception data order when exchanging using TCP/IP

(a) When a read request is made from the remote node

① Order during command transmission

Header			Application data		
Ethernet	IP	TCP	Subheader	Head address (L) (H)	Data length setting (L) (H)
(14 bytes)	(20 bytes)	(20 bytes)	61H 00H (2 bytes)	(2 bytes)	(2 bytes)

② Order during response reception

Header			Application data		
Ethernet	IP	TCP	Subheader	End code	Text (command)
(14 bytes)	(20 bytes)	(20 bytes)	E1H (1 byte)	(1 byte)	(Maximum 1017 words)

(b) When a write request is received from remote node

① Order during command transmission

Header			Application data			
Ethernet	IP	TCP	Subheader	Head address (L) (H)	Data length setting (L) (H)	Text (command)
(14 bytes)	(20 bytes)	(20 bytes)	62H 00H (2 bytes)	(2 bytes)	(2 bytes)	(Maximum 1017 words)

② Order during response reception

Header			Application data	
Ethernet	IP	TCP	Subheader	End code
(14 bytes)	(20 bytes)	(20 bytes)	E2H (1 byte)	(1 byte)

2

Transmission/reception data order when exchanging using UDP/IP

(a) When a read request is made from the remote node

① Order during command transmission

Header			Application data		
Ethernet	IP	UDP	Subheader	Head address (L) (H)	Data length setting (L) (H)
(14 bytes)	(20 bytes)	(8 bytes)	61H 00H (2 bytes)	(2 bytes)	(2 bytes)

② Order during response reception

Header			Application data		
Ethernet	IP	UDP	Subheader	End code	Text (command)
(14 bytes)	(20 bytes)	(8 bytes)	E1H (1 byte)	(1 byte)	(Maximum 1017 words)

(b) When a write request is received from remote node

① Order during command transmission

Header			Application data			
Ethernet	IP	UDP	Subheader	Head address (L) (H)	Data length setting (L) (H)	Text (command)
(14 bytes)	(20 bytes)	(8 bytes)	62H 00H (2 bytes)	(2 bytes)	(2 bytes)	(Maximum 1017 words)

② Order during response reception

Header			Application data	
Ethernet	IP	UDP	Subheader	End code
(14 bytes)	(20 bytes)	(8 bytes)	E2H (1 byte)	(1 byte)

8.2.2 Format When Exchanging with ASCII Code

The following shows the command in response data item order when exchanging the exchange data application data portion in ASCII code data when conducting random access buffer exchange.

1

Transmission/reception data order when exchanging using TCP/IP

(a) When a read request is made from the remote node

① Order during command transmission

Header			Application data							
Ethernet	IP	TCP	Subheader				Head address		Data length setting	
			"6"	"1"	"0"	"0"	(H)	(L)	(H)	(L)
			36 _H	31 _H	30 _H	30 _H				
(14 bytes)	(20 bytes)	(20 bytes)	(4 bytes)				(4 bytes)		(4 bytes)	

② Order during response reception

Header			Application data		
Ethernet	IP	TCP	Subheader	End code	Text (command)
"E"	"1"				
45H	31H				
(14 bytes)	(20 bytes)	(20 bytes)	(2 bytes)	(2 bytes)	(Maximum 1016 words)

(b) When a write request is received from remote node

① Order during command transmission

Header			Application data								
Ethernet	IP	TCP	Subheader				Head address		Data length setting		Text (command)
			"6"	"2"	"0"	"0"	(H)	(L)	(H)	(L)	
			36 _H	32 _H	30 _H	30 _H					
(14 bytes)	(20 bytes)	(20 bytes)	(4 bytes)				(4 bytes)		(4 bytes)		(Maximum 1016 words)

② Order during response reception

Header			Application data	
Ethernet	IP	TCP	Subheader	End code
"E"	"2"			
45H	32H			
(14 bytes)	(20 bytes)	(20 bytes)	(2 bytes)	(2 bytes)

2

Transmission/reception data order when exchanging using UDP/IP

(a) When a read request is made from the remote node

① Order during command transmission

Header			Application data							
Ethernet	IP	UDP	Subheader				Head address		Data length setting	
			"6"	"1"	"0"	"0"	(H)	(L)	(H)	(L)
			36 _H	31 _H	30 _H	30 _H				
(14 bytes)	(20 bytes)	(8 bytes)	(4 bytes)				(4 bytes)		(4 bytes)	

② Order during response reception

Header			Application data		
Ethernet	IP	UDP	Subheader		Text (command)
			"E"	"1"	End code
			45H	31H	
(14 bytes)	(20 bytes)	(8 bytes)	(2 bytes)		(Maximum 1016 words)

(b) When a write request is received from remote node

① Order during command transmission

Header			Application data								
Ethernet	IP	UDP	Subheader				Head address		Data length setting		Text (command)
			"6"	"2"	"0"	"0"	(H)	(L)	(H)	(L)	
			36 _H	32 _H	30 _H	30 _H					
(14 bytes)	(20 bytes)	(8 bytes)	(4 bytes)				(4 bytes)		(4 bytes)		(Maximum 1016 words)

② Order during response reception

Header			Application data	
Ethernet	IP	UDP	Subheader "E" "2" 45H 32H	End code
(14 bytes)	(20 bytes)	(8 bytes)	(2 bytes)	(2 bytes)

8.2.3 Exchange Data Item Contents

This shows the command in response data item contents when conducting random access buffer exchange.

1 Header

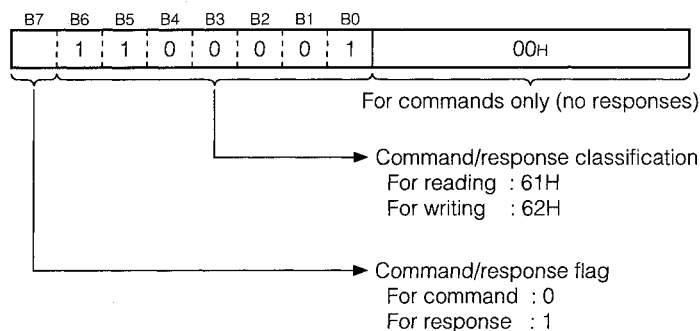
This header is the header that is used for TCP/IP or UDP/IP. For E71, since this can be added or removed the user is not required to make the settings.

2 Subheader

The subheader format is as shown below.

For the E71, since the E71 text is automatically added and removed the user is not required to make the settings. The subheader data code order when conducting random access buffer exchange is shown below.

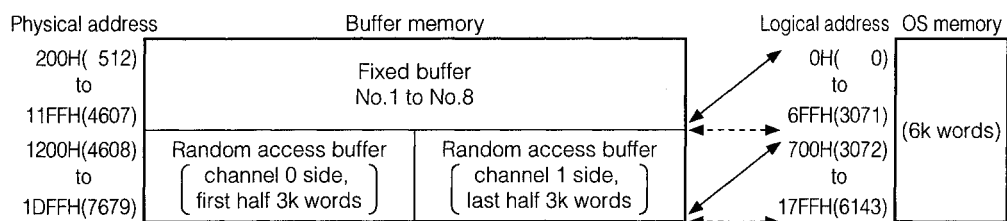
	Subheader code during exchange			
	For reading		For writing	
	For commands	For response exchange	For commands	For response exchange
For binary code exchange	61H 00H	E1H	62H 00H	E2H
For ASCII code exchange	36H 31H 30H 30H	45H 31H	36H 32H 30H 30H	45H 32H



3 Head address

This shows the random access buffer range head address (1200H to 1DFFH) using a logical address (0H to 17FFH, refer to Item 8.3.1) when reading/writing data.

- Specifies the head address with a binary value when exchanging with binary code.
- Specifies ASCII code when the head address is expressed using a hexadecimal numeral when exchanging with ASCII code.
- The random access buffer specification address is as shown below.



Remarks

The random access buffer specified address differs from the address specified by the remote node and the address specified by the PLC program FROM/TO instruction, so practice due caution.

Physical address Address specified by the PLC program FROM/TO instruction.

Logical address Address specified in the head address in the command for random access buffer exchange.

4 Data length setting

This shows the number of read/write data words in the random access buffer range.

- (a) When exchanging using binary code the number of words is specified by binary value.
- (b) When exchanging using ASCII code, the number of words is expressed in hexadecimal and is specified by ASCII code.

Point

- (1) When binary code has been specified the size of the random access buffer subject to read/write is a maximum of 1017 words.
- (2) When ASCII code is specified, the size of the random access buffer subject to read/write is a maximum of 508 words. This is approximately one-half of that when binary code is specified.

5 Text

This shows the write and read data for the random access buffer.

- (a) When exchanging using binary code the data code in the random access buffer is transmitted/received as is.
- (b) When exchanging using ASCII code the data in the random access buffer is transmitted/received after being converted to ASCII code.
- (c) For information regarding the data code and order when transmitting/receiving data refer to Item 8.2.4.

6 End code

Shows the end code added to the response during random access buffer exchange.

The end code is stored in the buffer memory exchange state storage area.

When Binary Code is Specified		When ASCII Code is Specified	
00H	Normal end	30H30H	Normal end
50H	Command/Response type undefined error	35H30H	Command/Response type undefined error
51H	Head address defective	35H31H	Head address defective
52H	Number of data words defective	35H32H	Number of data words defective
		35H34H	ASCII conversion error

(For details regarding error codes, refer to Chapter 13.)

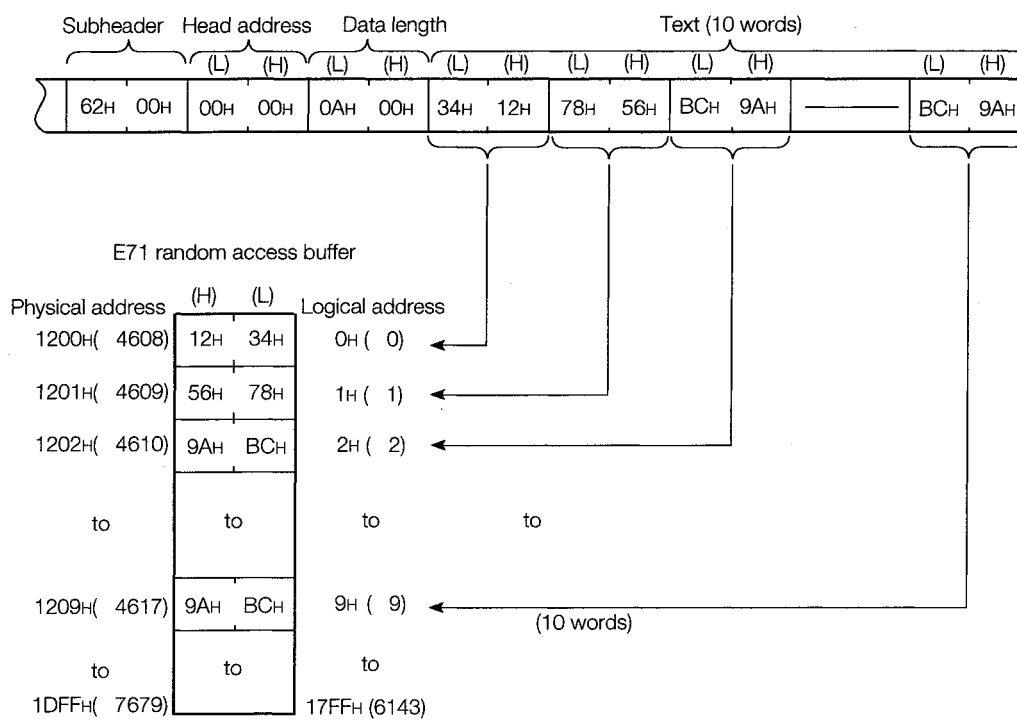
8.2.4 Example Command and Response Format

Following is an example of the command and response format during random access buffer exchange.

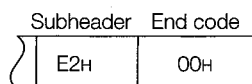
1 Write to buffer by write request from remote node

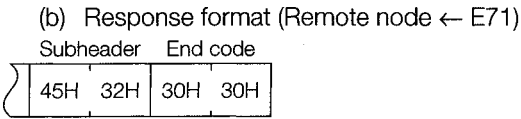
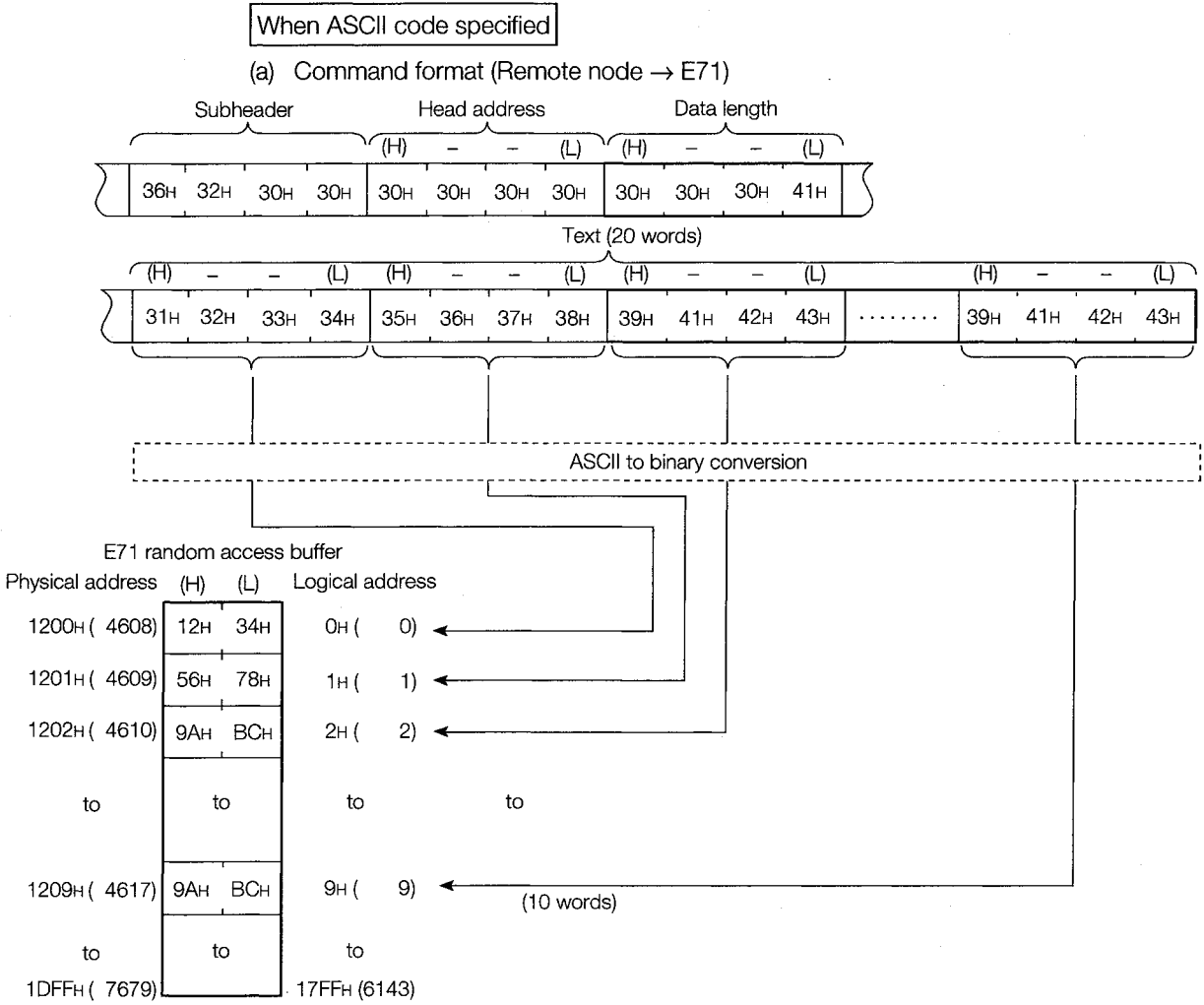
When binary code specified

(a) Command format (Remote node → E71)



(b) Response format (Remote node ← E71)



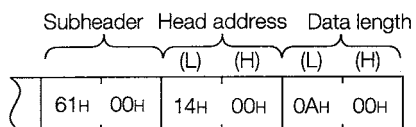


2

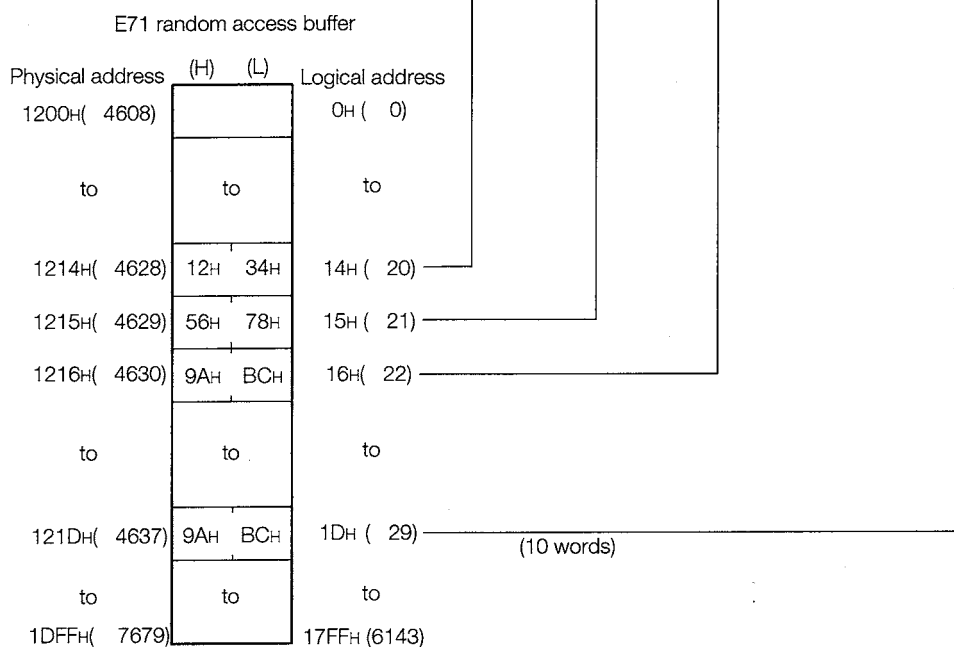
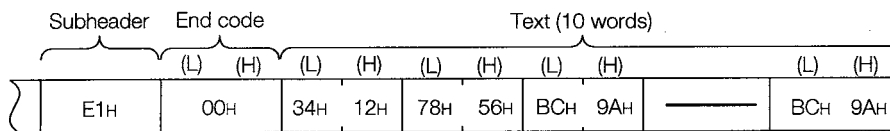
Read from buffer using read request from remote node

When binary code specified

(a) Command format (Remote node → E71)

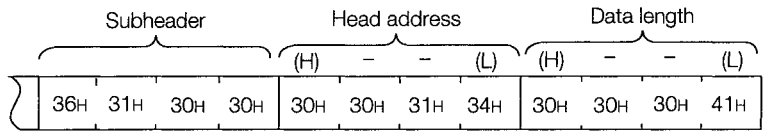


(b) Response format (Remote node ← E71)

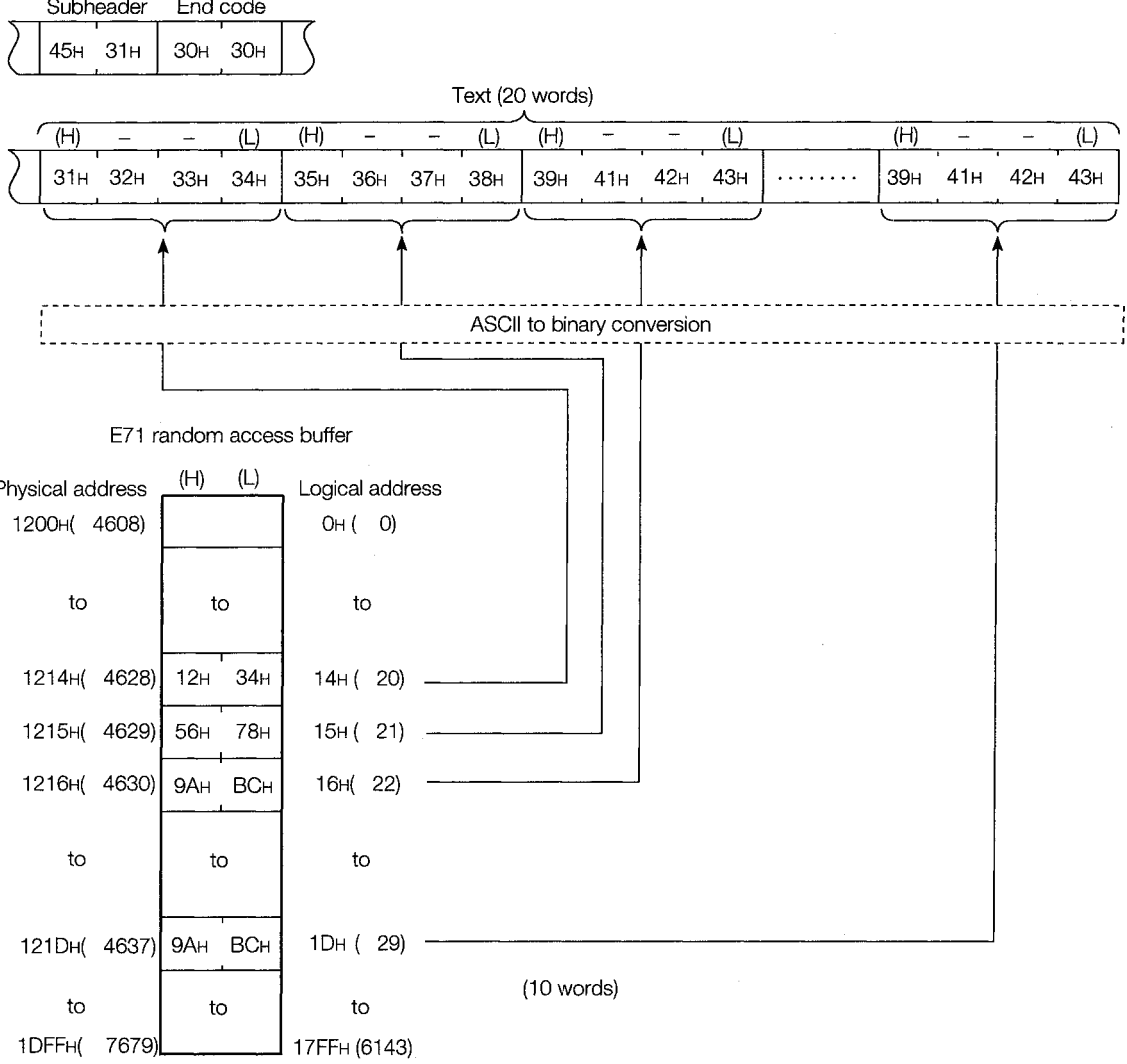


When ASCII code specified

(a) Command format (Remote node → E71)



(b) Response format (Remote node ← E71)



8.3 Programming

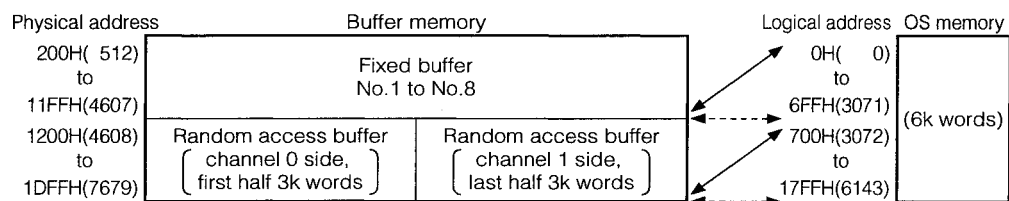
This section explains the programming for conducting exchange between the E71 and a remote node using the random access buffer.

8.3.1 Program Creation Precautions

- (1) Exchange with a remote node using the random access buffer is conducted asynchronously with the PLC CPU program.

When synchronous exchange is required, conduct exchange by adding a free protocol between the partner remote node to which exchange will be conducted and the PLC CPU.

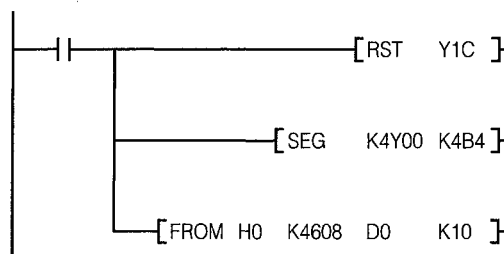
- (2) For the random access buffer, the address specified by the remote node and the address specified by the sequence program's FROM/TO commands differ, so caution is required.



- ① Random access buffer address specified by the remote node
 - The random access buffer can be read from or written to as a 6k word continuous area.
 - The random access buffer address can be specified with the head area as 0000H (0) and the logical address as 0000H to 17FFH (0 to 6143).
 - It is not necessary to recognize the channel switching signal (I/O signal: Y001C) on/off between the PLC CPU and the E71.
- ② Random access buffer address specified by the sequence program
 - The random access buffer can be read from/written to as a 3k byte area for each of the channel 0 and channel 1 side.
 - The channel 0 side and channel 1 side random access buffers can be read from/written to after the channel switching signal (Y001C) on/off between the PLC CPU and the E71.
 Y001C = off : Read/write to the channel 0 side random access buffer.
 Y001C = on : Read/write to the channel 1 side random access buffer.
 - Set the random access buffer address to physical address 1200H to 1DFFH (4608 to 7679).

- When the I/O control method of the PLC CPU of the station installed in the E71 is the refresh method and when random access buffer read/write is conducted after the buffer memory channel switching signal (Y001C) is changed from on → off/off → on, a read/write must be performed after outputting the channel switching signal (Y001C) to the E71. Output the channel switching signal (Y001C) to the E71 using the tail*1 of Item 3.6.2.

(Example) When the common instruction's SEG is used.



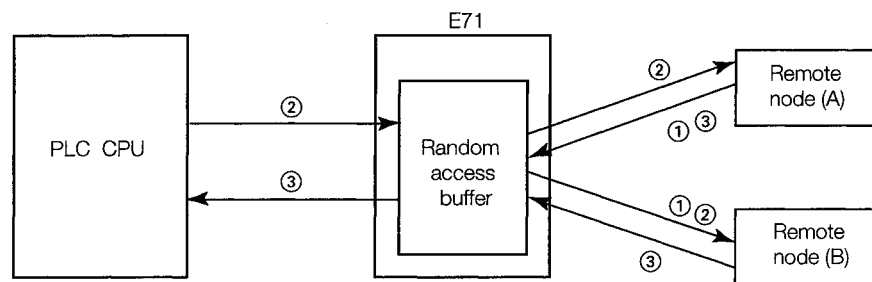
- (3) For command transmission, the next command should be sent after the completion of data communication (such as after the reception of a response) for the transmission of the previous command.

8.3.2 Program Creation Procedure

This section explains about the random access buffer exchange procedure.

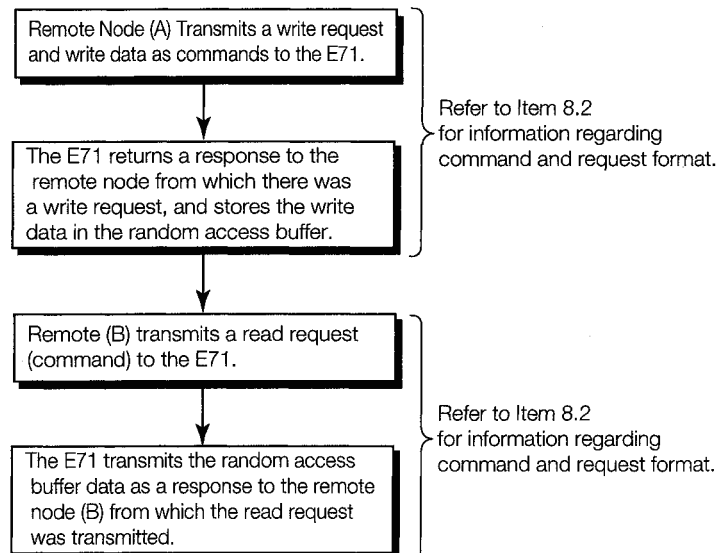
As shown below, there are three exchange methods that can be used for random access buffer exchange.

- ① Remote node (A) or (B) reads the data written into the E71's random access buffer by remote node (A).
- ② Remote nodes (A) and (B) read the data written in the E71's random access buffer by the sequence program.
- ③ The sequence program reads the data written in the E71's random access buffer by remote node (A) or (B).



Following is an explanation of the exchange procedure for three exchange methods described above.

1 Exchange method where remote (B) reads the data written by remote node (A)



2**Communication procedure when the remote node reads the data written by the PLC program**

The sequence program TO command writes data to the E71's random access buffer.



A remote node transmits a read request (command) to the E71.



The E71 transmits as a response the random access buffer data to the remote node from which the read request was received.

Refer to Item 8.2 for information regarding command and response format.

3**Exchange procedure when the data written by a remote node is read by the PLC program**

The remote node transmits a write request and write data as command to the E71



The E71 returns a response to the remote node from which the write request was transmitted, and stores the write data in the random access buffer.



The E71's random access buffer data is read by a sequence program FROM command.

Refer to Item 8.2 for information regarding commands and response format.

Remarks

With random access buffer exchange, a handshake cannot be conducted using the E71's I/O signal.

This image shows a single sheet of white paper with horizontal ruling lines. The lines are evenly spaced and run across the width of the page. There are no margins, text, or other markings on the paper.

READING/WRTING DATA IN THE PLC CPU SECTION

The reading and writing data in the PLC CPU section describes the method for reading and writing device memory and programs in the PLC CPU by the remote node's external devices via the Ethernet interface module, and the method for conducting remote control of the PLC CPU.

The read/write of data in the PLC CPU can be conducted regardless of the PLC CPU's RUN/STOP status when the data exchange function is used while the PLC CPU is stopped.

After connecting the communication line using the initial processing and open processing shown in Chapter 5 conduct a read/write of data in the PLC CPU.

In addition, conduct close processing and end processing when the data exchange ends in the corresponding communication line.

When conducting read/write of data in the PLC CPU, first read the explanation regarding common items in Chapter 9.

The read/write using E71 is explained in Chapter 10.

9. READING/WRITING DATA IN THE PLC CPU EXCHANGE

This section explains the control method, command list, and data exchange precautions when reading and writing PLC device and program data, etc., via the E71 from a remote node.

9.1 Control Method

This section explains the control method when reading and writing data in the PLC CPU.

1 Reading and writing data in the PLC CPU can be performed regardless of the E71's I/O signal on/off state and the existence of the data exchange sequence program.

2 When writing to the PLC CPU from a remote node, the write approval/prohibition during PLC CPU RUN, can be selected using the exchange condition setting switch (SW7: CPU exchange timing setting) of the E71.

Exchange timing setting (Refer to Item 4.3.2)

SW7=OFF: Writing from a remote node is not possible during PLC CPU RUN.

SW7=ON : Writing from the remote node is possible during both PLC CPU RUN/STOP.

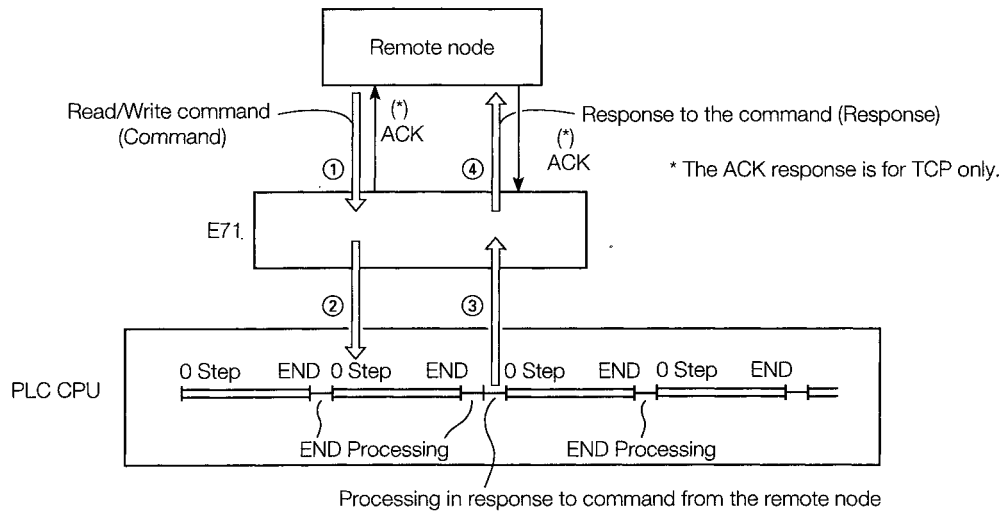
3 Data can be read from and written to the PLC CPU and special function unit by the remote node by transmitting the E71 commands described in Item 9.2 to the E71. In addition, it is also possible to read and write data to the remote station PLC CPU and special functions units on the MELSECNET.

Point

- (1) When writing to the special functions module installed in the remote I/O stations in the data link system or network system from the remote node, the exchange condition setting switch (SW7: CPU exchange timing setting) must be set to on. (The remote I/O station will change to the normal RUN state. You cannot switch between the RUN and STOP.)
- (2) Refer to each system reference manual for details regarding the access possible range for remote station PLC on data link system or network system.

9.1.1 Exchanging with the PLC CPU Installed in the Ethernet Interface Module

- 1 The control method for reading and writing data in the PLC CPU installed in the E71 is as follows.



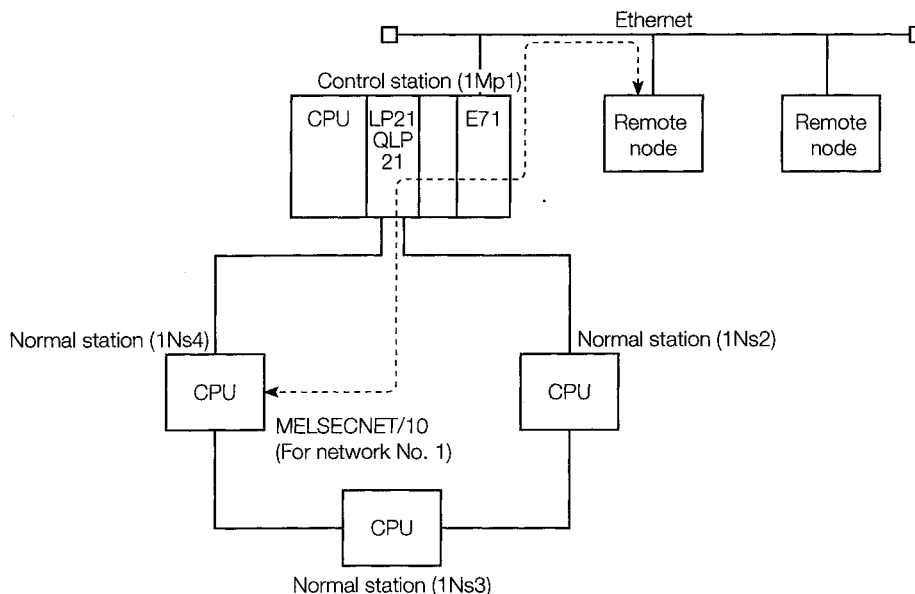
- ① The remote node transmits to the E71 a command (command) to read/write data in the PLC CPU.
- ② When the E71 receives the command from the remote node, it requests the read/write of data in the PLC CPU in accordance with the contents of the command.
- ③ When the sequence program's END command is executed, the PLC CPU follows the request from the E71 and conducts the data read/write and then transmits the processing results to the E71.
- ④ When the E71 receives the processing results from the PLC CPU, it sends a response that includes the processing results to the remote node from which the request originated.

Point

When read/write from the remote node is conducted during PLC CPU RUN, the processing time in response to the command from the remote node and the sequence program's scan time could become longer so caution is required.

9.1.2 Exchanging with the PLC CPU in the Network System

- 1 When reading and writing data in the PLC CPU, reading and writing to the remote station PLC on the MELSECNET/10 can be done via the PLC CPU installed in the E71 within the network system's specification range.



- 2 The PLC that conducts read/write is specified in the PLC No. (FFH, 00H to 40H) in the command text.

	Remote Node Access Station	PLC No. Specified by Remote Node
1	E71 installed station (Local station)	FFH
2	Network control station between PLC on the MELSECNET/10 (Other station) (When the E71 is installed in the write normal station in the network between PLC)	0H (0)
	Remote I/O net's master station on the MELSECNET/10 (Other station) (When the E71 is installed and remote I/O net remote station)	
3	Station on the MELSECNET/10 (Except for 1 and 2 above)	01H to 40H (1 to 64) (Access station No.)

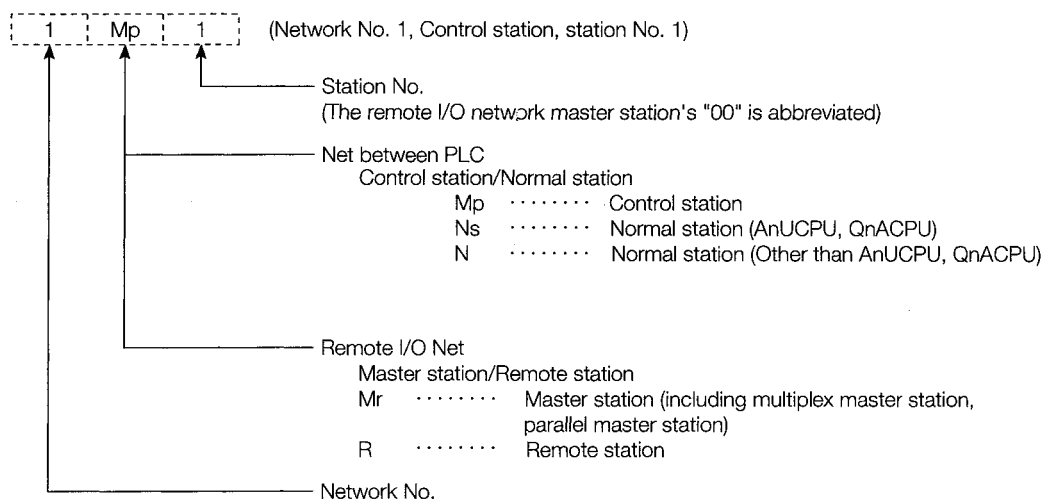
Point

- (1) When the station installed in the E71 is a base AnU/QnACPU and remote station access via the station installed in the E71 is conducted, the following parameters are set in the PLC CPU of the station installed in the E71 using the GX Developer.
 - * "Valid unit during remote station access" setting : Set in the number of units setting, and set the unit through its exchange will pass during remote station access.
- (2) When multiple network units are installed for the same network No. in the E71 installation station, remote station access is done via the network module installed in the base unit's slot of the newest No. when the network No. is specified.

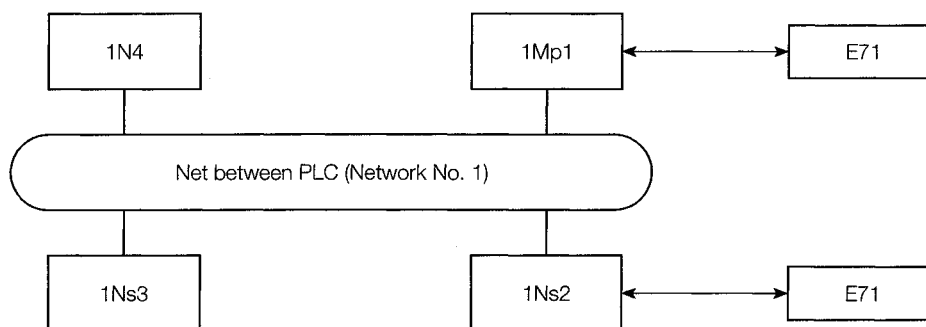
- 3** Of the other stations in the network system those for which exchange with a PLC is possible are shown below. The exchange possible PLC vary depending on the stations installed with the E71.

(Meaning of station symbols shown in the diagram)

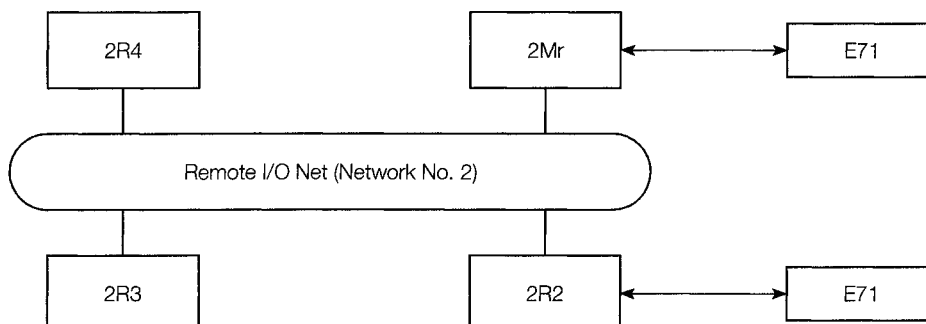
- Network system (MELSECNET/10)



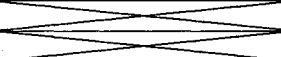
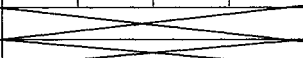
- (a) When the E71 installed station is the net between PLC command station/normal station, and when the remote I/O net master station



- (b) When the E71 installed station is the remote I/O net's remote station



(c) PLC No. when E71 commands are used

PLC installed in the E71	Exchange possible PLC and PLC No. item specification values (Hexadecimal numbers)								
	Local Station	1Mp	1Ns2	1Ns3	1N4	2Mr	2R2	2R3	2R4
1Mp1	FF	—	02	03	04				
1Ns2	FF	01	—	03	04				
2Mr	FF					—	02 ^{*1}	03 ^{*1}	04 ^{*1}
2R2	FF ^{*2}					00	—	x	

n Access to all devices is possible by setting the subject's PLC No.

n^{*1} Access to the special function unit buffer memory is possible by setting the subject PLC No.n^{*2} The devices shown in Item (2) of Section 10.2.1 can be accessed within the range of the A3ACPU by setting the appropriate PLC No.

x Access is not possible.

4

Transmission time when via network system

(a) The transmission time (T1) when data is transmitted to a PLC on a network system in which an E71 is not installed is shown below.

① For net between PLC

$$\bullet \text{ Transmission time (T1)} = \frac{\text{Transmission delay time}}{*1} + \text{E71 installed station 1 scan time} \times \frac{(n+1)}{*2 \quad *3}$$

*1 Refer to the network system reference manual for an explanation of the transmission delay time.

- *2
- When initial exchange is conducted for the subject station when the power is turned on and after the CPU is reset.
 - When exchange is conducted to the station except the 10 stations most recently exchange with.
 - When exchange is conducted the second time when the number of exchange stations is under 10.
 - When exchange is conducted the second time to the first 10 stations.
- $\left. \begin{array}{l} \text{---} \\ \text{---} \end{array} \right\} n = 6$
 $\left. \begin{array}{l} \text{---} \\ \text{---} \end{array} \right\} n = 1$

*3 When the "CPU exchange timing setting" of the E71 exchange condition setting switch SW7 is off (write prohibited), it is added only when data is written from remote node.

- Reason for transmission time (T1) delay

When commands that require two scans (device write, etc when the exchange condition setting switch (SW7: CPU exchange timing setting) is off, etc.) are executed, then the value is calculated using the above formula. Refer to Item 9.3 2 for the required the number of scans when a remote E71, GX Developer, etc. requests access to the same PLC CPU at the same time.

- Increase the CPU monitoring timer's monitoring time from the other station when exchange to other station is conducted via MELSECNET/10.

* For details regarding network systems, refer to the network system reference manuals.

(Example) When an E71 is installed in the station on the MELSECNET/10 (net between PLC) and the device memory for a remote system on the same MELSECNET/10 is read. (Second exchange time when the number of exchange stations is under 10)

- ST : Transmission scan time 120ms
- α T : Transmission link refresh time 10ms
- SR : Receive scan time 100ms
- α R : Receive link refresh time 5ms
- LS : Link scan 30ms
- Number of simultaneous transient requests : 3
- Maximum number of times for transient : 2 (Using the user set value when for the QnACPU.)

$$\text{Transmission Time (T1)} = \{ \underset{(\text{ST})}{120} \times \underset{(\alpha\text{T})}{2} + \underset{(\text{LS})}{30} \times \underset{(\text{SR})}{2} + \underset{(\alpha\text{R})}{5} \times \underset{(\text{ST})}{2} \}$$

$$+ \left[\frac{3 \text{ (Number of simultaneous transient requests)}}{2 \text{ (Maximum number of transient times)}} - 1 \right] \times \underset{(\text{LS})}{30} \times \underset{(\text{ST})}{2} + 1 = 890\text{ms}$$

Adjustment value (Decimal round off)

② For Remote I/O Net

$$\bullet \text{ Transmission Time (T1)} = \frac{\text{Transmission delay time}}{*1} + 1 \text{ link scan time} \times \frac{(n + 1)}{*2 \quad *3}$$

*1 Please refer to the explanation of transmission delay time in the network system reference manual.

- *2
- When exchange is conducted for the first time to the subject station after link is begun.
 - When conducting exchange to a station except the latest 10 stations to which exchange was conducted.
 - When conducting exchange for the second time when the number of exchange stations is under 10.
 - When conducting exchange for the second time to the latest 10 stations in which exchange was conducted.
- $\left. \begin{array}{l} \text{---} \\ \text{---} \end{array} \right\} n = 6$
 $\left. \begin{array}{l} \text{---} \\ \text{---} \end{array} \right\} n = 1$

*3 When the "CPU exchange timing setting" of the E71 exchange condition setting switch SW7 is off (write prohibited), it is added only when data is written from remote node.

• Reason For Transmission Time (T1) Delay

When commands that require two scans (device write, etc. when the exchange condition setting switch (SW7: CPU exchange timing setting) is off, etc.) are transmitted, the value is twice that calculated using the above formula.

Refer to Item 9.3 2 for information regarding the necessary number of scans when request access to the same PLC CPU is made at the same time by a remote E71, GX Developer, etc.

- Increase the CPU monitoring timer's monitoring time from the other station when exchange to other station is conducted via MELSECNET/10.

* Refer to the network system reference manual for details regarding network system.

(Example) When an E71 is installed in a station on an MELSECNET/10 and read from the other station device memory is conducted on the same MELSECNET/10.

(Second exchange time when the number of exchange stations is under 10)

- Sm : Master station sequence scan time 120ms
- α m : Master station link refresh time 10ms
- α r : Remote I/O station link refresh time 2ms
- LS : Link scan time 30ms

Because the above (Sm) > (LS) the formula is as follows. (When there is one master station)

$$\text{Transmission Time (T1)} = \{ (120 + 10) \times 3 + 30 \} \times 1 = 420\text{ms}$$

(Sm) (α m) (LS)

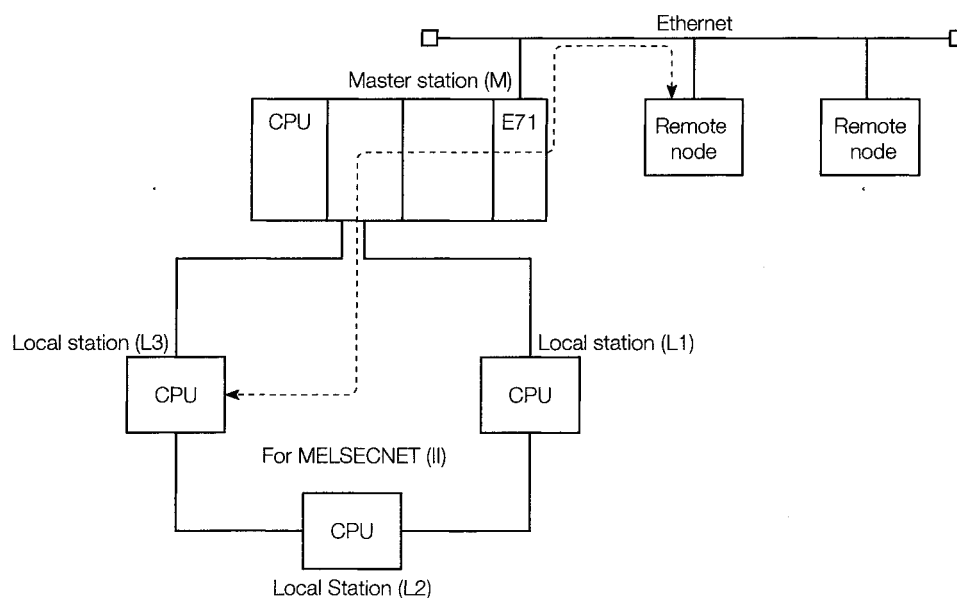
Point

There will be an appropriate delay corresponding to the conditions during data transmission to a PLC in which an E71 is not installed on the MELSECNET/10.

The transmission delay time for exchange with the PLC can be reduced by using the E71 installed station (PLC No.FFH) only and using the MELSECNET/10 data link (LB, LW) for exchange with remote station PLC.

9.1.3 Exchanging with the PLC CPU in the Data Link System

- 1 When reading and writing in the PLC CPU, reading and writing can be done to the other station PLC in the MELSECNET(II) and MELSECNET/B via the PLC CPU in which an E71 is installed within the data link system specification range.



- 2 The PLC that conducts read/write is specified in the PLC No. (FFH, 00H to 40H) in the command text.

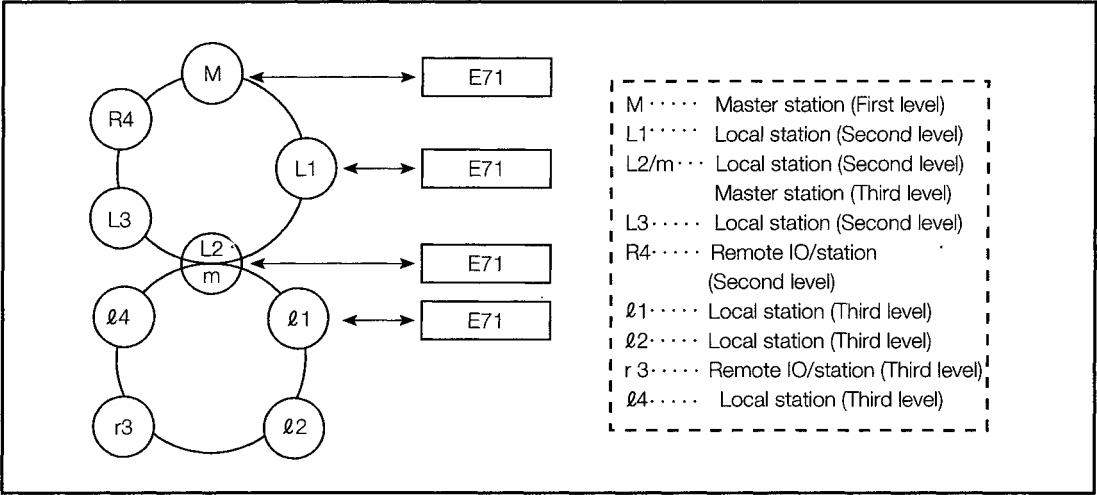
	Remote Node Access Station	PLC No. Specified by the Remote Node
1	When E71 is installed (Local station)	FFH
2	Master station on the MELSECNET(II) (Other station) (Except 1 above)	00H
3	Local station/Remote station on the MELSECNET(II) (Other station) (Except 1 and 2 above)	01H to 40H (1 to 64) (Access station's station No.)
4	Other station on the MELSECNET/B	(Same as 2 and 3 above)

Point

- (1) When the station installed in the E71 is a base AnU/QnACPU and other station access via the station installed in the E71 is conducted, the following parameters are set in the PLC CPU of the station installed in the E71 using the GX Developer.
- "Valid units during remote station access" setting
 Sets the unit value which exchange will be conducted for other station access in the settings and the number of unit settings.

3 The following shows the exchange possible PLC of the other stations in the data link system.

The exchange possible stations vary according to the stations with E71 installed.



PLC No. when using E71 commands

PLC Installed in the E71	Exchange Possible PLC and Subject Item Specification Values (Hexadecimal Numbers)									
	Local Station	M	L1	L2/m	L3	R4	ℓ 1	ℓ 2	r3	ℓ 4
M	FF	—	01	02	03	04 ^{*1}	x			
L1	FF	00	—	x						
L2/m	FF	00	x	—	x	01	02	03 ^{*1}	04	
ℓ 1	FF	x		00	x	—	x			

n All devices can be accessed by specifying the subject PLC's No.
 n^{*1} ... The special function unit' buffer memory can be accessed by specifying the subject PLC's No.
 x Access not possible

Point

Exchange cannot be done with A0J2CPUP23/R23 and A0J2P25/R25.

4

Transmission time when done via data link system

- (a) The following shows the transmission time (T1) when data transmission is conducted to a PLC in which an E71 is not installed in the data link system.

- Local station

$$\text{Transmission Time (T1)} = \frac{(\text{Transmission delay time A} + \text{E71 installed 1 station scan time}) \times (n+1)}{*1 \quad *2 \quad *3}$$

- Remote I/O station

$$\text{Transmission time (T1)} = \frac{(\text{Transmission delay time B} + \text{Master station 1 scan time}) \times (n+1)}{*1 \quad *2 \quad *3}$$

- *1 Refer to the explanation on the subject data link system transmission delay time in the data link system reference manual.

Transmission delay time A :Refer to the symbol column for the LRDP command processing time

Transmission delay time B :Refer to the symbol column for the RFRP command processing time

- *2
- When exchange is conducted for the first time to the subject system when the power is turned on or when the CPU reset has been conducted.
 - When exchange is conducted with a station other than the latest 10 stations to which exchange has been conducted.
 - When exchange is conducted for the second time when the number of exchange stations is under 10.
 - When exchange is conducted for the second time to the latest 10 stations to which exchange has been conducted.
- } n = 3
} n = 1

- *3 When the "CPU exchange timing setting" of the E71 exchange condition setting switch SW7 is off (write prohibited), it is added only when data is written from remote node.

- Reason for transmission time (T1) delay

When commands that require two scans (device write, etc. when the exchange condition setting switch (SW7: CPU exchange timing setting) is off, etc.) are executed, then the value is calculated using the above formula.

Refer to Item 9.3 2 for the required number of scans when a remote E71, GX Developer, etc.

- Lengthen the CPU monitoring timer's monitoring time from the other station when exchange is conducted with the other station via the data link system.

* Refer to the data link system reference manual for details regarding data links.

(Example) When the E71 is installed in the MELSECNET (II) master station, and the local station's device memory is read.

(Conditions $L < LS < M$, $M : 80\text{ms}$ $\alpha 1 : 10\text{ms}$)

Transmission Time (T1) = $(M \times 4 + \alpha 1 \times 4 + M) \times 1 = (80 \times 4 + 10 \times 4 + 80) \times 1 = 440$

T1 is 880ms.

- M : MELSECNET master station scan time
- $\alpha 1$: MELSECNET master station link refresh time
- LS : Link scanner time
- L : MELSECNET local station's scan time

Point

Depending on conditions, a considerable delay can occur in data transmission to the PLC in which an E71 is not installed on the MELSECNET.

The transmission delay time can be reduced by using only the E71 installed station (PLC No. FFH) for exchange with the PLC, and by using a data link (B, W) for exchange with the other station PLC CPU.

9.1.4 Exchanging with the PLC CPU in Mixed Systems

Reading and writing cannot be done to the following other station PLC.

- ① Other station PLC on data link systems via network systems.
- ② Other station PLC on network systems via data link systems.

9.2 List of E71 Commands and Functions

This section explains the commands and functions used to read and write data in the PLC CPU from remote nodes.

Functions			Command Response Types	Description of Processing	Number of Processes Performed For 1 Exchange	
Device memory ^{*4}	Batch read	Bit unit	00H	Bit devices (X, Y, M, etc.) are read in 1 point unit.	256 Points	
		Word unit	01H	Bit devices (X, Y, M, etc.) are read in 16 point units. Word devices (D, R, T, C, etc.) are written in 1 point unit. ^{*3}	128 Words (2048 Points)	
	Batch write	Bit unit	02H	Bit devices (X, Y, M, etc.) are written in 1 point unit.	256 Points	
		Word unit	03H	Bit devices (X, Y, M, etc.) are written in 16 point units. Word devices (D, R, T, C, etc.) are written in 1 point unit. ^{*3}	40 Words (640 Points) 256 Points	
	Test (Random write)	Bit unit	04H	Bit devices (X, Y, M, etc.) are set and reset in 1 point unit and the devices and device No. are randomly set.	80 Points	
		Word unit	05H	Bit devices (X, Y, M, etc.) are set and reset in 16 point units, and the devices and device No. are randomly set.	40 Words (640 Points)	
				Word devices (D, R, T, C, etc.) are written in 1 point units, and the devices and device No. are randomly set.	40 Points	
	Monitor data registration	Bit unit	06H	Bit devices that monitor (X, Y, M, etc.) are registered in 1 point unit.	40 Points ^{*2}	
		Word unit	07H	Bit devices that monitor (X, Y, M, etc.) are registered in 16 point units.	20 Words (320 Points) ^{*2}	
				Word devices that monitor (D, R, T, C, etc.) are registered in 1 point unit.	20 Points	
	Monitor	Bit unit	08H	Device monitors for which monitor data registration was conducted.	(Number of registrations portion)	
		Word unit	09H			
Extension file register	Batch read		17H	Extension file registers (R) are read in 1 point unit.	256 Points	
	Batch write		18H	Extension file registers (R) are written in 1 point unit.	256 Points	
	Test (Random write)		19H	Extension file registers (R) are written in 1 point unit and the block No. and device No. are randomly set.	40 Points	
	Monitor data registration		1AH	The extension file registers that monitor (R) are registered in 1 point unit.	20 Points	
	Monitor		1BH	Monitors the extension file registers (R) that conduct the monitor data registration.	—	
	Direct read		3BH	Reads in 1 point unit the extension file registers (R) that are directly set.	256 Points	
	Direct write		3CH	Reads in 1 point unit the extension file registers (R) that are directly set.	256 Points	
Special function Module	Batch read		0EH	Reads the contents of the special function module buffer memory.	256 Bytes(128 Words)	
	Batch write		0FH	Writes the data to the special function module buffer memory.	256 Bytes(128 Words)	

	Subject PLC CPUs that can Execute Commands																	PLC CPU State ^{*1}			Reference Item					
	A0J2	A0J2 H	A1, A1N	A2, A2N A2S (S1)	A3, A3N A1SH A1SJH A2SH (S1)	A3H, A3M	A2C, A2CJ	A1S, (S1) A1SJ	A2A (S1)	A3A	A2U, A2AS (S1)	A3U	A4U	AJ72 P25/ R25	QLP 25 LP25 / QBR 15 BR15	Q2A Q2AS Q2ASH (S1)	Q3A	Q4A Q4AR	During STOP	During RUN						
																				Write Possible Setting		Write not Possible Setting				
	x						○							x	○		○		○	○	○	Item 10.2.2				
	x						○							x	○		○						Item 10.2.3			
	x						○							x	○		○							Item 10.2.4		
	x						○							x	○		○		○	○	x	Item 10.2.5				
	x						○							x	○		○						Item 10.2.6			
	x						○							x	○		○							○	○	x
	x						○							x	○		○		Item 10.2.8							
	x						○							x	○		○			○	○	○	Item 10.2.8			
	x						○							x	○		○							○	○	○
	x						○							x	○		○		○							
	x	○	x				○									x				○	○	○	Item 10.3.3			
	x	○	x				○									x								○	○	x
	x	○	x				○									x			○							
	x	○	x				○									x				○	○	○	Item 10.3.6			
	x	○	x				○									x								○	○	○
	x	○	x				○									x			○							
	x																x			○	○	○	Item 10.4.2			
	x																x							○	○	x

Functions				Command Response Types	Description of Processing	Number of Processes Performed For 1 Exchange		
Sequence program	*6	Batch read	Main	0AH	Sequence program	Reads the main sequence program.	256 Steps	
			T/C set value		Reads the T/C set value used by the main sequence program.	256 Points		
		Sub	Main	0BH	Sequence program	Reads the sub sequence program.	256 Steps	
			T/C set value		Reads the T/C set value used by the sub sequence program.	256 Points		
	Batch write	Main	0CH	Sequence program	Writes the main sequence program.	256 Steps		
				T/C set value	Writes the T/C set value used by the main sequence program.	256 Points		
		Sub	0DH	Sequence program	Writes the sub sequence program.	256 Steps		
				T/C set value	Writes the T/C set value used by the sub sequence program.	256 Points		
Microcomputer program	Batch read	Main	1EH	Reads the main microcomputer program.	256 Bytes			
		Sub	1FH	Reads the sub microcomputer program.				
	Batch write	Main	20H	Writes the main microcomputer program.				
		Sub	21H	Writes the sub microcomputer program.				
Comment	Batch read		1CH		Reads the comment data.	256 Bytes		
	Batch write		1DH		Writes the comment data.			
Extension comment	Direct read		39H		Reads the extension comment data.	256 Bytes		
	Direct write		3AH		Writes the extension comment data.			
Parameter	*7	Batch read		10H		Reads the PLC CPU parameter contents.	256 Bytes	
		Batch write		11H		Writes the parameter contents to the PLC CPU.		
		Analysis request		12H		Recognizes and checks the overwritten parameter contents in the PLC CPU.	—	
PLC CPU	Remote RUN		13H		Requests a remote RUN/STOP of the PLC CPU.	—		
	Remote STOP		14H					
	PLC model read		15H				Reads whether the PLC CPU model is A1N, A2N, A3N, A3H...	
Loopback test				16H		The characters received from the remote node are returned to the remote node.	256 Bytes	

	Subject PLC CPUs that can Execute Commands																	PLC CPU State ^{*1}			Reference Item	
	A0J2	A0J2 H	A1, A1N	A2, A2N A2S (S1)	A3, A3N A1SH A1SJH A2SH (S1)	A3H, A3M	A2C, A2CJ	A1S, (S1) A1SJ	A2A (S1)	A3A	A2U, A2AS (S1)	A3U	A4U	AJ72 P25/ R25	QLP 25 LP25 / QBR 15 BR15	Q2A Q2AS Q2ASH (S1)	Q3A	Q4A Q4AR	During STOP	During RUN		
																				Write Possible Setting		Write not Possible Setting
	×	○											×	×		○	○	○	Item 10.6.4			
	×	○											×	×								
	×	×		○		×		○	×		○	×	×	○	×	×	○	○		○		
	×	×		○		×		○	×		○	×	×	○	×	×						
	×	○											×			○	○ ^{*5}	×				
	×	○											×			○	○	×				
	×	×		○		×		○	×		○	×	×	×	×	×	○	○ ^{*5}		×		
	×	×		○		×		○	×		○	×	×	×	×	×	○	○		×		
	×	○											×			○	○	○	Item 10.6.5			
	×	×		○		×					×	×		○	○ ^{*5}	×						
	×	○											×									
	×	×		○		×					×	×										
	×	○											×	×	×	○	○	○	Item 10.6.6			
	×	○											×	×	×	○	○	×				
	×	×						○		×	×	×	×	×	×	×	○	○	○	Item 10.6.7		
	×	×								○	×			×	×	○	○	×				
	×	○											×	×		○	○	○	Item 10.6.3			
	×	○											×	×		○	×	×				
	×	○											×	×		○	×	×				
	×	○											×	○		○	○	○				
	×	○											×	○		○	○	○	Item 10.5.2			
	×	○													○	○	○	○	Item 10.5.3			
	—	—													—	○	○	○	Item 10.7			

- *1 Use the E71's exchange condition setting switch (SW7: CPU exchange timing setting) to set whether it is possible to write to the PLC CPU during RUN.

SW7 = ON Write possible during RUN (Possible)

SW7 = OFF Write not possible during RUN (Forbidden)

- *2 For other than A3HCPU, AnA, AnU, QnACPU, QCPU, 2 points worth of points are processed for each point for device X (input).

When X is included in the set device, make it as follows

$$\begin{aligned} & ((\text{Number of specified points} \times 2) \\ & \quad + \text{number of other device set points}) \leq \text{number of points processed} \\ & \hspace{10em} \text{per one exchange} \end{aligned}$$

When only X is specified, the number of points that can be processed per exchange is one half the value shown in the table.

- *3 When reading or writing extension file registers, use the extension file register's special command.

- *4 The AnUCPU can be accessed using the AnACPU device range. Only devices with the same name as the devices existing in the AnACPU can be accessed in the Q/QnACPU using the AnACPU device range. (Except below)

The following Q/QnACPU devices cannot be accessed from remote node:

- Devices newly added to the Q/QnACPU

- Latch relay (L) and step relay (S)

* For the Q/QnACPU, the latch relay (L) and step relay (S) are separate devices from internal relays (M), but access will be made to internal relays when either one is specified.

- File register (R)

Q/QnACPU Accessible Devices (Accessible with E71 Commands)...When the Parameter Settings are the Default							
Classification	Device	Device No. (Settings range)	Decimal/ Hexadecimal Expression	Classification	Device	Device No. (Settings range)	Decimal/ Hexadecimal Expression
Internal user device	Input relay	X0 to X7FF	Hexadecimal expression	Internal user device	Timer	Contact point	TS0 to TS2047
	Output relay	Y0 to Y7FF				Coil	TC0 to TC2047
	Internal relay	M0 to M8191	Decimal expression			Current value	TN0 to TN2047
	Link relay	B0 to BFFF	Hexadecimal expression		Counter	Contact point	CS0 to CS1023
	Enunciator	F0 to F2047	Decimal expression			Coil	CC0 to CC1023
	Data register	D0 to D6143	Decimal expression			Current value	CN0 to CN1023
	Link register	W0 to WFFF	Hexadecimal expression	Internal system device	Special relay **1	M9000 to M9255	Decimal expression
					Special register **2	D9000 to D9255	

**1 Access for SM1000 to SM1255 is set at M9000 to M9255.

**2 Access for SD1000 to SD1255 is set at D9000 to D9255.

For the MELSECNET/10 remote I/O station, access can be made to B, W, X, Y, special relays M9000 to M9255 and special registers D9000 to D9255 within the device range of the AnACPU.

- *5 Conduct program write during RUN when all of the following conditions are met.

- ① The PLC CPU is A3, A3N, A3A, A3U, or A4U.
- ② The program is not a program that is running. (Shows a subprogram when the main program is running.)
- ③ The PLC CPU special relay is in the following states.
 - (a) M9050 (signal flow exchange point) OFF (A3CPU only)
 - (b) M9051 (CHG instruction execution prohibited) ON

- *6 Read/write cannot be conducted for the A4UCPU subprogram's sub 2 to sub 4.

- *7 The parameter capacity for AnUCPU is 3k byte + MELSECNET/10 parameter (max. 24k byte).

9.3 PLC CPU Operation during Data Exchange

This section explains the PLC CPU operation when reading and writing data to the PLC CPU is conducted.

1

PLC CPU scan time

Access to the E71 and PLC CPU is processed once for each request for each END processing when the PLC CPU is running when a request is received from the E71. Therefore, this will increase the scan time processing time. For information regarding the PLC CPU interrupt time required for communication between the E71 and the PLC CPU, refer to Appendix 3.

2

Simultaneous access to the PLC CPU

Only one request is processed for END processing by the PLC CPU.

When the same PLC CPU is accessed at the same time from a unit and the GX Developer, the access is made to wait until other processing is completed, so the number of scans required for the processing is increased.

*1 The following is the way to process multiple requests in one scan of the PLC CPU.

- 1) For An(N)CPU, AnACPU, AnUCPU, QnACPU

Providing a COM instruction in a sequence program enables multiple access requests to be processed within one scan.

However, the scan time increases by the COM instruction execution time.

- 2) For AnUCPU

By turning on the special relay M9029 (data exchange request batchprocessing), all the data exchange requests received from any modules during one scan are processed by the AnUCPU at the END processing of that scan.

- 3) For QnACPU

When set to the "PLC System Setting" of the QnACPU parameter of the station installed E71.

If the "PLC system settings" general data processing setting has been made, the QnACPU will process the requests for general data processing settings using END processing.

(Example)

If the "PLC system settings" general data processing setting is "4", a maximum of 4 access requests given by the modules and GX Developer during one scan are all processed by the QnACPU at the END processing of that scan.

9.4 Data Exchange Precautions

Following is a list of precaution items for when reading and writing data to the PLC CPU is conducted.

- 1** Conduct read/write when the E71's initial normal end signal (X19) and the open end signal (X10 to X17) of the connection to be used are turned on. If these signals are on, then it is possible to conduct read/write of data in the PLC CPU from the remote node regardless of whether the PLC program is valid.
- 2** When writing data when the PLC CPU is running, set the exchange condition setting switch (SW7: exchange timing setting) of the E71 to on.
- 3** When conducting a PLC CPU remote stop, use the data exchange function (Refer to Item 5.6) while the PLC CPU is stopped.
- 4** When the usage availability of the connection being opened is without procedure, reading and writing data to the PLC CPU cannot be conducted.
- 5** Changing the remote station PLC CPU to which data will be exchanged.
After the E71 is booted up, remote station PLC CPU information is read in and stored. To change the remote station PLC CPU to which data will be exchanged after the E71 is booted up, reboot the E71 after changing the PLC CPU's model name. (Local station PLC power reset/CPU reset)
- 6** When transmitting a command for reading/writing data in the PLC CPU, send the next command after the completion of the data communication for the transmission of the previous command.
- 7** Do not change data, program or remote control's RUN and STOP while operating with a PC connected to the special function module.
Please make sure that you have read this manual carefully or you may cause erroneous operation or failure.

10. WHEN CONDUCTING READ/WRITE OF DATA IN THE PLC CPU

This section explains the control method and data format used to conduct reading/writing from the remote node via the E71 for devices and program data in the PLC CPU.

10.1 Data Format

Following is shown the exchange data (command and response) data item order and contents when conducting exchange by reading/writing data in the PLC CPU between the E71 and a remote node.

As is shown below, the exchange data consists of a header and application data.

Header	Application data
--------	------------------

10.1.1 Format When Exchanging Using Binary Code

Following shows the command and response data item order when exchange binary code data for the application data portion of the exchange data when exchanging by reading/writing data in the PLC CPU.

1 Transmission/reception data order when exchanging using TCP/IP

(a) Order during command transmission

Header			Application data (*1)			
Ethernet	IP	TCP	Subheader	PLC No.	ACPU monitor timer (L) (H)	Text (command)
(14 bytes)	(20 bytes)	(20 bytes)	(1 byte)	(1 byte)	(2 bytes)	(Maximum 2044 bytes)

← Differs depending on the function →

(b) Order during response reception

Header			Application data (*1 *2)		
Ethernet	IP	TCP	Subheader	End code	Text (response)
(14 bytes)	(20 bytes)	(20 bytes)	(1 byte)	(1 byte)	

← Differs depending on the function →

2 Transmission/reception data order when exchanging using UDP/IP

(a) Order during command transmission

Header			Application data (*1)			
Ethernet	IP	UDP	Subheader	PLC No.	ACPU monitor timer (L) (H)	Text (command)
(14 bytes)	(20 bytes)	(8 bytes)	(1 byte)	(1 byte)	(2 bytes)	(Maximum 2044 bytes)

← Differs depending on the function →

(b) Order during response reception

Header			Application data (*1 *2)		
Ethernet	IP	UDP	Subheader	End code	Text (response)
(14 bytes)	(20 bytes)	(8 bytes)	(1 byte)	(1 byte)	

← Differs depending on the function →

*1 The data order for each function and the data order when the status is normal are shown in each function explanation item from Item 10.2 and later.

*2 The application data portion data order is as follows when the response end code is "5B_H" (fault end).

Header	Application data			
	Subheader	End code 5B _H	Error code	00 _H
	(1 byte)	(1 byte)	(1 byte)	(1 byte)

10.1.2 Format When Exchanging Using ASCII Code

Following shows the command and response data item order when exchange ASCII code data for the application data portion of the exchange data when exchanging by reading/writing data in the PLC CPU.

1 Transmission/reception data order when exchanging using TCP/IP

(a) Order during command transmission

Header			Application data (*1)				
Ethernet	IP	TCP	Subheader	PLC No.	ACPU monitor timer	Text (command)	
(H) (L)	(H) (L)	(H) (L)	(H) (L)	(H) (L)	(H) (L)		
(14 bytes)	(20 bytes)	(20 bytes)	(2 bytes)	(2 bytes)	(4 bytes)	(Maximum 2040 bytes)	

← Differs depending on the function →

(b) Order during response reception

Header			Application data (*1 *2)		
Ethernet	IP	TCP	Subheader	End code	Text (response)
(H) (L)	(H) (L)	(H) (L)	(H) (L)	(H) (L)	
(14 bytes)	(20 bytes)	(20 bytes)	(2 bytes)	(2 bytes)	

← Differs depending on the function →

2 Transmission/reception data order when exchanging using UDP/IP

(a) Order during command transmission

Header			Application data (*1)				
Ethernet	IP	UDP	Subheader	PLC No.	ACPU monitor timer	Text (command)	
(H) (L)	(H) (L)	(H) (L)	(H) (L)	(H) (L)	(L) (H)		
(14 bytes)	(20 bytes)	(8 bytes)	(2 bytes)	(2 bytes)	(4 bytes)	(Maximum 2040 bytes)	

← Differs depending on the function →

(b) Order during response reception

Header			Application data (*1 *2)		
Ethernet	IP	UDP	Subheader	End code	Text (response)
(H) (L)	(H) (L)	(H) (L)	(H) (L)	(H) (L)	
(14 bytes)	(20 bytes)	(8 bytes)	(2 bytes)	(2 bytes)	

← Differs depending on the function →

*1 The data order for each function and the data order when the status is normal are shown in each function explanation item from Item 10.2 and later.

*2 The application data portion data order is as follows when the response end code is "5" "B" (error end).

Header	Application data					
	Subheader	End code	Error code			
	(H) (L)	(H) (L)	(H) (L)	"5"	"B"	
	(2 bytes)	(2 bytes)	(2 bytes)	(2 bytes)	(2 bytes)	

10.1.3 Exchange Data Item Contents

The following shows the command and response data item contents when conducting exchange by reading/writing data in the PLC CPU.

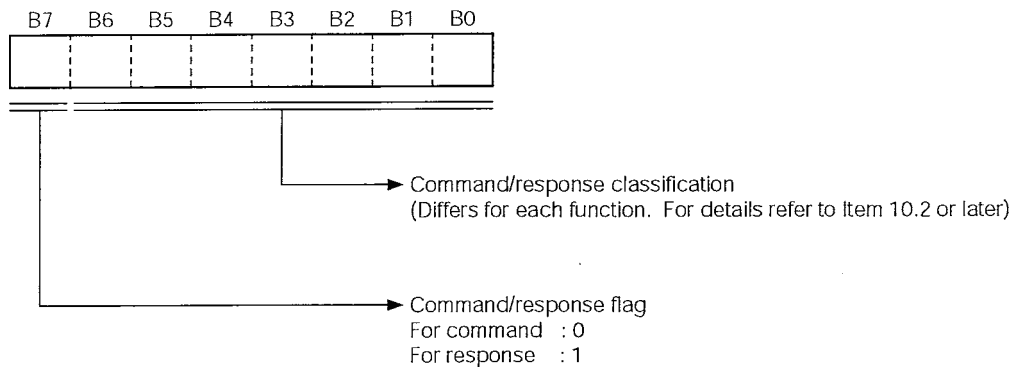
For the response returned to the remote node by the E71, the E71 automatically sets the data, so it is not necessary for the user to make the setting.

1 Header

The header is the header used by the TCP/IP or UDP/IP. For the E71, it is added or removed by the E71, so the user is not required to do the setting.

2 Subheader

The subheader format has the configuration shown below.



3 PLC No.

This shows for which PLC station the remote node conducts exchange by reading/writing data in the PLC CPU. Specify the target PLC station's PLC No. in accordance with Item 9.1.2

2 and Item 9.1.3 2.

- (a) When exchanging using binary code the PLC No. is shown using a binary value.
- (b) When exchanging using ASCII code the PLC No. is shown using ASCII code when expressed using a hexadecimal number.
- (c) An example specification is shown at the end of this item (remarks).

4 ACPU watchdog timer

To this timer, set the waiting time from when the E71 (that has received request data from an external device) output a read/write request to the PLC CPU until the result is returned.

- (a) Specify the setting with the following value.

0000H (0) : Endless waiting *1

0001 to FFFFH (1 to 65535) : Waiting time (unit: 250ms)

*1 The E71 keeps waiting until a response is returned from the PLC CPU.

- (b) To make normal data communication, it is recommended to use the timer in the setting range given in the following table depending on the communication destination.

Setting Range	Communication Destination
1 to 40H (0.25 to 10 seconds)	Host
2 to 240H (0.5 to 60 seconds)	Other station via MELSECNET/10 or other station by router relay

5 Test (command)

The E71 commands etc., that show the functions that can be used when a remote node reads/writes data in the PLC CPU in the target PLC station. The data contents and order for the text (command) portion differs depending on the functions used. The data order for each function is given in the function explanations from Item 10.2 and later.

6 Text (response)

This shows the read data/processing results etc., when a remote node reads/writes data in the PLC CPU in the target PLC station. The data contents and order of the text (response) portion varies depending on the functions used. The data order during normal end for each function is shown in the function explanations in Item 10.2 and later.

7 End code

The following values are used to show the processing results when a remote node reads/writes data in the PLC CPU to a target PLC station.

00H : Normal end

Other than 00H : Error end (01H to B001H)

- (a) When exchanging using binary code, the end code is shown as a binary value.
- (b) When exchanging using ASCII code, the end code is shown as an ASCII code when expressed as a hexadecimal number.
- (c) When an error end occurs, check the contents and take countermeasures in accordance with Chapter 13. When the end code is 5B_H/"5B", the error code (10H to 21H) data immediately following and 00_H/"00" are included.

8 Error code

This shows the error contents when the end code is 5B_H/"5B" when the processing result is an error when a remote node reads/writes data in the PLC CPU to the target PLC station. (Error code: 10H to 21H)

- (a) When exchanging using binary code, the fault code is shown as a binary value.
- (b) When exchanging using ASCII code, the fault code is shown as an ASCII code when expressed as a hexadecimal number.
- (c) Check the contents and conduct countermeasures in accordance with Chapter 13.

Point

The data code (ASCII, binary) used for transmission and reception of commands and responses between the E71 and a remote node are set using the exchange condition setting switch (SW2: Data code setting) of the E71.

Transmit the values handled by the items in the command and response by the E71 and the remote node to which exchange is being conducted using the following codes in accordance with the above settings. In addition, conduct reception using the following codes. In the explanations for items hereafter, the values handled by the items in the command and responses will be shown as binary values.

- (1) For Binary Code Exchange
Unless otherwise explained, the values shown in the explanations are the binary values and are transmitted and received in the specification order (L to H).
- (2) For ASCII Code Exchange
Unless especially explained, the values given in the explanations are converted into hexadecimal ASCII code and transmitted and received in the specification order (H to L).

Remarks

Following shows an example specification of the subheader to ACPU monitor timer when data is read/written in the PLC CPU under the following conditions.

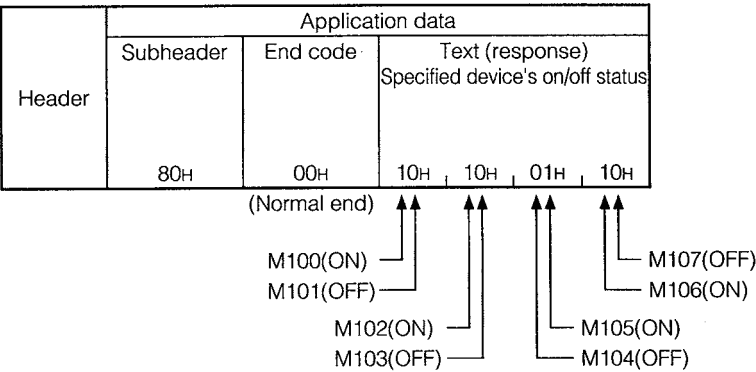
- Specified value
- Target station : PLC CPU station installed in the E71 (Local station)
..... FFH
 - Function used : Device memory batch read (Bit unit)
..... 00H (E71 command)
 - ACPU monitor timer value : 2500ms: 000AH

1 Format when exchanging using binary code

(a) Order during command transmission (Remote node → E71)

Header	Application data									
	Subheader	PLC No.	ACPU monitor timer		Text (command)					
			(L)	(H)	(Head device No.)		(Device name)		Number of device points	
					(L)	—	—	(H)	(L)	(H)
	00H	FFH	0AH	00H	64H	00H	00H	00H	20H	4DH
		(Local station)	(2500ms)		(100)			(M)	(8 points)	

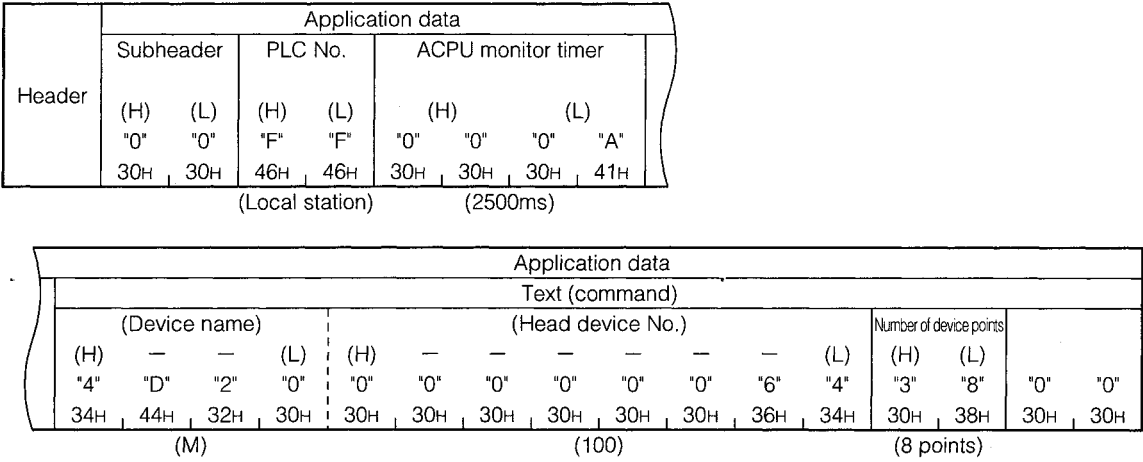
(b) Order during response reception (Remote node ← E71)



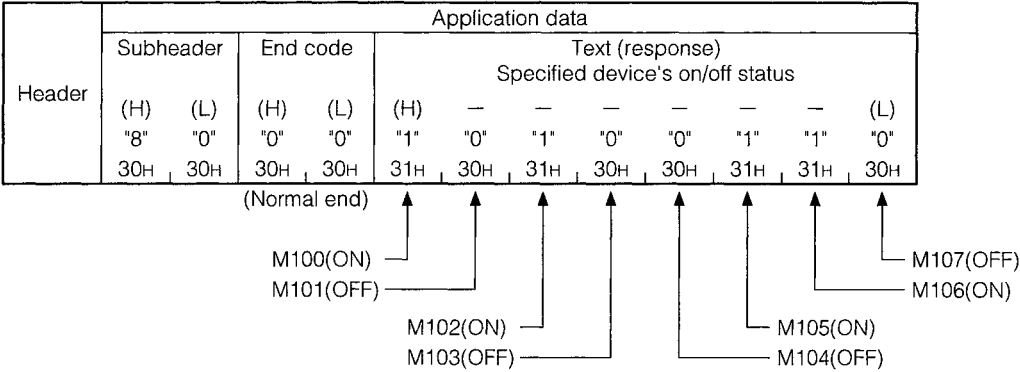
2

Format when exchanging using ASCII code

(a) Order during command transmission (Remote node → E71)



(b) Order during response reception (Remote node ← E71)



10.1.4 Thinking Regarding Transmission Data

This section explains the thinking regarding the transmission data that handles the character portions when data is transmitted and received between a remote node and the PLC using commands.

1 When Exchanging data using binary code

(a) When reading to and writing from bit device memories

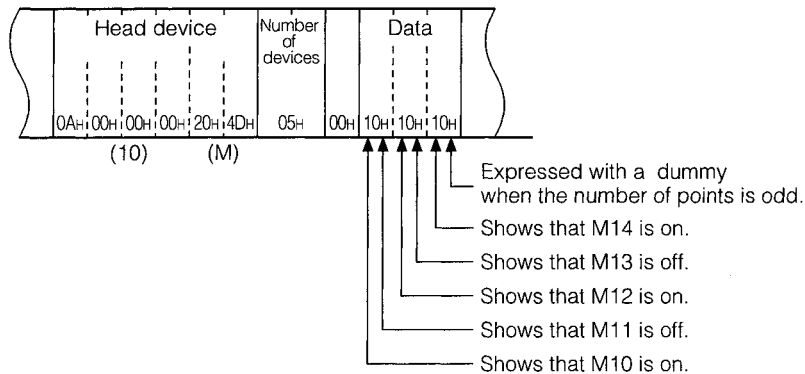
The bit device memory is sometimes handled in bit units (1 point units) and word units (16 points).

This section explains the thinking regarding these transmission data.

① Bit unit (1 point unit)

When bit device memory is handled in bit units, 1 point is specified as 4 bits and if the specified device number of points portions from the specified head device are turned on in the order from the first bit, then "1" is displayed and if off, then "0" is displayed.

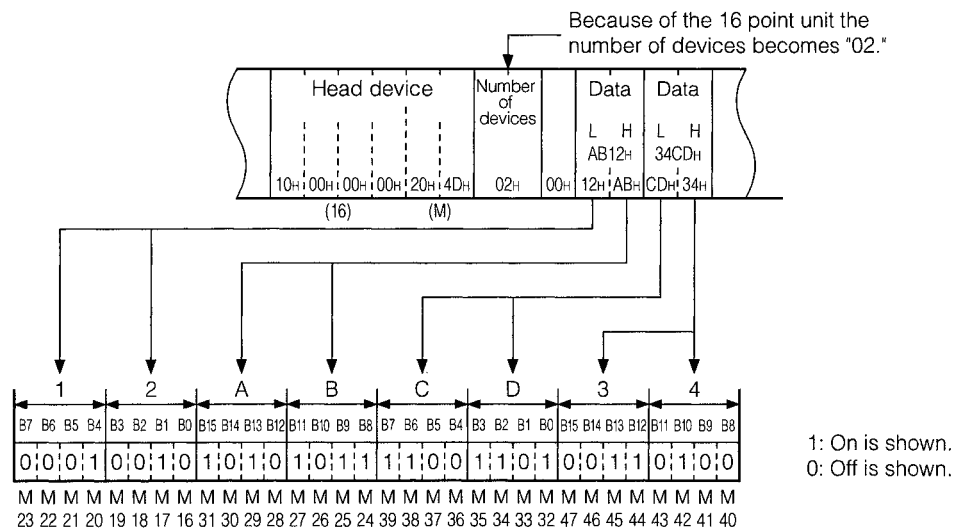
Example: When the 5 points from M10 are displayed in on/off.



② Word unit (16 point unit)

When the bit device memory is handled in 1 word units, 1 point is specified as 1 bit, and the specified device number of points from the specified head device is 16 point units, so the bits are expressed in the order from Low bytes (L: bits 0 to 7) to the High bytes (H: bits 8 to 15).

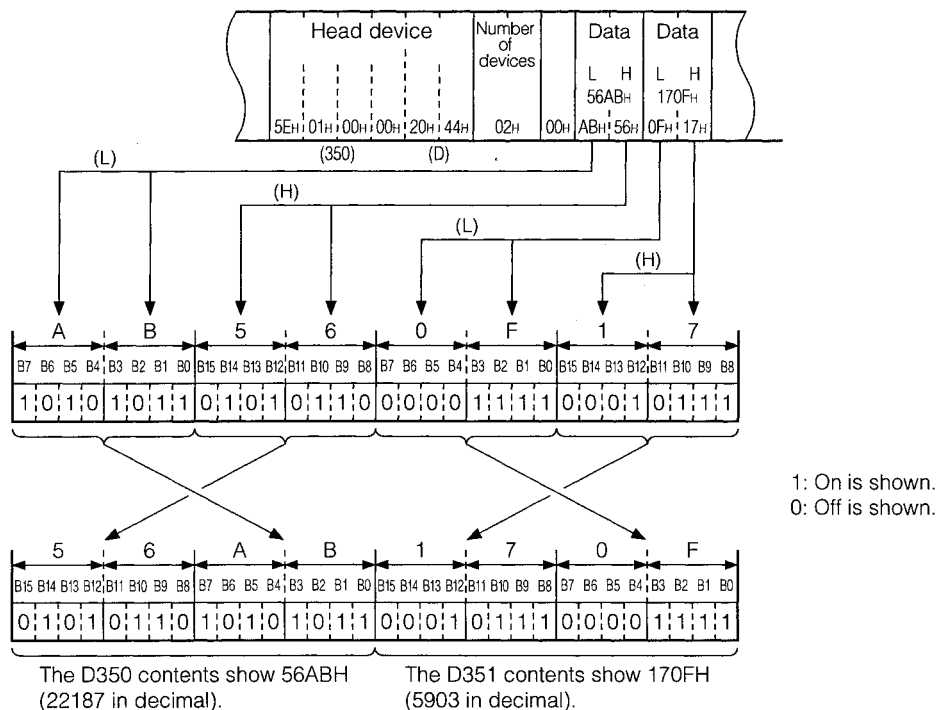
Example: When the 32 points from M16 are displayed in on/off.



(b) When reading to and writing from the word device memory

The word device memory is specified in one word as 16 bits, so the specified device number of bits from the specified head device is in 1 bit units, and the bits are displayed in the order from the Low bytes (L: bits 0 to 7) to the High bytes (H: bits 8 to 15).

Example: When the stored contents in data registers D350 and D351 are displayed.

**Point**

When other than integers (real numbers, character strings) are stored in the word device memory that will read the data, the E71 reads the stored values as integer values.

Example: When real numbers (0.75) are stored in D0 to D1, the following integer values are read.

D0 = 0000H, D1 = 3F40H

Example 2: When character strings ("12AB") are stored in D2 to D3, the following integer values are read.

D2 = 3231H, D3 = 4241H

Remarks

The same thinking as used for the word device memory also applies to word data that handles buffer memory reading and writing.

2 When exchanging data using ASCII code

(a) When reading to and writing from bit device memory

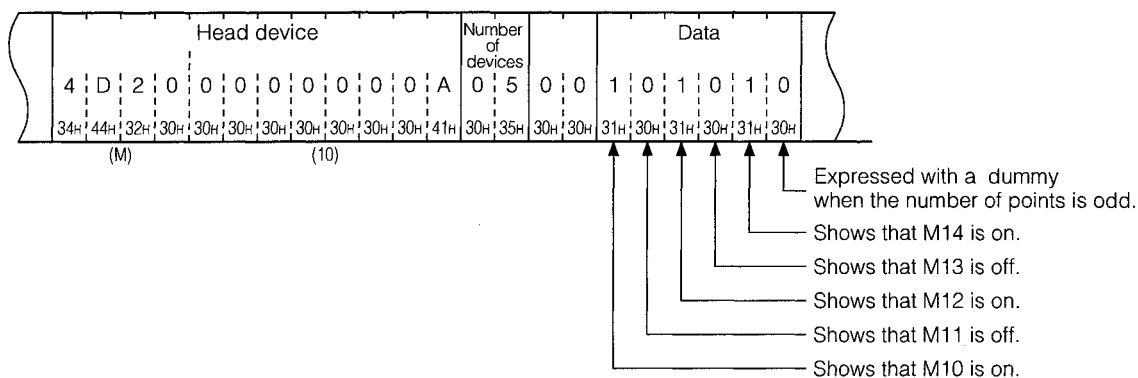
The bit device memory is sometimes handled in bit units (1 point units) and word units (16 points).

The following explains the thinking regarding the various transmission data.

① Bit unit (1 point unit)

When the bit device memory is handled in bit units, and if the specified device number of points portion from the specified head device are turned on in order from the left, then "1" (31H) is displayed and if off, then "0" (30H) is displayed.

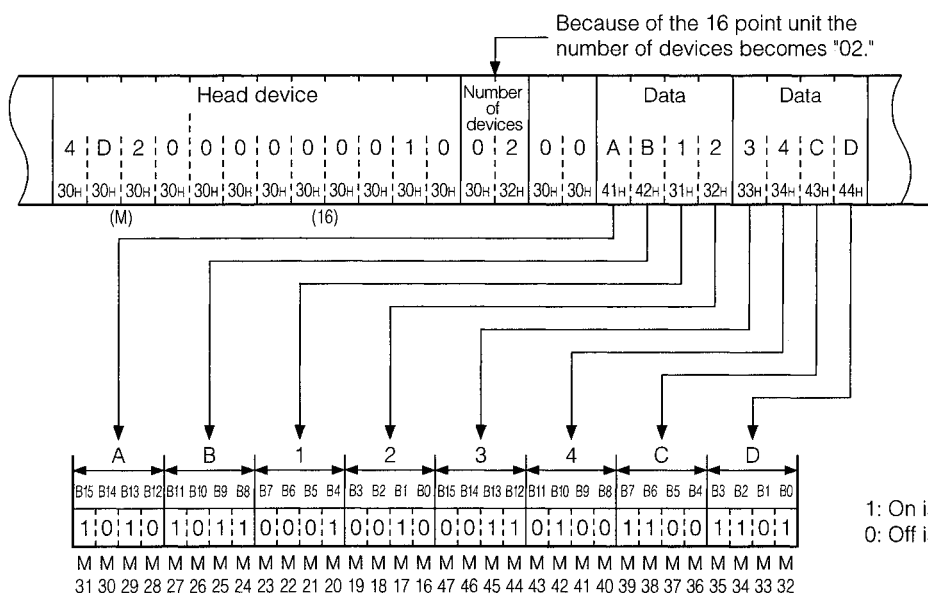
Example: When the 5 points from M10 are displayed in ON/OFF.



② Word units (16 point units)

When the bit device memory is handled in word units, one word is 4 bit units and the word is displayed in order from the first bit using hexadecimal numbers.

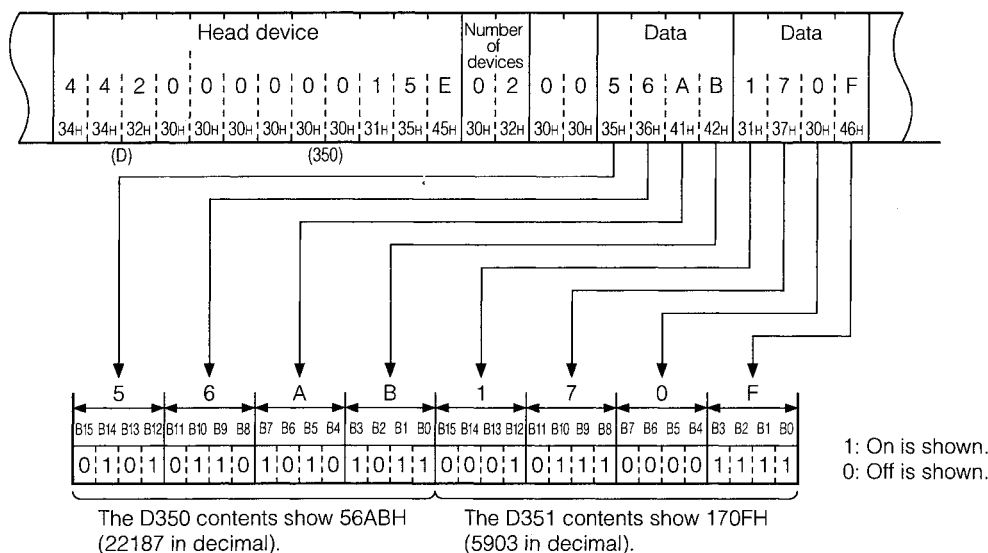
Example: When the 32 points from M16 are displayed in on/off.



(b) When reading to and writing from word device memory

The word device memory is one word of 4 bit units which are displayed from the first bit in order using hexadecimal numbers.

Example: When the stored contents in data registers D350 and D351 are displayed.

**Point**

- (1) Use the capital character codes when alpha characters are specified for the text.
- (2) When other than integers (real numbers, character strings) are stored in the word device memory that will read the data, the E71 reads the stored values as integer values.

Example: When real numbers (0.75) are stored in D0 to D1, the following integer values are read.

D0 = 0000H, D1 = 3F40H

Example 2: When character strings ("12AB") are stored in D2 to D3, the following integer values are read.

D2 = 3231H, D3 = 4241H

Remarks

The same thinking as used for the word device memory also applies to word data that handles buffer memory reading and writing.

10.2 Device Memory Read/Write

This section explains the control method for reading from and writing to the device memory.

10.2.1 Command and Device Range

1

The functions occurring in device memory read and write are shown in Table 10.1.

Table 10.1 Function list

Item		Command/ response classification	Processing description	Number of processing points con- ducted in one communication	PLC CPU status		
					Stopped	Running Write possible setting	Write impossible setting
Batch read	Bit unit	00H	Bit devices (X, Y, M, etc.) are read in 1 point units	256 points	○	○	○
	Word unit	01H	Bit devices (X, Y, M, etc.) are read in 16 point units	128 words (2048 points)			
			Word devices (D, R, T, C, etc.) are read in 1 points units	256 points			
Batch write	Bit unit	02H	Bit devices (X, Y, M, etc.) are written to in 1 point units	256 points	○	○	×
	Word unit	03H	Bit devices (X, Y, M, etc.) are written to in 16 point units	40 words (640 points)			
			Word devices (D, R, T, C, etc.) are read in 1 points units	256 points			
Test (random write)	Bit unit	04H	The device and device No. for bit devices (X, Y, M, etc.) are randomly specified as set/reset in 1 points units.	80 points	○	○	×
	Word unit	05H	The device and device No. for bit devices (X, Y, M, etc.) are randomly specified as set/reset in 16 points units.	40 words (640 points)			
			The device and device No. of word devices (D, R, T, C, etc.) are randomly specified as write in 1 point units.	40 points			
Monitor data registration	Bit unit	06H	The bit devices to be monitored (X, Y, M, etc.) are set in 1 point units.	40 points *	○	○	○
	Word unit	07H	The bit devices to be monitored (X, Y, M, etc.) are set in 16 point units.	20 words * (320 points)			
			The word devices to be monitored (D, R, T, C, etc.) are set in 1 point units.	20 points			
Monitor	Bit unit	08H	Monitors the device that conducted the monitor data registration.	(Number of registrations)	○	○	○
	Word unit	09H					

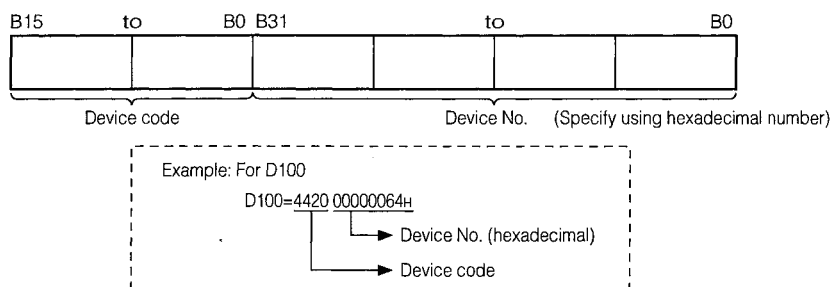
In the PLC CPU status column in the above table the "O" represents execution possible and the "X" represents execution not possible. The *number for when other than AnA, AnU, and QnA, 2 points are processed for each point for the device X (input). For example, when X is included in the specified device in monitor data registration bit units, make it so that

$$((X \text{ specified number of points}) \times 2 + \text{other device specified number of points}) \leq \text{number of points processed during one exchange}$$

2

Device specification method and range

- (a) The device setting method for device memory read/write is performed using the device code and device No. shown in the diagram below.



- (b) The device codes and device Nos. are shown in Table 10.2.

Table 10.2 Device List (CPU module without restrictions)

○ : Access enabled × : Access disabled — : No device

Device (*1)	Device code	Device range (*1)	Device No.	A1S, A1SH, A1SJ, A1SJH, A1, A1N	A2S, A2SH, A2, A2N, A2C, A2CJ, A0J2H	A2-S1, A2N-S1, A2SH-S1	A3, A3N	A2A	A2A-S1	A3A
Data register	D0 (44H, 20H)	D0 to D1023	0000H to 03FFH	○	—	○	—	—	○	—
		D1024 to D6143	0400H to 17FFH	—	—	—	—	—	○	—
		D9000 to D9255	2328H to 2427H	○	—	○	—	—	○	—
Link register	W0 (57H, 20H)	W0 to W3FF	0000H to 03FFH	○	—	○	—	—	○	—
		W400 to WFFF	0400H to 0FFFH	—	—	—	—	—	○	—
File register	R0 (52H, 20H)	R0 to R4095	0000H to 0FFFH	—	—	○	—	—	○	—
		R4096 to R8191	1000H to 1FFFFH	—	—	—	○	—	○	—
Timer	Current value	TN (54H, 4EH)	T0 to T255	○	—	○	—	—	○	—
			T256 to T2047	—	—	—	—	—	○	—
	Contact	TS (54H, 53H)	T0 to T255	○	—	○	—	—	○	—
			T256 to T2047	—	—	—	—	—	○	—
	Coil	TC (54H, 43H)	T0 to T255	○	—	○	—	—	○	—
			T256 to T2047	—	—	—	—	—	○	—
Counter	Current value	CN (43H, 4EH)	C0 to C255	○	—	○	—	—	○	—
			C256 to C1023	—	—	—	—	—	○	—
	Contact	CS (43H, 53H)	C0 to C255	○	—	○	—	—	○	—
			C256 to C1023	—	—	—	—	—	○	—
	Coil	CC (43H, 43H)	C0 to C255	○	—	○	—	—	○	—
			C256 to C1023	—	—	—	—	—	○	—
Input	X0 (58H, 20H)	X0 to X0FF	0000H to 00FFH	○	—	○	—	—	○	—
		X100 to X1FF	0100H to 01FFH	—	—	○	—	—	○	—
		X200 to X3FF	0200H to 03FFH	—	—	—	○	—	—	○
		X400 to X7FF	0400H to 07FFH	—	—	—	—	○	—	—
Output	Y0 (59H, 20H)	Y0 to Y0FF	0000H to 00FFH	○	—	○	—	—	○	—
		Y100 to Y1FF	0100H to 01FFH	—	—	○	—	—	○	—
		Y200 to Y3FF	0200H to 03FFH	—	—	—	○	—	—	○
		Y400 to Y7FF	0400H to 07FFH	—	—	—	—	○	—	—
Internal relay * Latch relay and step relay are included	M0 (4DH, 20H)	M0 to M2047	0000H to 07FFH	○	—	○	—	—	○	—
		M2048 to M8191	0800H to 1FFFFH	—	—	—	—	—	○	—
		M9000 to M9255	2328H to 2427H	○	—	○	—	—	○	—
Link relay	B0 (42H, 20H)	B0 to B3FF	0000H to 03FFH	○	—	○	—	—	○	—
		B400 to BFFF	0400H to 0FFFH	—	—	—	—	—	○	—
Annunciator	F0 (46H, 20H)	F0 to F255	0000H to 00FFH	○	—	○	—	—	○	—
		F256 to F2047	0100H to 07FFH	—	—	—	—	—	○	—

Table 10.2 Device List (CPU module with restrictions)

○ : Access enabled × : Access disabled — : No device

Device (*1)		Device code	Device range (*1)	Device No.	A2AS, A2U	A2AS-S1, A2U-S1	A3U, A4U	Q2A, Q2AS, Q2ASH	Q2A-S1, Q2AS-S1, Q2ASHS1	Q3A, Q4A, Q4AR
Data register		D0 (44 _H , 20 _H)	D0 to D6143	0000H to 17FFH	—	○	—	—	○	—
			D6144 to D8191	1800H to 1FFFH	—	×	—	—	×	—
			D8192 or more	2000H or more	—	—	—	—	×	—
			D9000 to D9255 (SD1000 to SD1255)	2328H to 2427H	—	○	—	—	○	—
			(SD1256 to SD2047)	—	—	—	—	—	×	—
Link register		W0 (57 _H , 20 _H)	W0 to WFFF	0000H to 0FFFH	—	○	—	—	○	—
			W1000 to W1FFF	1000H to 1FFFH	—	×	—	—	×	—
			W2000 or more	2000H or more	—	—	—	—	×	—
File register		R0 (52 _H , 20 _H)	R0 to R8191	0000H to 1FFFH	—	○	—	—	×	—
			R8192 or more	2000H or more	—	—	—	—	×	—
Timer	Current value	TN (54 _H , 4E _H)	T0 to T2047	0000H to 07FFH	—	○	—	—	○	—
			T2048 or more	0800H or more	—	—	—	—	×	—
	Contact	TS (54 _H , 53 _H)	T0 to T2047	0000H to 07FFH	—	○	—	—	○	—
			T2048 or more	0800H or more	—	—	—	—	×	—
	Coil	TC (54 _H , 43 _H)	T0 to T2047	0000H to 07FFH	—	○	—	—	○	—
			T2048 or more	0800H or more	—	—	—	—	×	—
Counter	Current value	CN (43 _H , 4E _H)	C0 to C1023	0000H to 03FFH	—	○	—	—	○	—
			C1024 or more	0400H or more	—	—	—	—	×	—
	Contact	CS (43 _H , 53 _H)	C0 to C1023	0000H to 03FFH	—	○	—	—	○	—
			C1024 or more	0400H or more	—	—	—	—	×	—
	Coil	CC (43 _H , 43 _H)	C0 to C1023	0000H to 03FFH	—	○	—	—	○	—
			C1024 or more	0400H or more	—	—	—	—	×	—
Input		X0 (58 _H , 20 _H)	X0 to X1FF	0000H to 01FFH	—	○	—	—	○	—
			X200 to X3FF	0200H to 03FFH	×	—	○	×	—	○
			X400 to X7FF	0400H to 07FFH	—	×	—	—	×	—
			X800 to X1FFF	0800H to 1FFFH	—	×	—	—	×	—
			X2000 or more	2000H or more	—	—	—	—	×	—
			Y0 to Y1FF	0000H to 01FFH	—	○	—	—	○	—
Output		Y0 (59 _H , 20 _H)	Y200 to Y3FF	0200H to 03FFH	×	—	○	×	—	○
			Y400 to Y7FF	0400H to 07FFH	—	×	—	—	×	—
			Y800 to Y1FFF	0800H to 1FFFH	—	×	—	—	×	—
			Y2000 or more	2000H or more	—	—	—	—	×	—
			Y0 to Y1FF	0000H to 01FFH	—	○	—	—	○	—
Internal relay		M0 (4D _H , 20 _H)	M0 to M8191	0000H to 1FFFH	* Including Latch relay (L)/Step relay (S).			—	○	—
			M8192 or more	2000H or more	—	—	—	—	×	—
			M9000 to M9255 (SM1000 to SM1255)	2328H to 2427H	—	○	—	—	○	—
			(SM1256 to SM2047)	—	—	—	—	—	×	—
Latch relay					(Depends on the above)			* Access to Internal relay (M) even if Latch relay (L) is specified.		
Step relay					(Depends on the above)			* Access to Internal relay (M) even if Step relay (S) is specified.		
Link relay		B0 (42 _H , 20 _H)	B0 to BFFF	0000H to 0FFFH	—	○	—	—	○	—
			B1000 to B1FFF	1000H to 1FFFH	—	×	—	—	×	—
			B2000 or more	2000H or more	—	—	—	—	×	—
Annunciator		F0 (46 _H , 20 _H)	F0 to F2047	0000H to 07FFH	—	○	—	—	○	—
			F2048 or more	0800H or more	—	—	—	—	×	—

*1 Refer to Item 9.2*4 for precaution items when reading/writing to and from the QnACPU.

Point

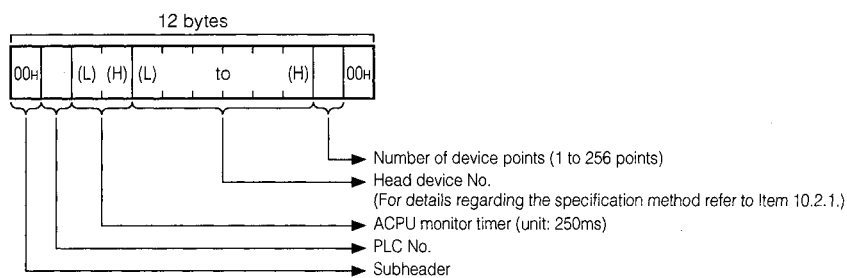
- (1) The bit device and word device classifications are as follows.
Bit device X, Y, M, L, B, F, T (contact), T (coil), C (contact), C (coil)
Word device T (current value), C (current value), D, W, R
- (2) Be sure to use device Nos. that are in multiples of 16 for the bit device's device No. for word unit specification.
- (3) The special relay's (M9000 to M9255) and special registers (D9000 to D9255), are for special read, special write, and system use. Conducting a write in areas outside of the write possible range will cause a PLC CPU error. For details regarding the special relay and the special register, refer to the ACPU programming manual.
- (4) When reading/writing file registers to and from the PLC CPU that uses extension file registers, use the commands explained in "Item 10.3 Extension File Register Reading/Writing." When using the extension file registers, there are times when correct reading and writing cannot be done when processing file registers using device batch read and write.
- (5) Reading/writing to the AnUCPU and QnACPU can be done in the AnACPU device range.

10.2.2 Bit Unit Batch Read

This section explains the command/response format when conducting bit device memory batch read.

1 When exchanging using binary code

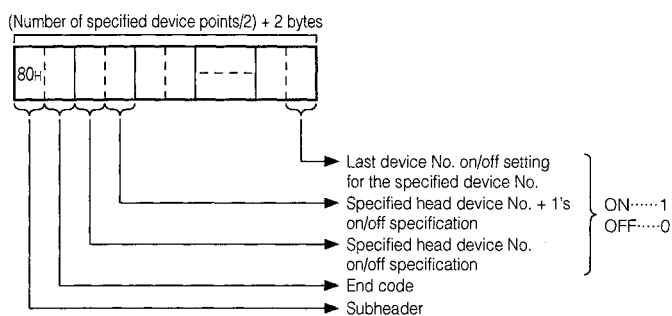
Command format



Remarks

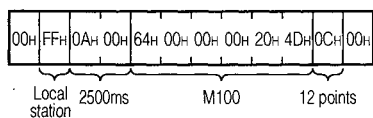
Set to "00H" when specifying the number of device points as 256 points.

Response format

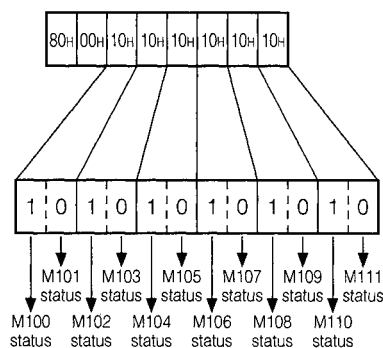


Example: When reading the M100 to M111 on/off status of the PLC CPU installed in E71.

Command (remote node → E71)



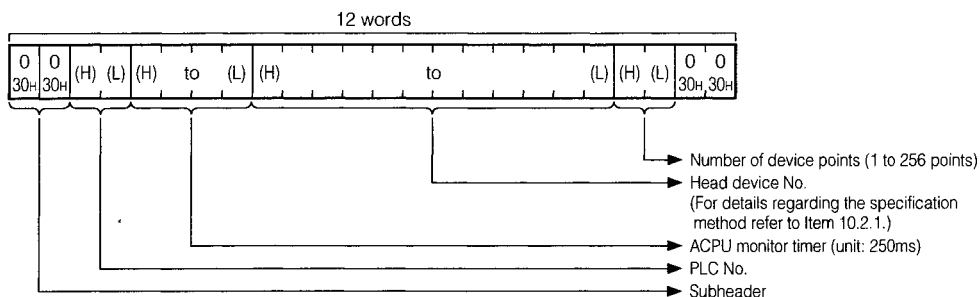
Response (E71 → remote node)



2

When exchanging using ASCII code

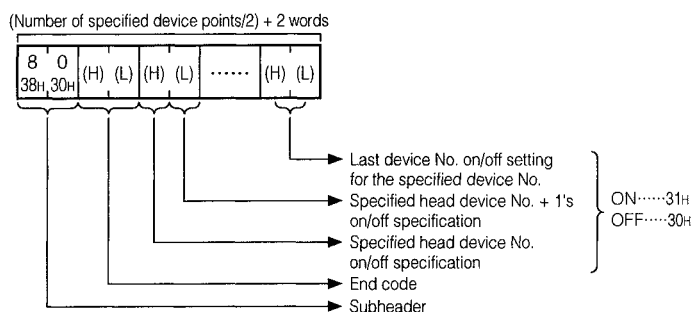
Command format



Remarks

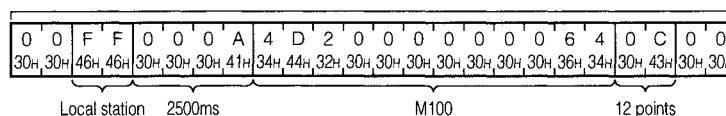
- (1) Set to "3030H" when specifying the number of device points as 256 points.
- (2) 1 byte of dummy data (30H) is added to the response data when the number of specified device points is odd.
 For example, 4 points worth of data is returned when 3 points are read. The last 1 byte is the dummy data.

Response format

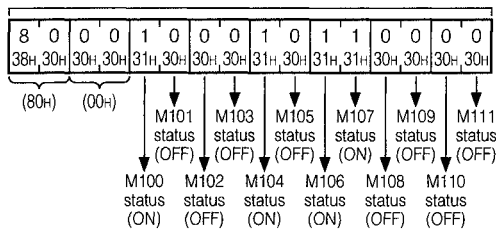


Example: When reading the M100 to M111 on/off status of the PLC CPU installed in E71.

Command (remote node → E71)



Response (E71 → remote node)

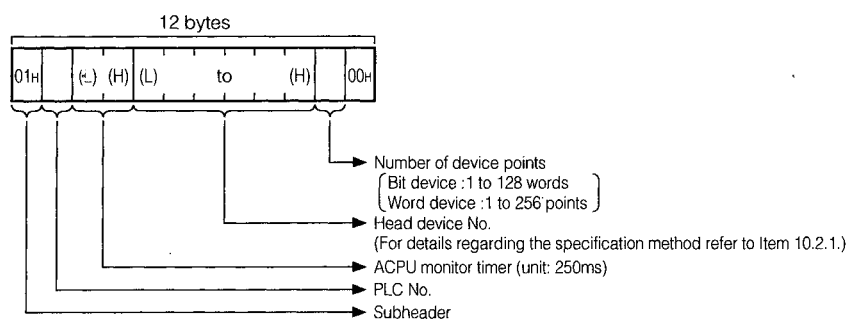


10.2.3 Word Unit Batch Read

This section explains the command/response format when conducting word device memory batch read and bit device memory (16 unit) batch read.

1 When exchanging using binary code

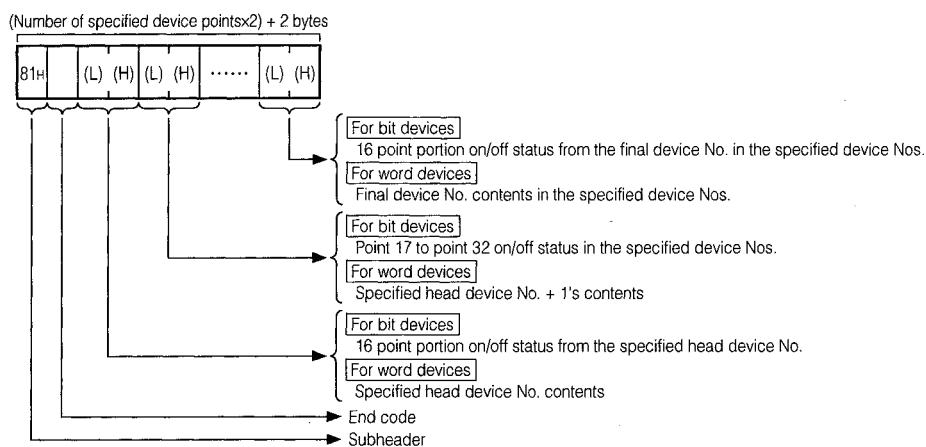
Command format



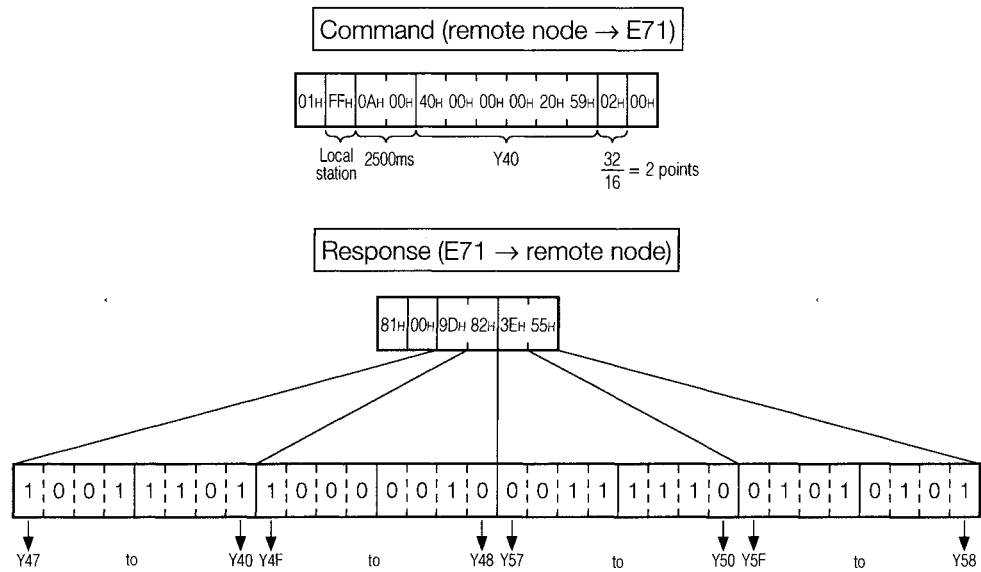
Remarks

Set to "00H" when specifying the number of device points as 256 points.

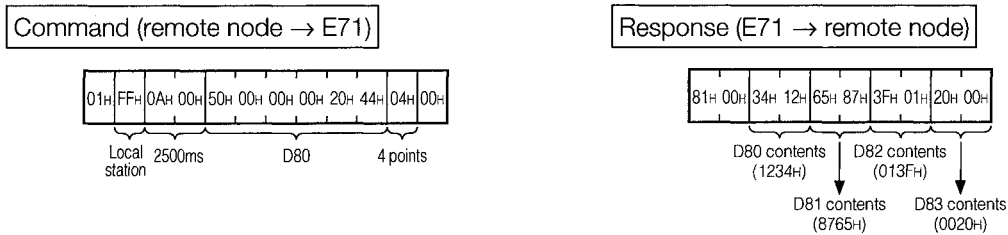
Response format



Example 1: When reading the Y40 to 5F (32 points) on/off status of the PLC CPU installed in E71.

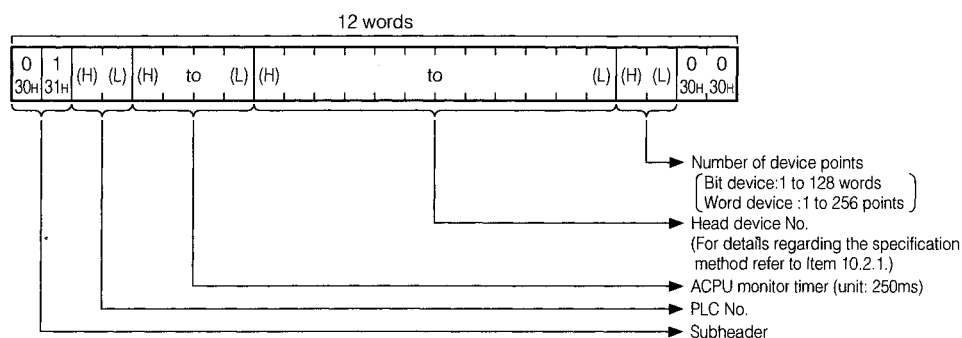


Example 2: When reading the D80 to 83 contents of the PLC CPU installed in E71.



2 When exchanging using ASCII code

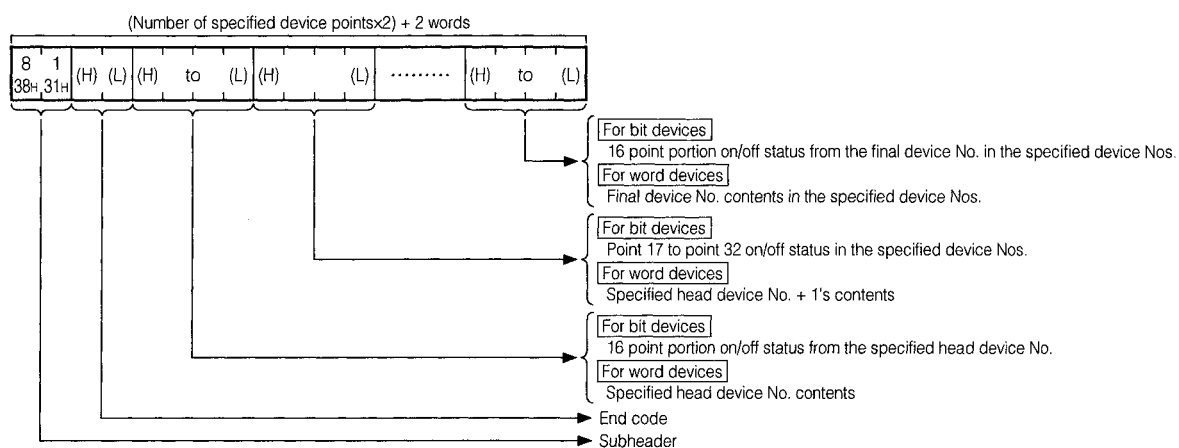
Command format



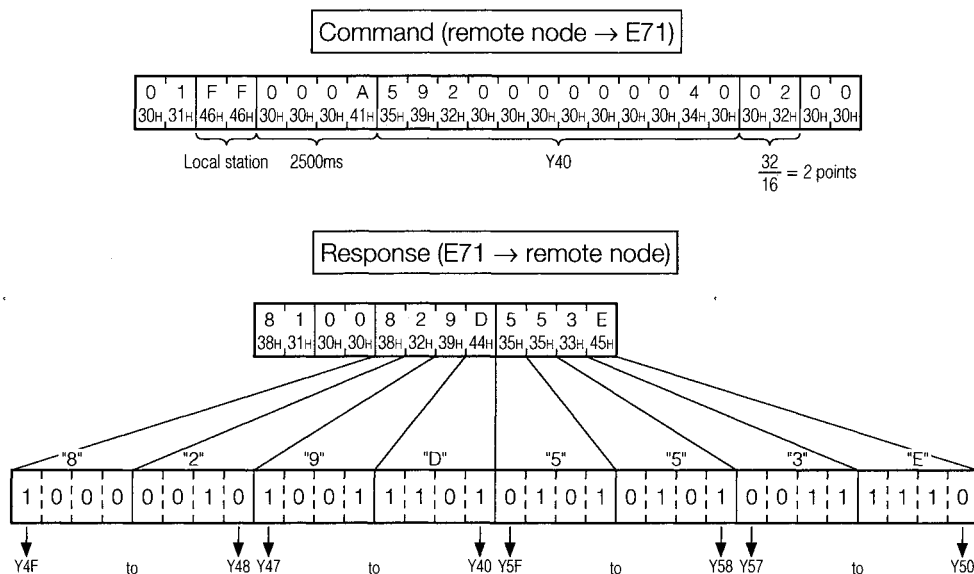
Remarks

Set to "3030H" when specifying the number of device points as 256 points.

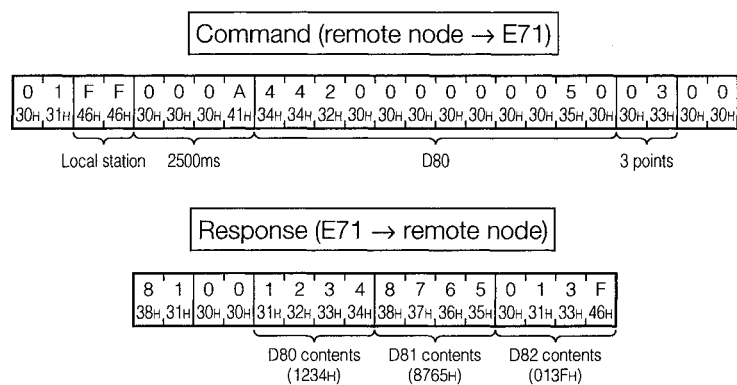
Response format



Example 1: When reading the Y40 to 5F (32 points) on/off status of the PLC CPU installed in E71.



Example 2: When reading the D80 to D82 contents of the PLC CPU installed in E71.

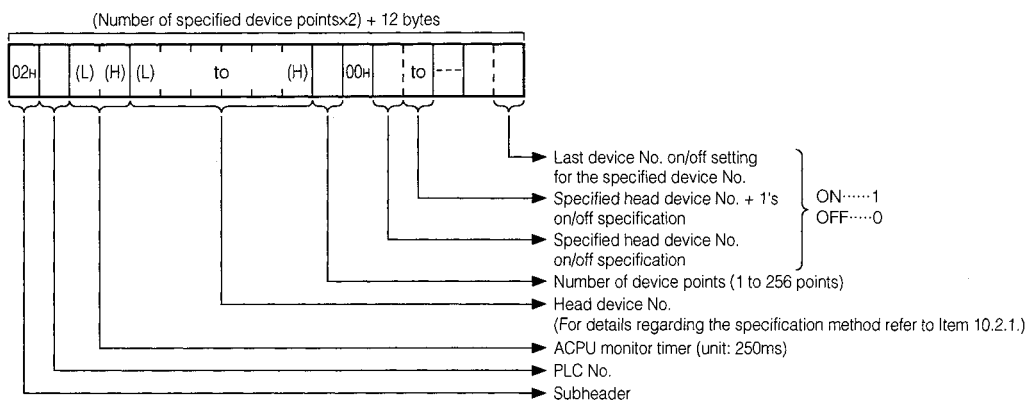


10.2.4 Bit Unit Batch Write

This section explains the command/response format when conducting bit device memory batch write.

1 When exchanging using binary code

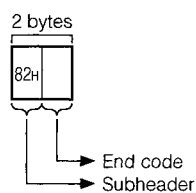
Command format



Remarks

Set to "00H" when specifying the number of device points as 256 points.

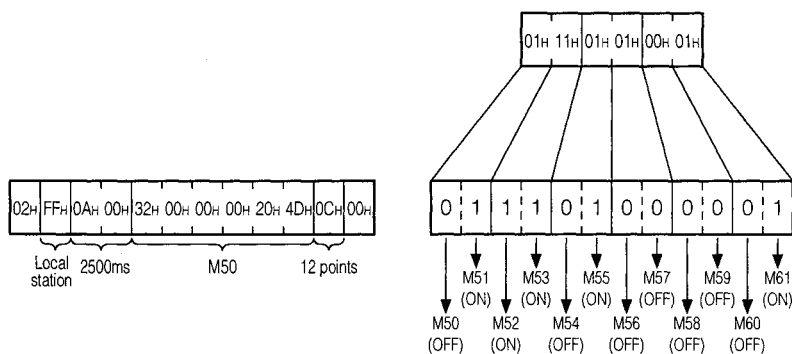
Response format



Example: When reading the M50 to M61 on/off status of the PLC CPU installed in E71.

Command (remote node → E71)

Response (E71 → remote node)



2

When exchanging using ASCII code

Command format

(Number of specified device points×2) + 12 words

0	2	(H)	(L)	(H)	to	(L)	(H)		to	(L)	(H)	(L)	0	0		
30H	32H												30H	30H			

→ Last device No. on/off setting for the specified device No.

→ Specified head device No. + 1's on/off specification

→ Specified head device No. on/off specification

→ Number of device points (1 to 256 points)

→ Head device No.
(For details regarding the specification method refer to Item 10.2.1.)

→ ACPU monitor timer (unit: 250ms)

→ PLC No.

→ Subheader

ON.....31H

OFF.....30H

Remarks

(1) Set to "3030H" when specifying the number of device points as 256 points.

(2) 1 byte of dummy data (30H) is added to the end of the write data when the number of specified device points is odd. For example, when 3 points are read the dummy data (30H) is added to the end.

Response format

2 words

8	2	(H)	(L)
38H	32H		

→ End code

→ Subheader

Example: When writing the M50 to M61 on/off data to the PLC CPU installed in E71.

Command (remote node → E71)

0	2	F	F	0	0	0	A	4	D	2	0	0	0	0	0	0	0	3	2	0	C	0	0	0	1	1	0	1						
30H	32H	46H	46H	30H	30H	30H	41H	34H	44H	32H	30H	30H	30H	30H	30H	30H	30H	33H	32H	30H	43H	30H	30H	30H	31H	31H		30H	31H						
		Local station		2500ms				M50												12 points						M51 (ON)		M52 (OFF)				M60 (OFF)		M61 (ON)	

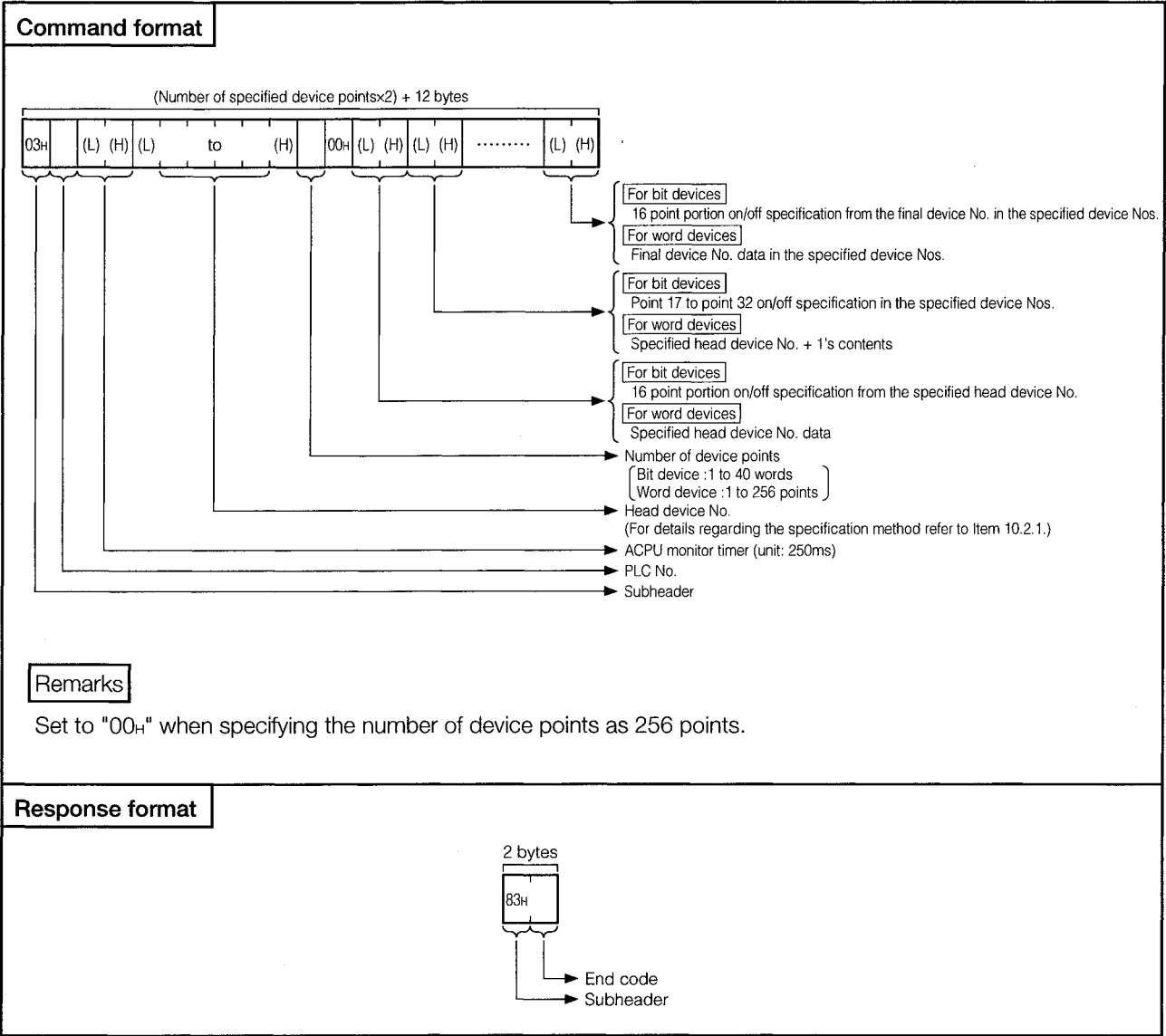
Response (E71 → remote node)

8	2	0	0
38H	32H	30H	30H

10.2.5 Word Unit Batch Write

This section explains the command/response format when conducting word device memory batch write and bit device memory (16 unit) batch write.

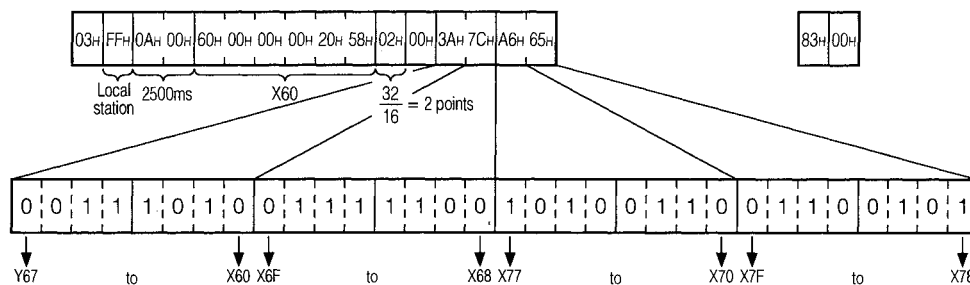
1 When exchanging using binary code



Example 1: When writing the X60 to 7F (32 points) on/off data to the PLC CPU installed in E71.

Command (remote node → E71)

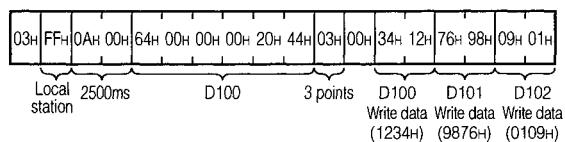
Response (E71 → remote node)



Example 2: When writing the D100 to 102 data to the PLC CPU installed in E71.

Command (remote node → E71)

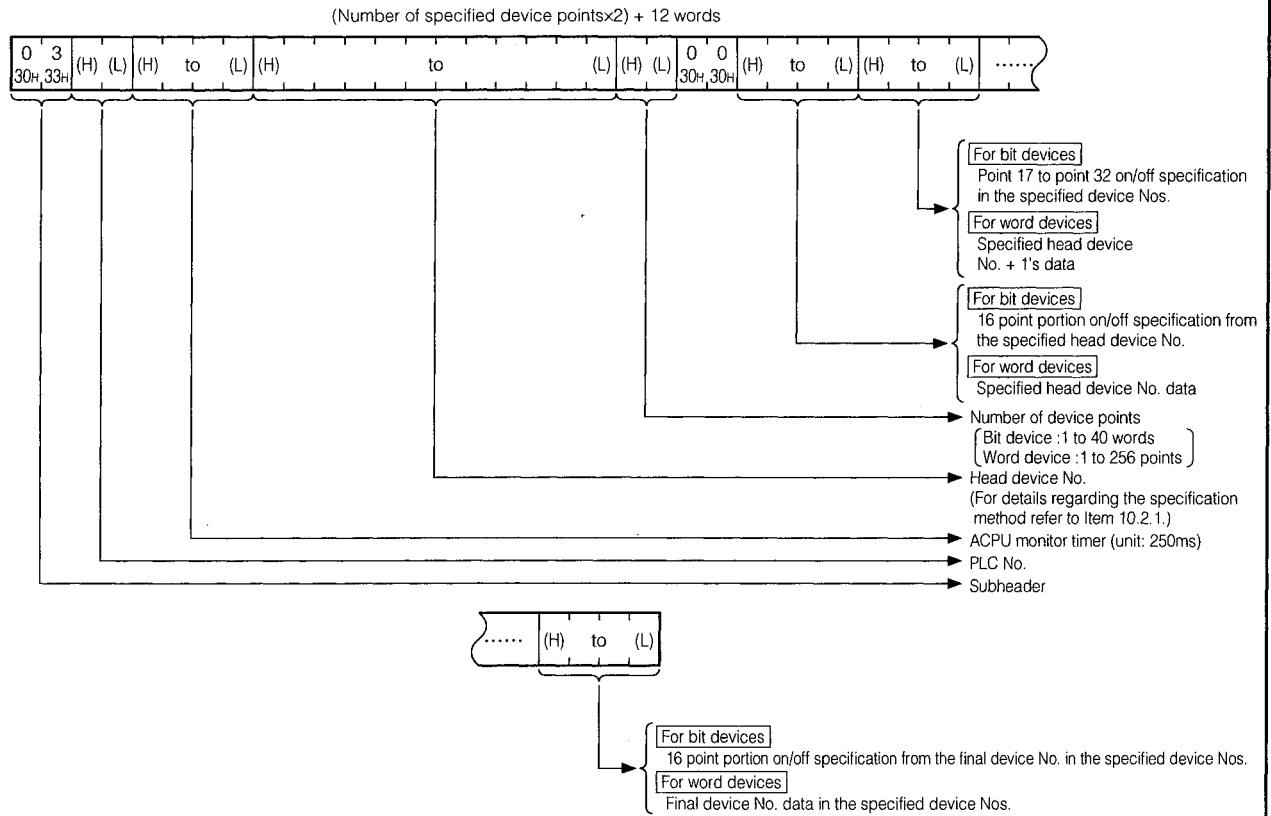
Response (E71 → remote node)



2

When exchanging using ASCII code

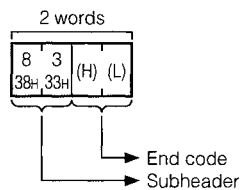
Command format



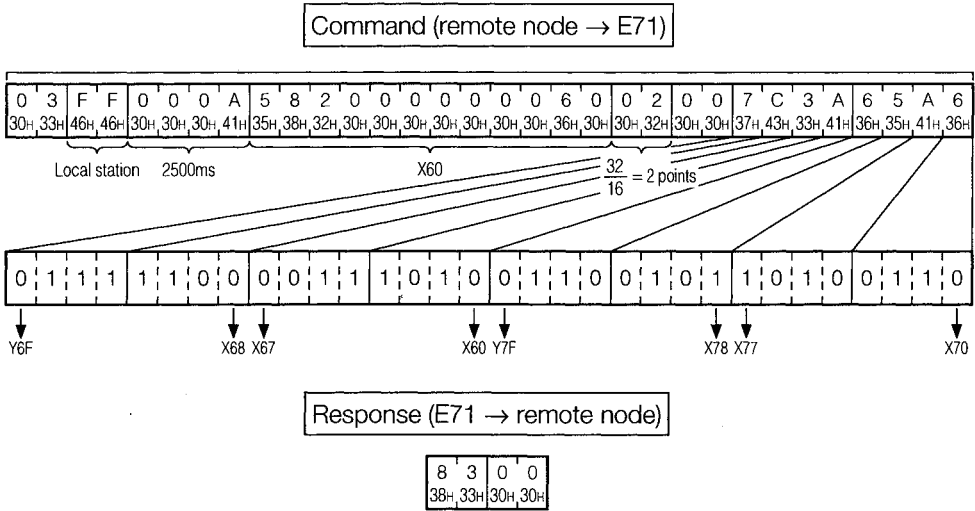
Remarks

Set to "3030H" when specifying the number of device points as 256 points.

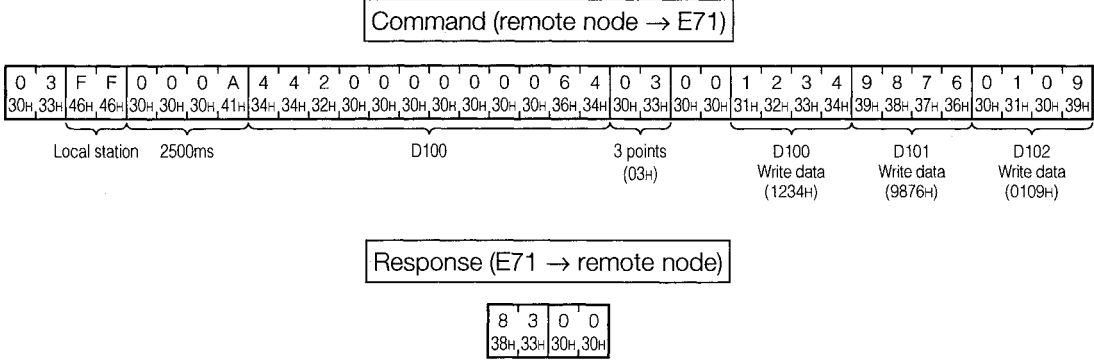
Response format



Example 1: When writing the X60 to 7F (32 points) on/off data to the PLC CPU installed in E71.



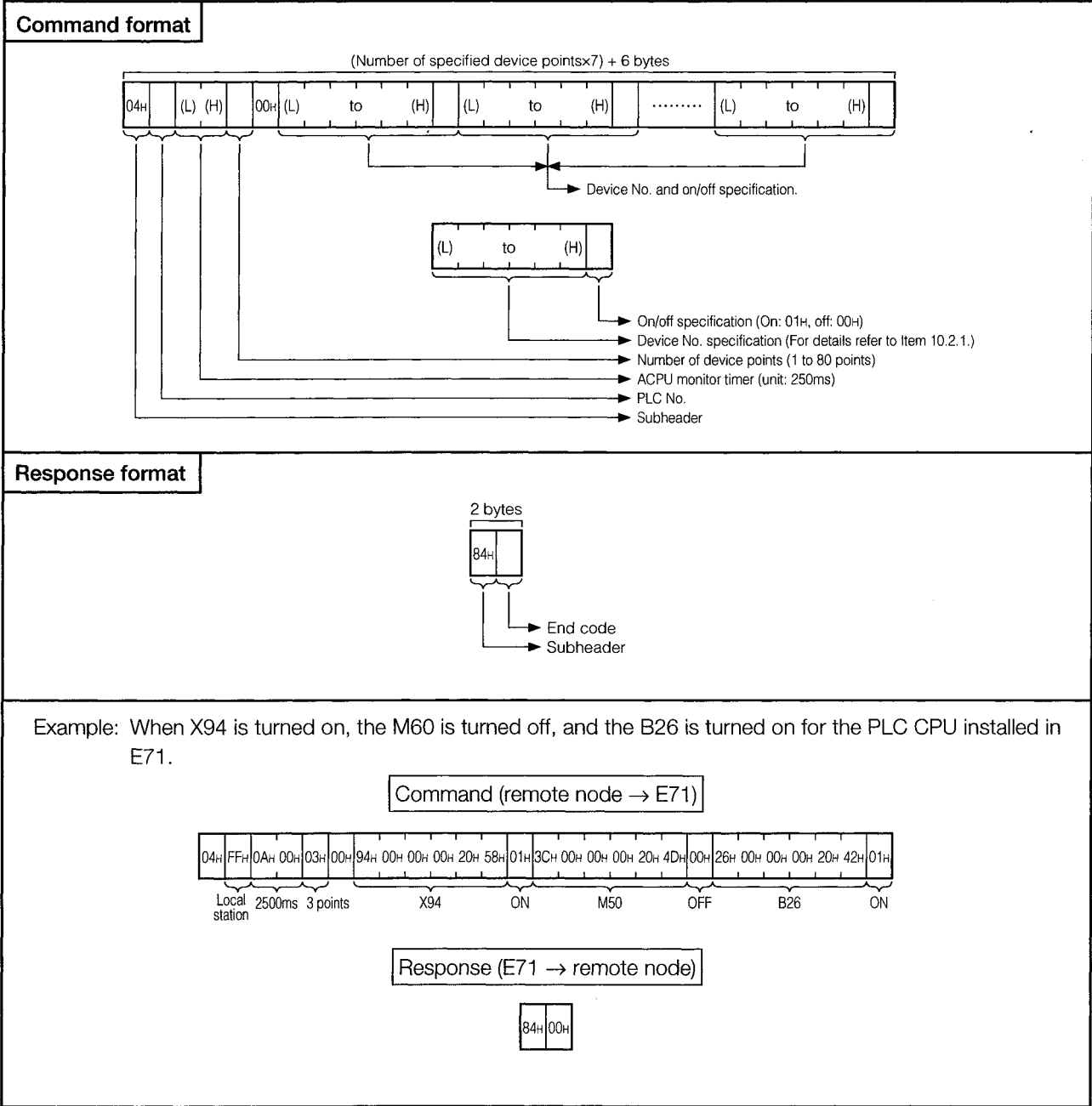
Example 2: When writing the D100 to 102 data to the PLC CPU installed in E71.



10.2.6 Bit Unit Test (Random Write)

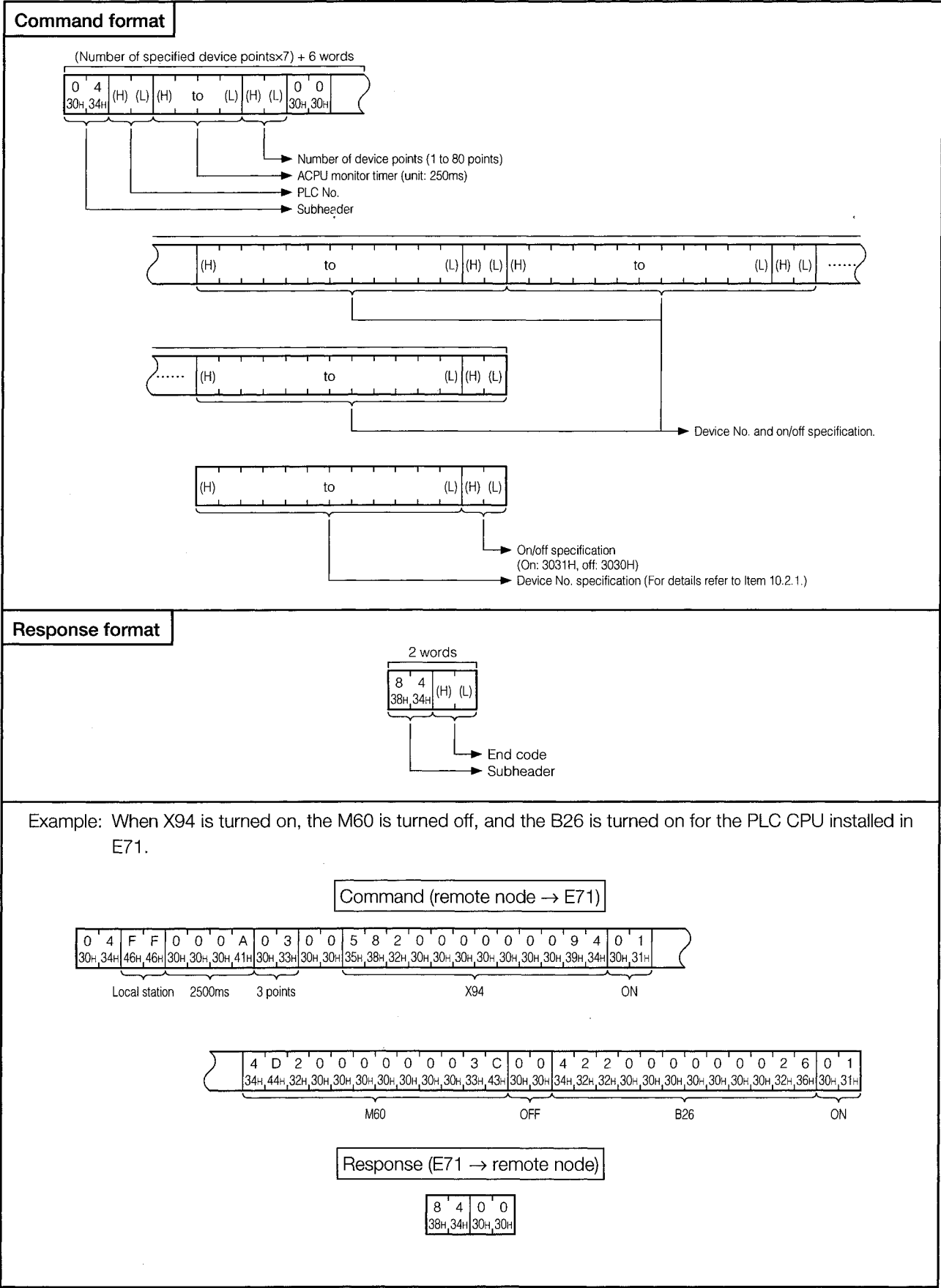
This section explains the command/response format when conducting a random write to a bit device memory.

1 When exchanging using binary code



2

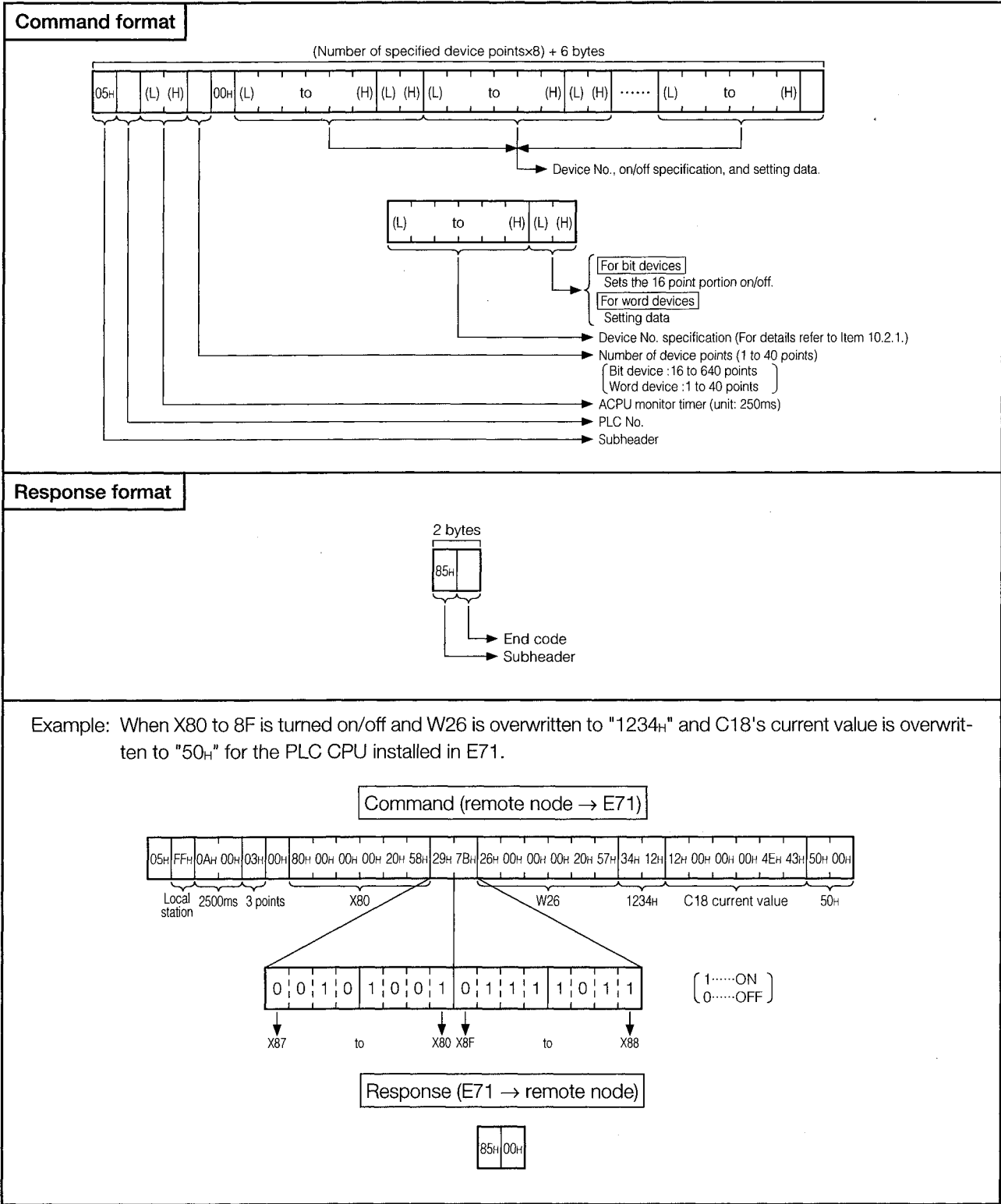
When exchanging using ASCII code



10.2.7 Word Unit Test (Random Write)

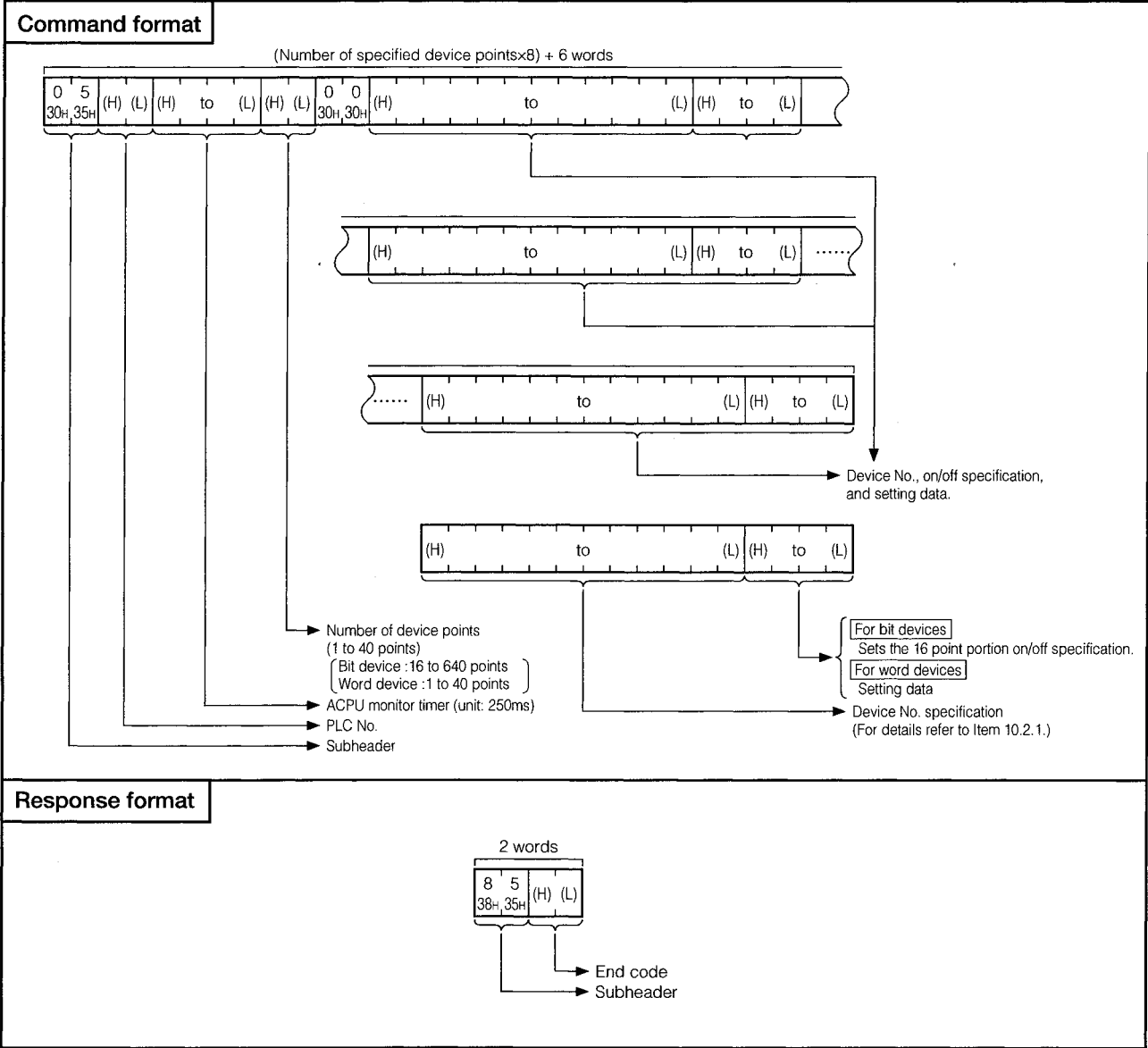
This section explains the command/response format when conducting a random write to a word device memory and bit device memory (16 point unit).

1 When exchanging using binary code



2

When exchanging using ASCII code



10.2.8 Device Memory Monitor

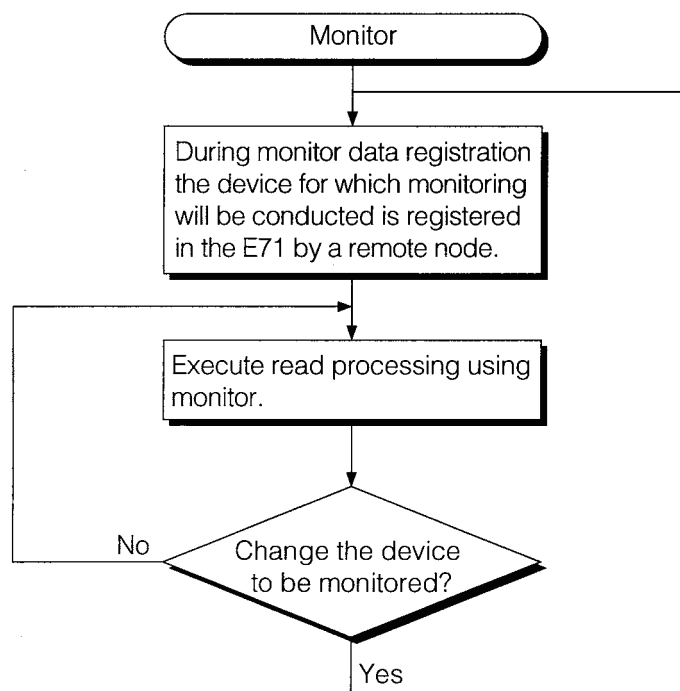
The device No. (device No. registered in the E71) on/off status and the contents can be monitored by a remote node by registering beforehand the device and device No. that you want to monitor with a remote node in the E71 and then executing a monitor instruction from the remote node.

Reading using device memory batch read can be processed in continuous device No., but by reading using the monitor it is possible to randomly specify a free device and No. and conduct the reading.

1

Monitor operation procedure

The operation procedure when conducting monitor is shown below.



Point

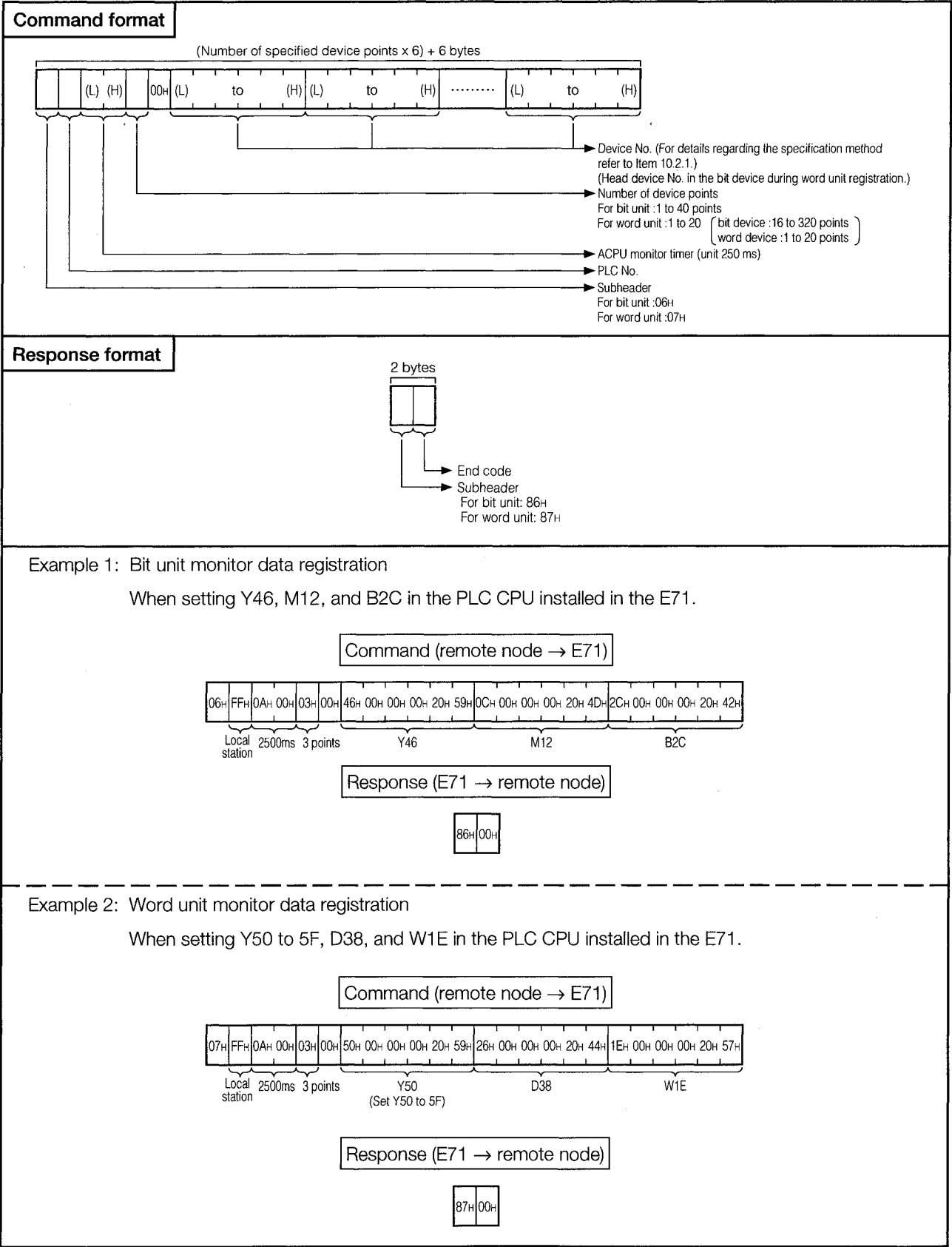
- (1) In operation procedures like that above where monitoring will be executed, the monitor data registration operation must be conducted. If monitoring is executed without conducting monitor data registration, an error (End code 57H) will occur.
- (2) The monitor data registration contents will be erased if the power is turned off or the PLC CPU is reset.
- (3) The 3 types of monitor data registration, device memory bit unit, word unit, and extension file register can be registered in the E71.
- (4) When monitor data registration is performed from multiple remote nodes to the device memory of the PLC CPU on the same station, the registration data will be overwritten. Thus, the device memory last registered will become effective.

2

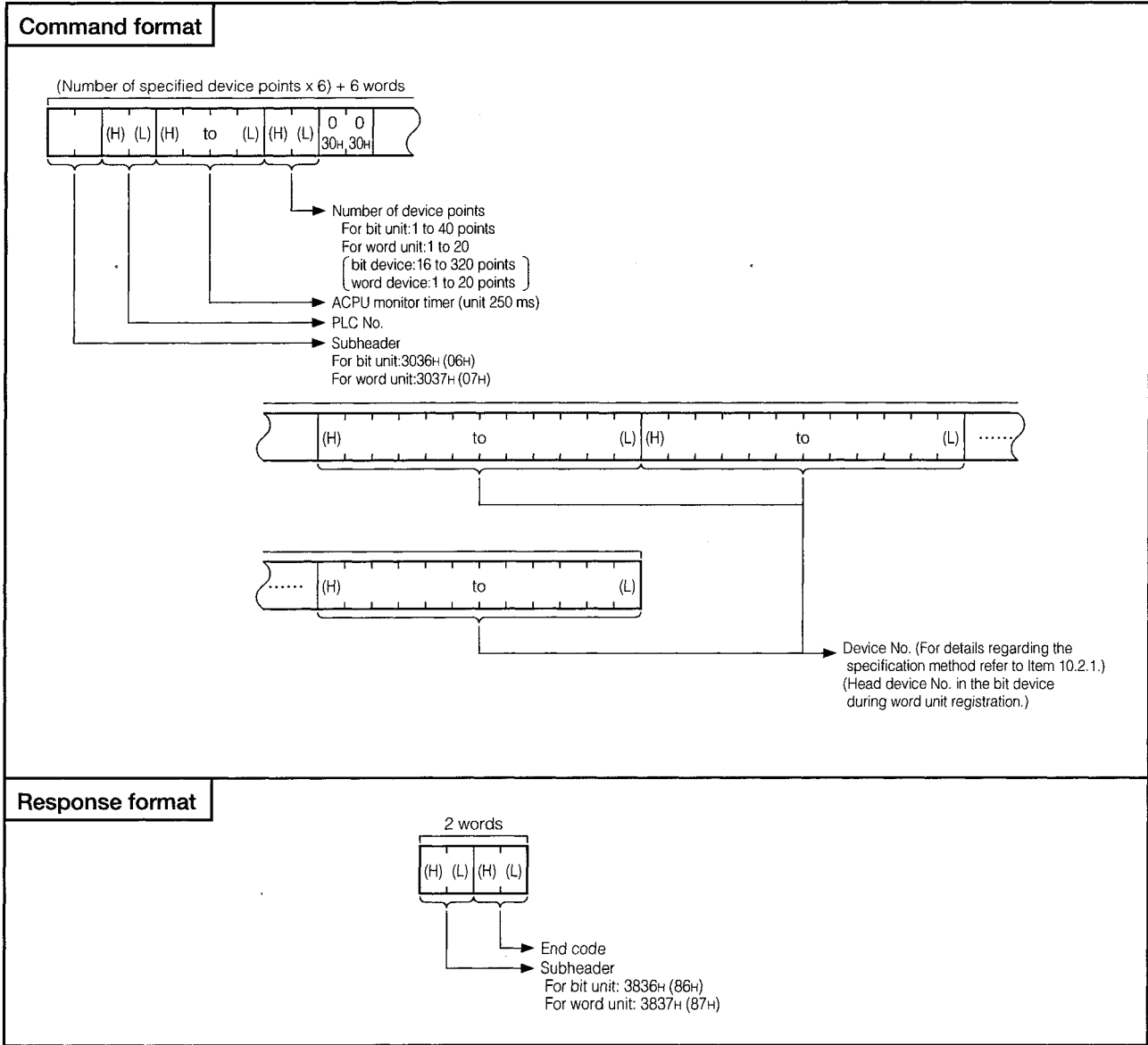
Monitor data registration

This section explains the command/response format when registering devices to be monitored.

(a) When exchanging using binary code



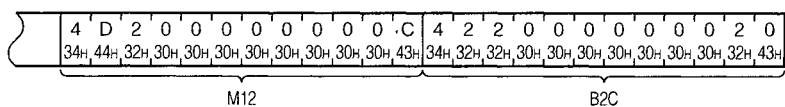
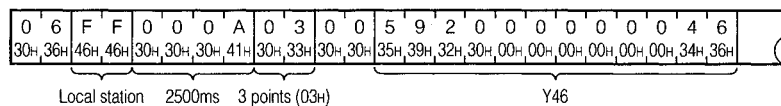
(b) When exchanging using ASCII code



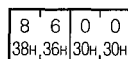
Example 1: Bit unit monitor data registration

When setting Y46, M12, and B2C in the PLC CPU installed in the E71.

Command (remote node → E71)



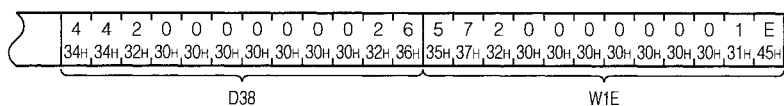
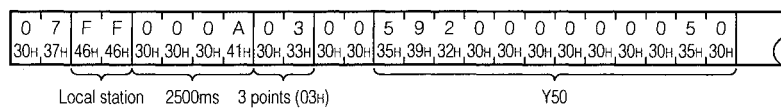
Response (E71 → remote node)



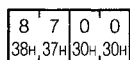
Example 2: Word unit monitor data registration

When setting Y50 to 5F, D38, and W1E in the PLC CPU installed in the E71.

Command (remote node → E71)



Response (E71 → remote node)

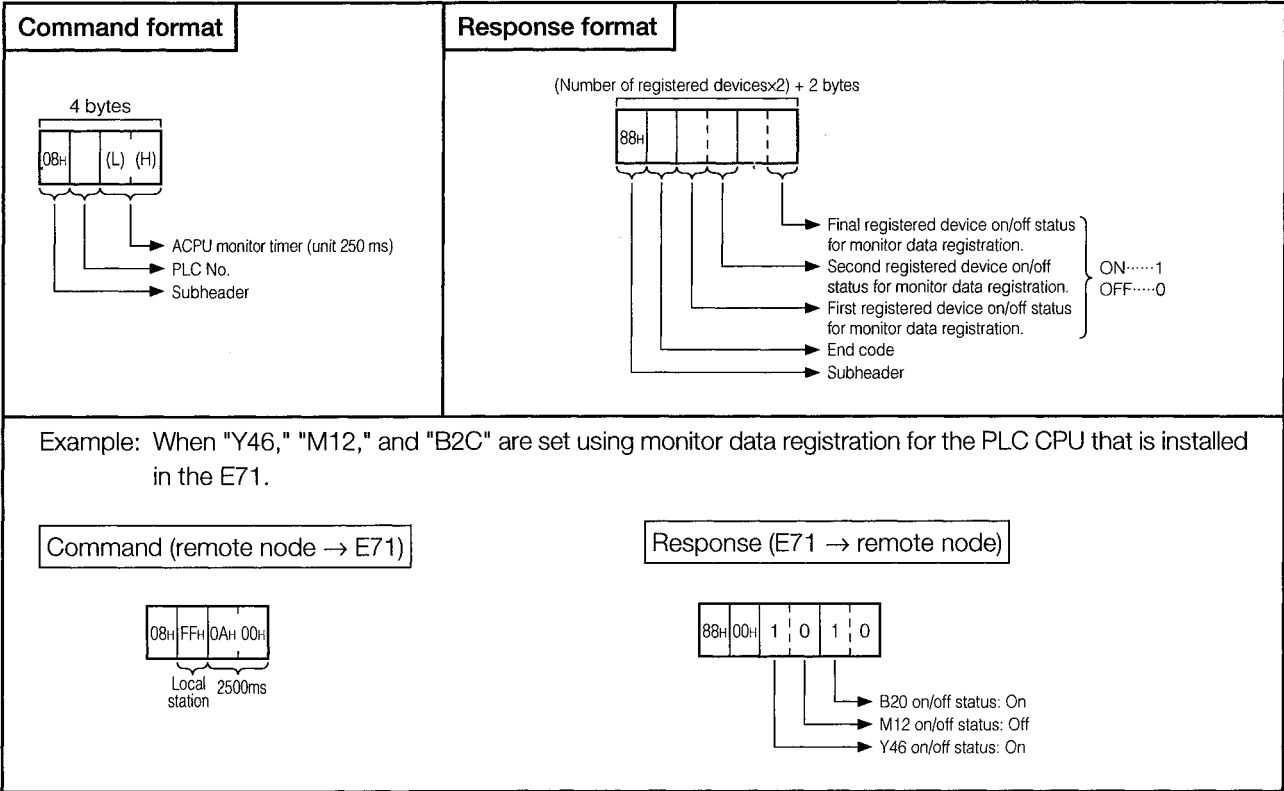


3

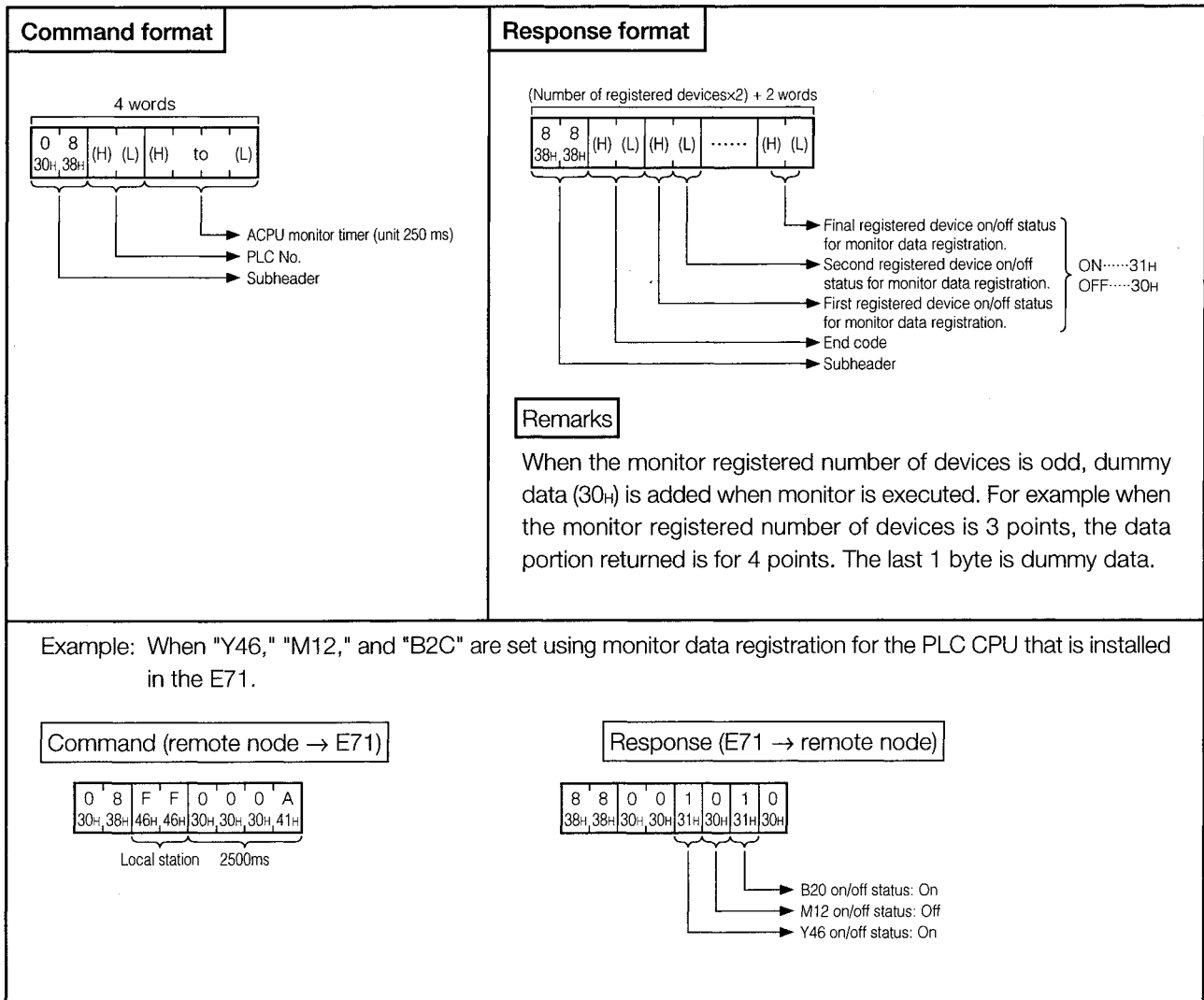
Bit unit monitor

The following section explains the command/response format when conducting monitoring of a set bit device which monitor data registration has been conducted.

(a) When exchanging using binary code



(b) When exchanging using ASCII code

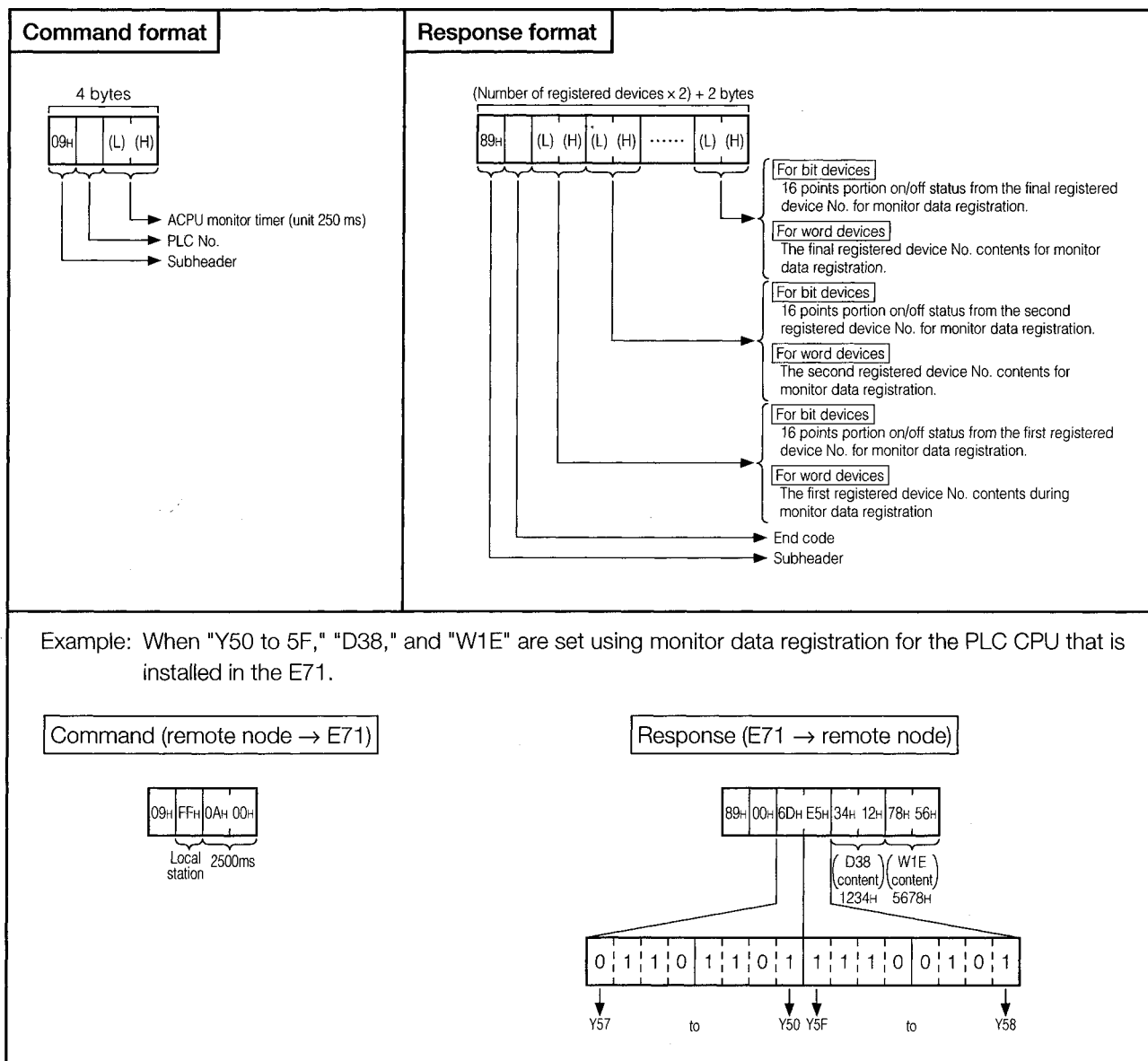


4

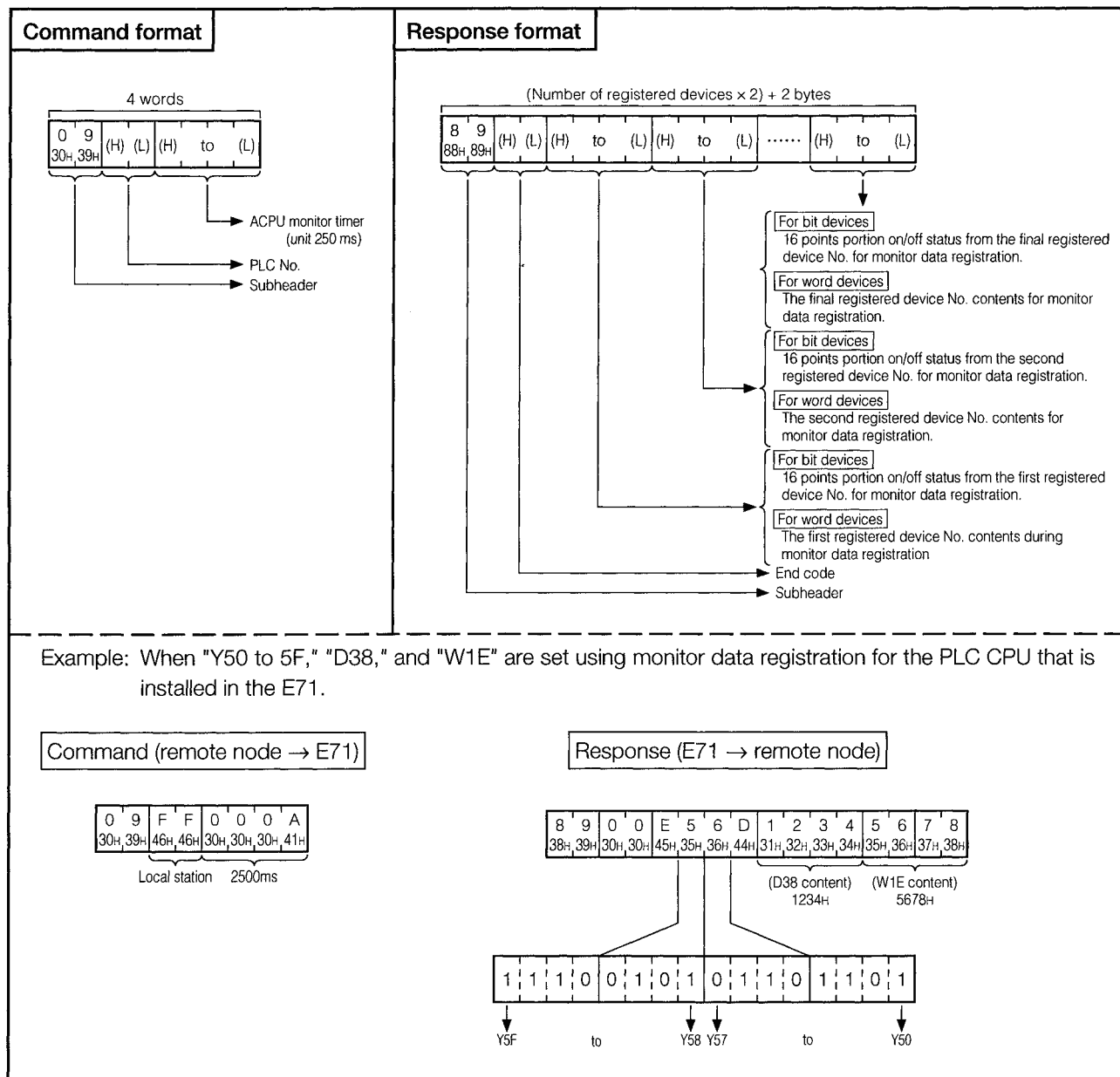
Word unit monitor

The following section explains the command/response format when conducting monitoring of a set word device and bit device (16 point unit) which monitor data registration has been conducted.

(a) When exchanging using binary code



(b) When exchanging using ASCII code



10.3 Extension File Register Read and Write

An extension file register is a file register that uses the empty area of the PLC CPU user memory area as a memory area for storing the data and calculated results required for the various types of data processing.

This section explains the control procedure specification contents, method, and an example specification for reading and writing the following extension file register.

10.3.1 Commands and Addresses

The function used for reading/writing extension file registers are shown in Table 10.3.

Table 10.3 Functions List

Item	Command/ response format	Processing description	Number of processing points con- ducted in one exchange	PLC CPU status		
				Stopped	Running Write possible setting	Write impossible setting
Batch read	17 _H	Extension file register (R) is read in 1 point unit.	256 points	○	○	○
Batch write	18 _H	The extension file register (R) is written to in 1 point unit.	256 points	○	○	×
Test (random write)	19 _H	The block No. and device No. are specified in the extension file register (R) is randomly written to in 1 point unit.	40 points	○	○	×
Monitor data registration	1A _H	The device No. to be monitored is registered in 1 point unit.	20 points	○	○	○
Monitor	1B _H	Monitor data registration is conducted and the extension file register is monitored.	—	○	○	○

In the PLC CPU status column in the above table the “O” represents execution possible and the “X” represents execution not possible.

Extension file register address

(a) Number of extension file registers.

Block No. 0 The number of registers specified by the PLC CPU parameter.

After block No. 1 There are 8192 registers for each block.

(b) The specifiable block No. range varies depending on the PLC CPU memory capacity (memory cassette type) and the PLC CPU parameter setting. For details refer to the SW-6 GHP-UTLPLC-FN1 Utility Package Operating Manual or the AnA/AnU Programming Manual (Dedicated Instruction Edition).

10.3.2 Precautions When Reading/Writing Extension File Registers

This section explains the precautions when reading/writing extension file registers.

- (1) Extension file registers cannot use A1 and A1NCPU.
- (2) Reading and writing cannot be performed for QnACPU extension file registers.
- (3) An error (End code 58H) sometimes cannot be detected even when read/write is executed for a block No. that does not exist. In this case, the read data is not correct data. In addition, when write is conducted the PLC CPU's user memory can be corrupted.

10.3.3 Extension File Register Batch Read

This section explains the command/response format when executing an extension file register batch read.

1 When exchanging using binary code

Command format

14 bytes

17H	(L)	(H)	(L)	to	(H)	(L)	(H)	00H
-----	-----	-----	-----	----	-----	-----	-----	-----

Number of devices (1 to 256)

Block No. specification
(for details refer to Item 10.3.1)

Device No. (for details regarding the
specification method refer to Item 10.2.1.)

ACPU monitor timer (unit 250 ms)

PLC No.

Subheader

Remarks

Set to "00H" when specifying the number of devices as 256.

Response format

(Specified number of devices x 2) + 2 bytes

97H	(L)	(H)	(L)	(H)	(L)	(H)
-----	-----	-----	-----	-----	-------	-----	-----

Final device No. contents for the specified block No.'s specified device No.

Specified device No. + 1 contents for the specified block No.

Specified device No. contents for the specified block No.

End code

Subheader

Example: When the contents of extension file register No. 2 block's R70 to 72 for the PLC CPU installed in the E71 are read.

Command (remote node → E71)

17H	FFH	0AH	00H	46H	00H	00H	00H	20H	52H	02H	00H	03H	00H
Local station		2500ms		R70				3 points					
→ Block No. 2													

Response (E71 → remote node)

97H	00H	34H	12H	65H	87H	3FH	01H
		Block No. 2's R72 contents (013FH)		Block No. 2's R71 contents (8765H)		Block No. 2's R70 contents (1234H)	

2

When exchanging using ASCII code

Command format

14 words

1	7	(H)	(L)	(H)	to	(L)	(H)	to	(L)	(H)	to	(L)	(H)	(L)	0	0
31H	37H														30H	30H

Number of devices (1 to 256)

Device No. (for details regarding the specification method refer to Item 10.2.1.)

Block No. specification (for details refer to Item 10.3.1)

ACPU monitor timer (unit 250 ms)

PLC No.

Subheader

Remarks

Set to "00H" when specifying the number of devices as 256.

Response format

(Specified number of devices x 2) + 2 bytes

9	7	(H)	(L)	(H)	to	(L)	(H)	to	(L)	(H)	to	(L)
39H	37H												

Final device No. contents for the specified block No.'s specified device No.

Specified device No. + 1 contents for the specified block No.

Specified device No. contents for the specified block No.

End code

Subheader

Example: When the contents of extension file register No. 2 block's R70 to 72 for the PLC CPU installed in the E71 are read.

Command (remote node → E71)

1	7	F	F	0	0	0	A	0	0	0	2	5	2	2	0	0	0	0	0	0	4	6	0	3	0	0
31H	37H	46H	46H	30H	30H	30H	41H	30H	30H	30H	32H	35H	32H	32H	30H	30H	30H	30H	30H	30H	34H	36H	30H	33H	30H	30H
Local station				2500ms				Block No. 2				R70								3 points						

Response (E71 → remote node)

9	7	0	0	1	2	3	4	8	7	6	5	0	1	3	F
39H	37H	30H	30H	31H	32H	33H	34H	38H	37H	36H	35H	30H	31H	33H	46H
				Block No. 2's R70 contents (1234H)				Block No. 2's R71 contents (8765H)				Block No. 2's R72 contents (013FH)			

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10.3.4 Extension File Register Batch Write

This section explains the command/response format when executing an extension file register batch write.

1 When exchanging using binary code

Command format

(Specified number of devices x 2) + 14 bytes

18H

(L) (H) (L)

to

(H) (L) (H)

00H

(L) (H) (L) (H)

.....

(L) (H)

Final device No. data for the specified block No.'s specified device No.

Specified device No. + 1 data for the specified block No.

Specified device No. data for the specified block No.

Number of devices (1 to 256)

Block No. specification (for details refer to Item 10.3.1)

Device No. (for details regarding the specification method refer to Item 10.2.1.)

ACPU monitor timer (unit 250 ms)

PLC No.

Subheader

Remarks

Set to "00H" when specifying the number of devices as 256.

Response format

2 bytes

98H

End code

Subheader

Example: When the contents of extension file register No. 3 block's R100 to 102 for the PLC CPU installed in the E71 are written.

Command (remote node → E71)

18H FFH 0AH 00H 64H 00H 00H 00H 20H 52H 03H 00H 03H 00H 09H 01H 76H 98H 34H 12H

Local station 2500ms R100 3 points Block No. 3

Write data (1234H) to block No. 3's R102

Write data (9876H) to block No. 3's R101

Write data (0109H) to block No. 3's R100

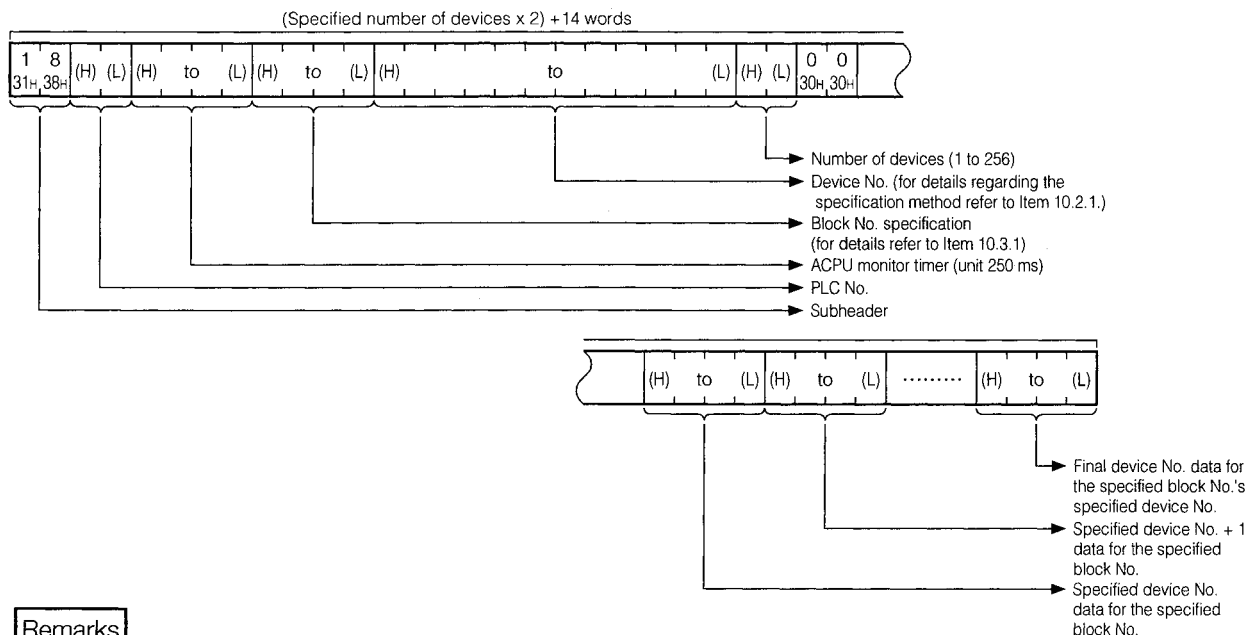
Response (E71 → remote node)

98H 00H

2

When exchanging using ASCII code

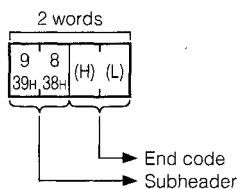
Command format



Remarks

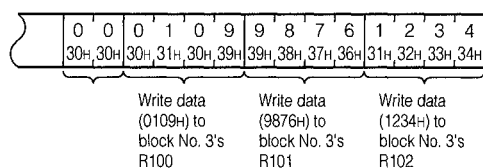
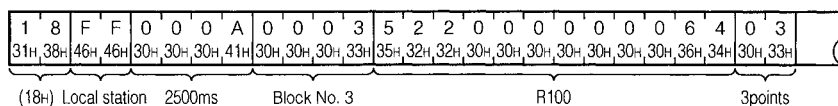
Set to "3030H" when specifying the number of devices as 256.

Response format

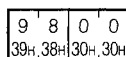


Example: When the contents of extension file register No. 3 block's R100 to 102 for the PLC CPU installed in the E71 are written.

Command (remote node → E71)



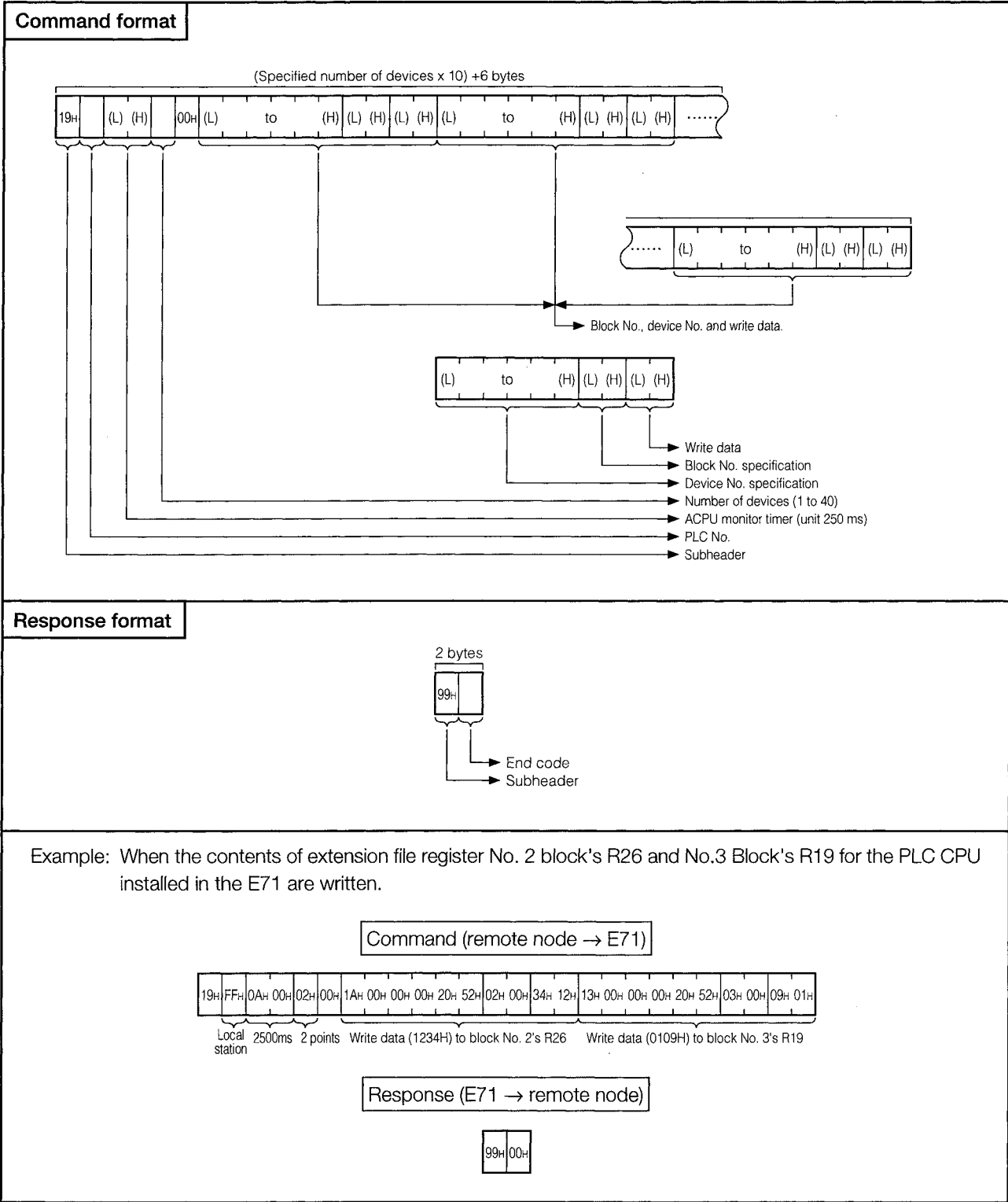
Response (E71 → remote node)



10.3.5 Extension File Register Test (Random Write)

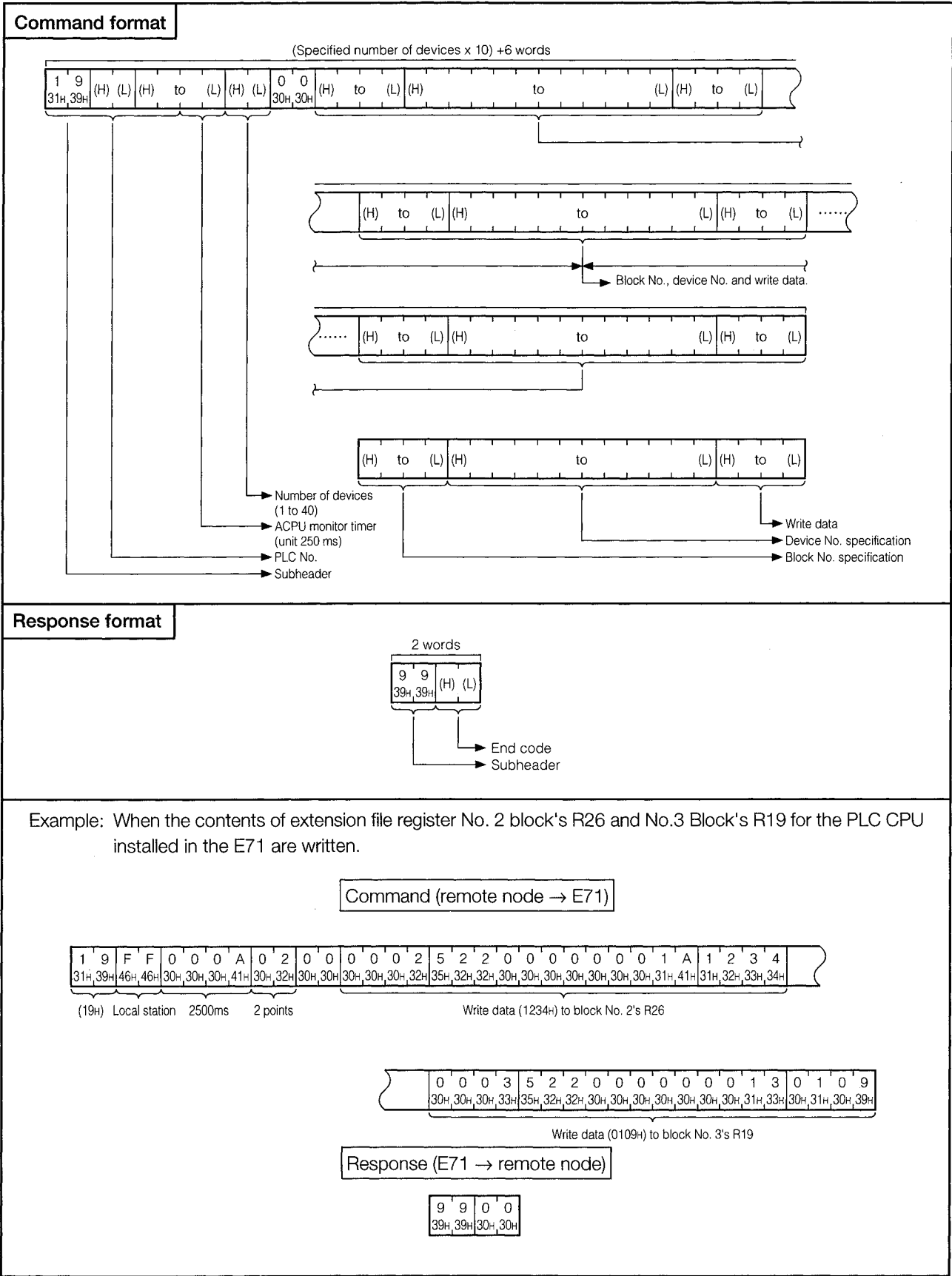
This section explains the command/response format when executing an extension file register random write.

1 When exchanging using binary code



2

When exchanging using ASCII code



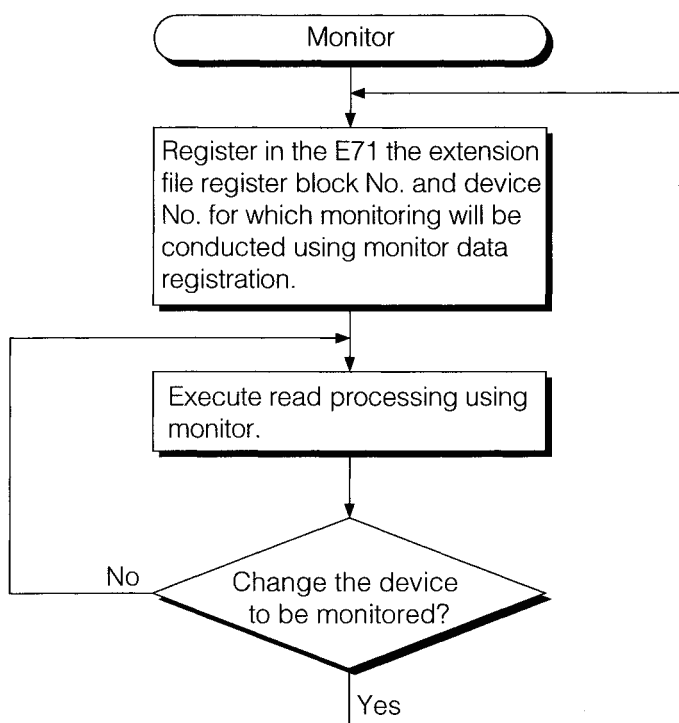
10.3.6 Extension File Register Monitor

The extension file register in the PLC CPU (device No. registered in the E71) the contents can be monitored by a remote node by registering beforehand the extension file register block No. and device No. that you want to monitor with a remote node in the E71 and then executing a monitor instruction from the remote node.

Reading using extension file register batch read can be processed in continuous device No., but by reading using the monitor it is possible to randomly specify a free device and No. and conduct the reading.

1 Monitor operation procedure

The operation procedure when conducting monitor is shown below.



Point

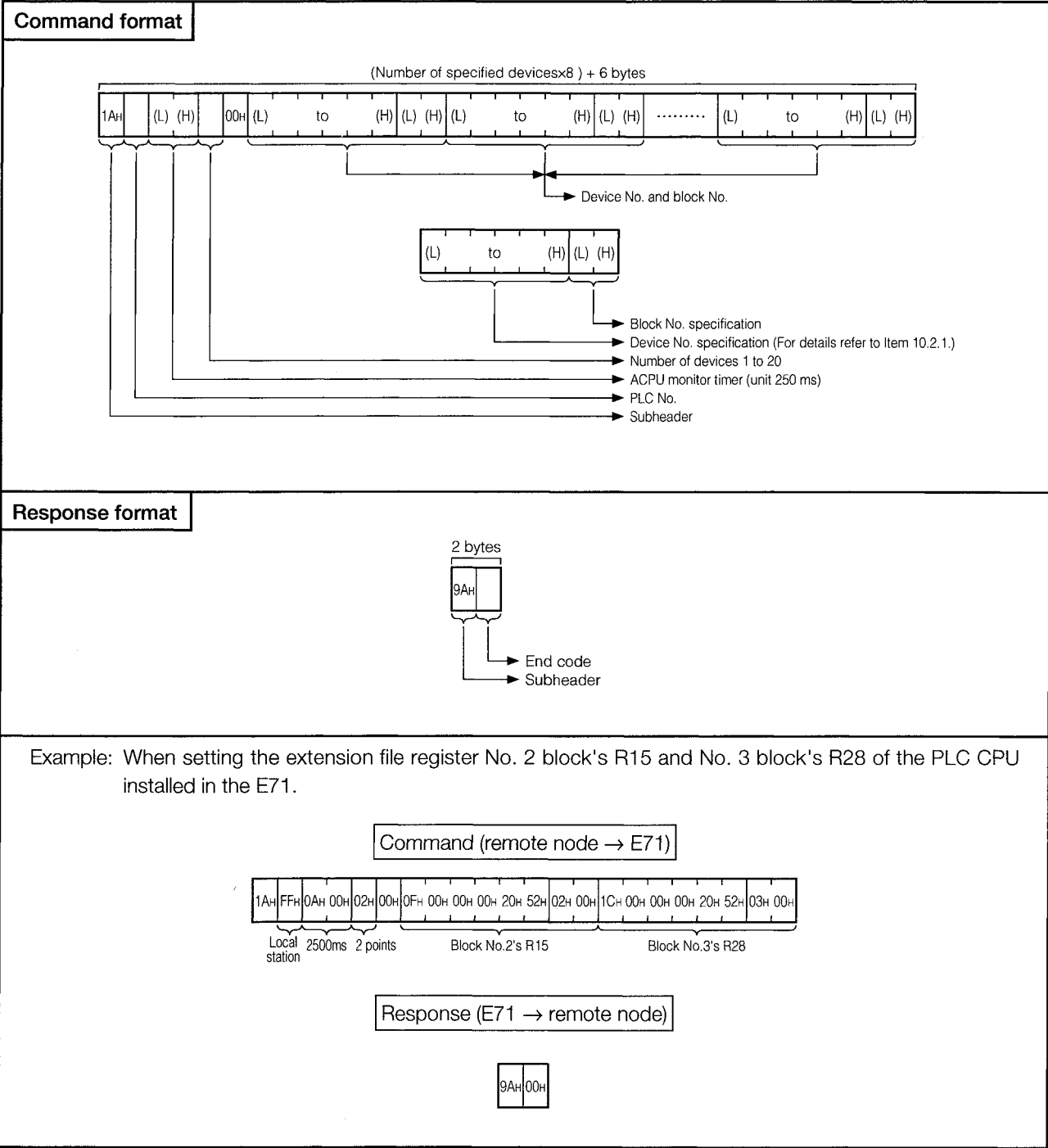
- (1) In operation procedures like that above where monitoring will be executed, the monitor data registration operation must be conducted. If monitoring is executed without conducting monitor data registration, an error (End code 57H) will occur.
- (2) The monitor data registration contents will be erased if the power is turned off or the PLC CPU is reset.
- (3) The 3 types of monitor data registration, device memory bit unit, word unit, and extension file register can be registered in the E71.
- (4) When monitor data registration is performed from multiple remote nodes to the device memory of the PLC CPU on the same station, the registration data will be overwritten. Thus, the device memory last registered will become effective.

2

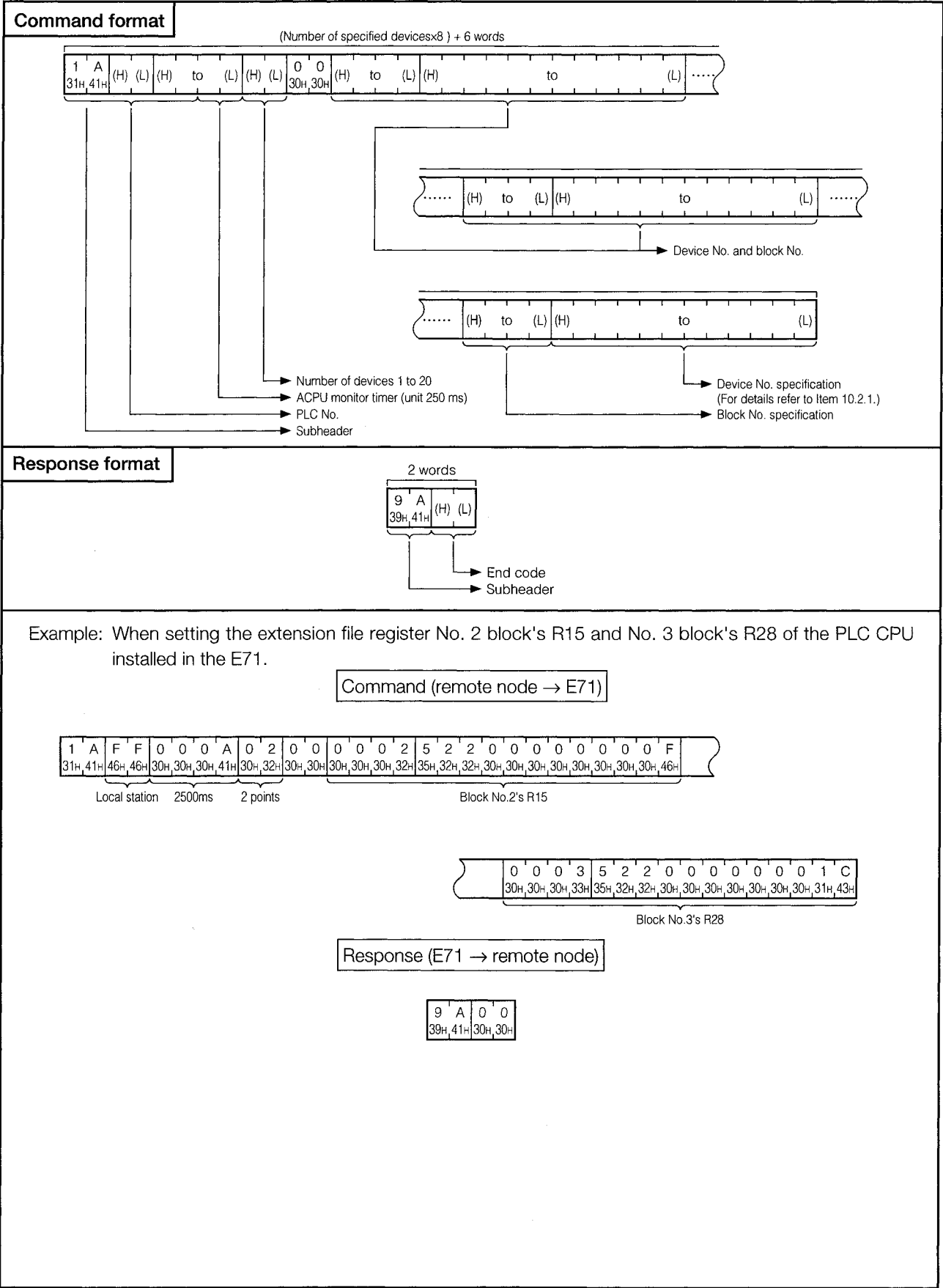
Monitor data registration

This section explains the command/response format when registering the extension file register device to be monitored.

(a) When exchanging using binary code



(b) When exchanging using ASCII code



3

Monitor

The following section explains the command/response format when conducting monitoring of a set extension file register which monitor data registration has been conducted.

(a) When exchanging using binary code

Command format

4 bytes

1B_H

(L)

(H)

ACPU monitor timer (unit 250 ms)

PLC No.

Subheader

Response format

(Number of registered devicesx2) + 2 bytes

9B_H

(L)

(H)

(L)

(H)

.....

(L)

(H)

Final registered device No. contents for monitor data registration.

Second registered device No. contents for monitor data registration.

First registered device No. contents for monitor data registration.

End code

Subheader

Example: When No.2 block's R15 and No.3 block's R28 are set using monitor data registration for the PLC CPU that is installed in the E71.

Command (remote node → E71)

1B_H

FF_H

0A_H

00_H

Local station

2500ms

Response (E71 → remote node)

9B_H

00_H

6D_H

E5_H

34_H

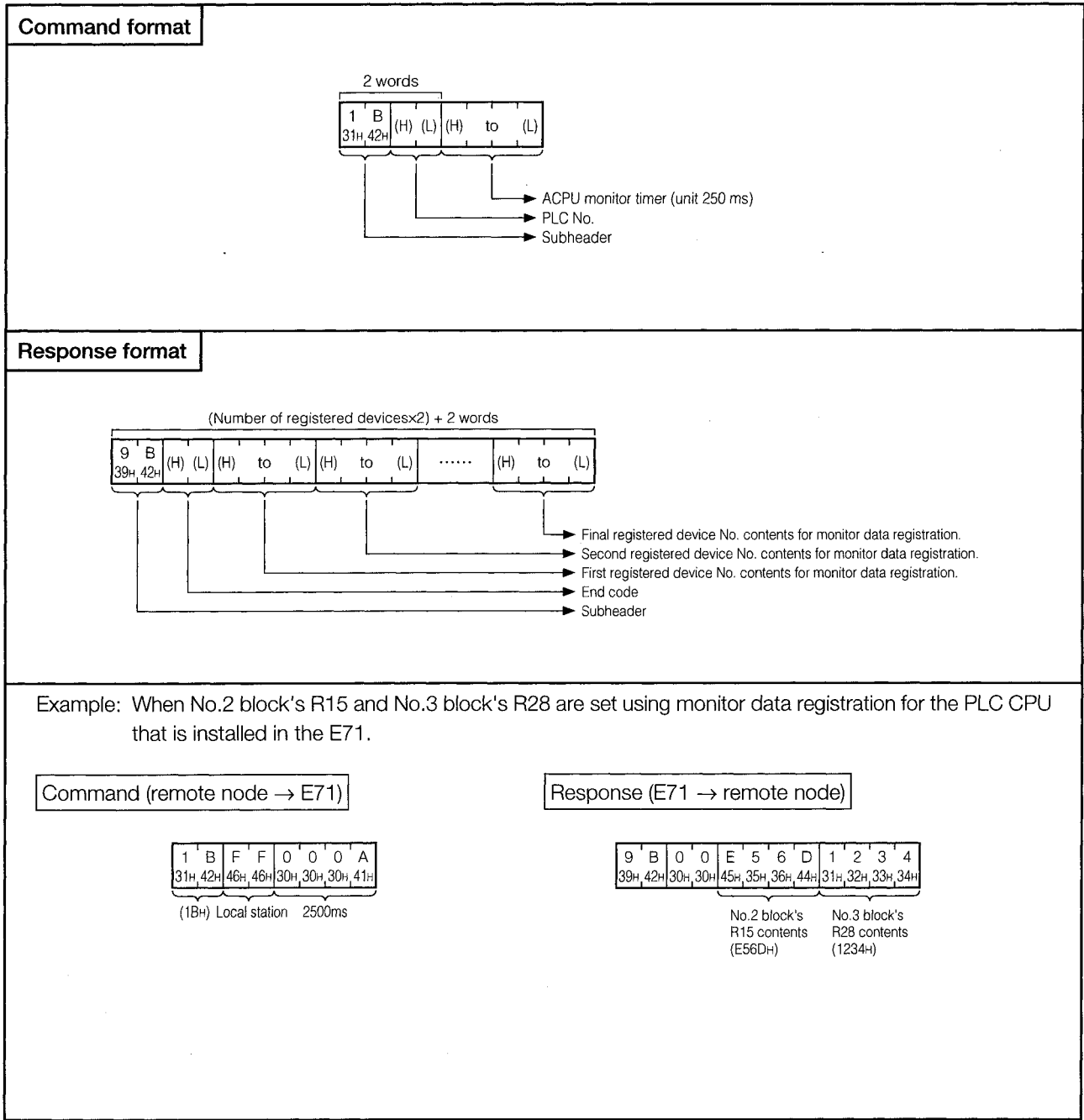
12_H

No.3 block's R28 contents (1234_H)

No.2 block's R15 contents (E56D_H)

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(b) When exchanging using ASCII code



10.3.7 Extension File Register Direct Read/Write

1

The AnACPU dedicated commands used for extension file register direct read/write are shown below. These command functions are used to access the extension file registers in block No. 0 to No. 256, and can specify the addresses from the block No. 1 device No. 0 as device No. without concern for each block No. and can access them. (The usable number of blocks \times 8192 extension file registers can be accessed using continuous device Nos.)

Item	Command/ response format	Processing description	Number of processing points con- ducted in one exchange	PLC CPU status		
				Stopped	Running	
					Write possible setting	Write impossible setting
Direct read	3B _H	Reads the extension file register (R) in one point unit.	256 points	○	○	○
Direct write	3C _H	Writes the extension file register (R) in one point unit.	256 points	○	○	×

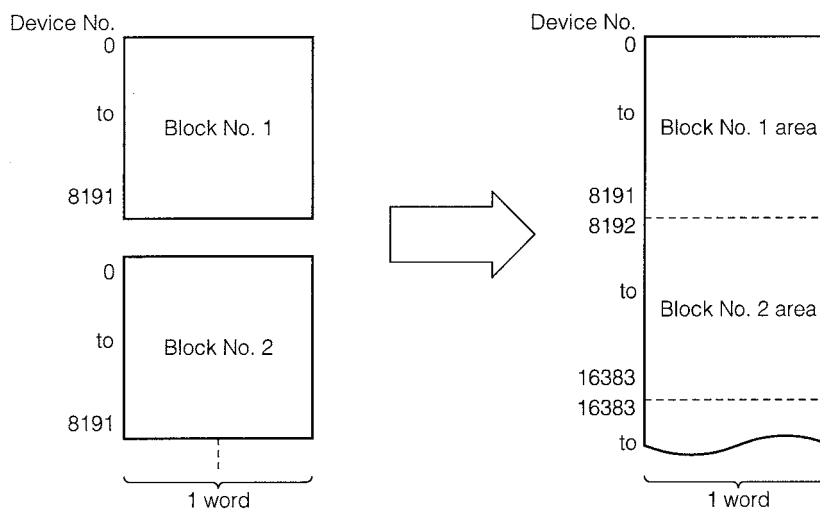
In the PLC CPU status column in the above table the “○” stands for executing possible and the “×” stands for execution not possible.

2

Extension file register device No.

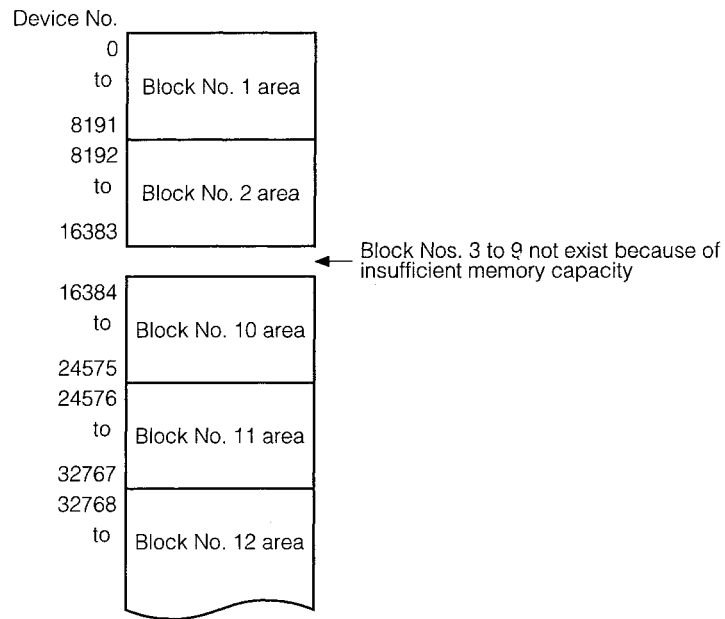
(a) The device No. range that can be specified is shown below.

0 to (usable number of blocks \times 8192) – 1



The device Nos. that will be used for direct read/write are automatically allocated in order from the smallest device of the block No. from those after block No. 1. The device Nos. that can be specified vary depending on the type of memory cassette and PLC CPU parameter setting.

Device Nos. are not allocated for block Nos. not existing in the memory cassette. However, device Nos. are automatically allocated skipping the block Nos. that do not exist in the memory cassette.



Point

- (1) The AnACPU dedicated commands can only be used when executing read or write of data in the extension file registers for blocks Nos. 0 to 256. In addition, these commands can be used regardless of whether the parameter file register settings are valid.
- (2) When accessing the specified file register (R) using parameters or when accessing by specifying the block No., use the commands given in Item 10.3.6.
- (3) The calculation method for the header device No. specified using the AnACPU dedicated commands is as follows.

Given that the device No. of the nth block from the header is m(0 to 8191), then

$$\text{Header device No.} = (n - 1) \times 8192 + m$$

3

Extension file register direct read

This section explains the command/response format when executing an extension file register direct read.

(a) When exchanging using binary code

Command format

12 bytes

3BH

(L)

(H)

(L)

to

(H)

00H

Number of devices (1 to 256)

Device No.
(for details regarding the specification method refer to Item 10.2.1.)

ACPU monitor timer (unit 250 ms)

PLC No.

Subheader

Remarks

Set to "00H" when specifying the number of devices as 256.

Response format

(Specified number of devices x 2) + 2 bytes

BBH

(L)

(H)

(L)

(H)

.....

(L)

(H)

Final device No. contents for the specified No.'s specified device No.

Specified device No. + 1 contents

Specified device No. contents

End code

Subheader

Example: When the contents of extension file register R70 to 73 for the PLC CPU installed in the E71 are read.

Command (remote node → E71)

3BH

FFH

0AH

00H

46H

00H

00H

00H

20H

52H

04H

00H

4 points

R70

2500ms

Local station

Response (E71 → remote node)

BBH

00H

34H

12H

65H

87H

3FH

01H

20H

00H

R73 contents
(0020H)

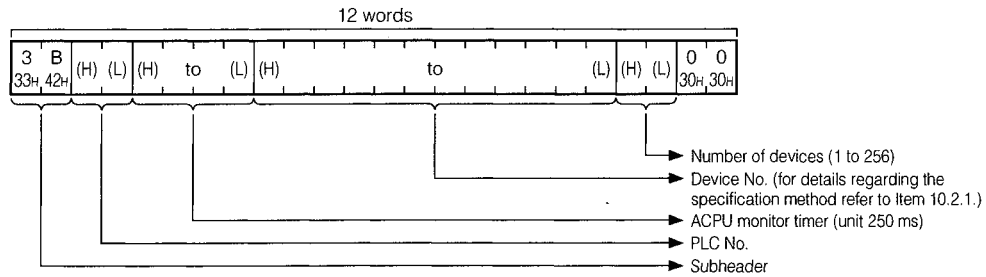
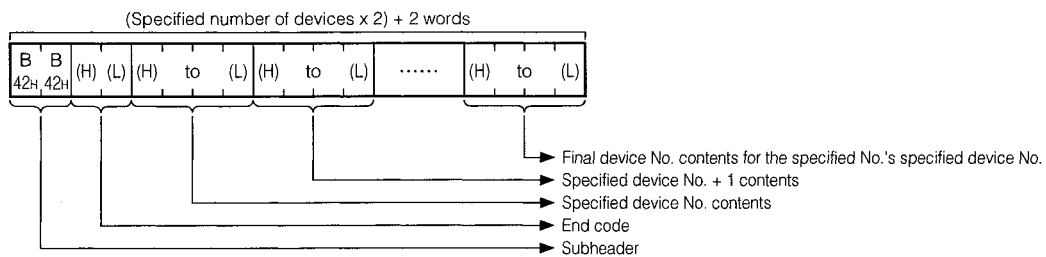
R72 contents
(013FH)

R71 contents
(8765H)

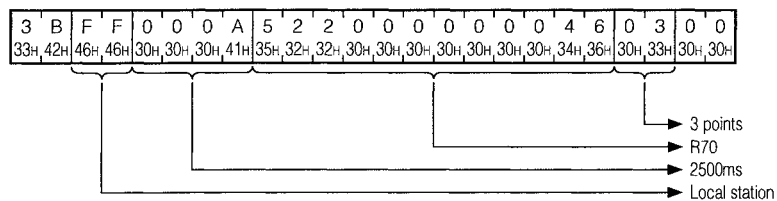
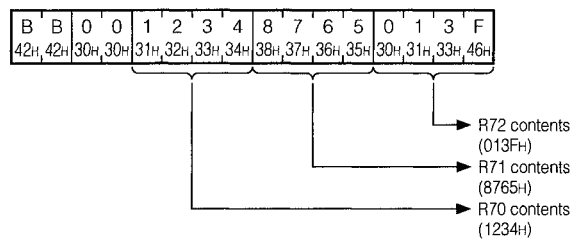
R70 contents
(1234H)

10 - 55

(b) When exchanging using binary code

Command format**Remarks**Set to "00_H" when specifying the number of devices as 256.**Response format**

Example: When the contents of extension file register R70 to 72 for the PLC CPU installed in the E71 are read.

Command (remote node → E71)**Response (E71 → remote node)**

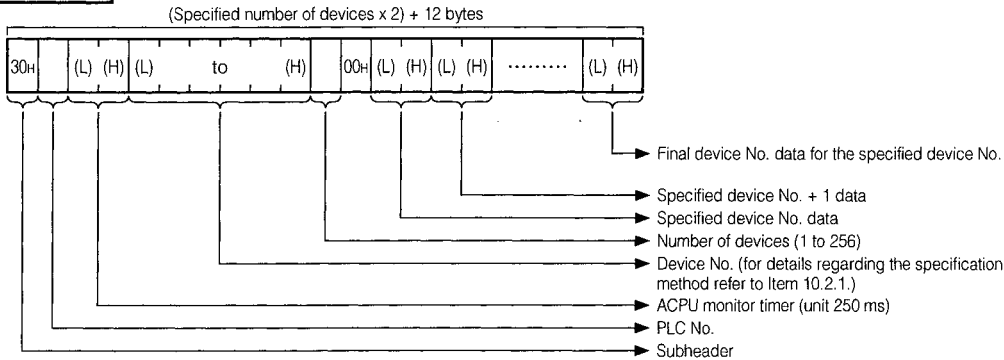
4

Extension file register direct write

This section explains the command/response format when executing an extension file register direct write.

(a) When exchanging using binary code

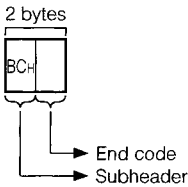
Command format



Remarks

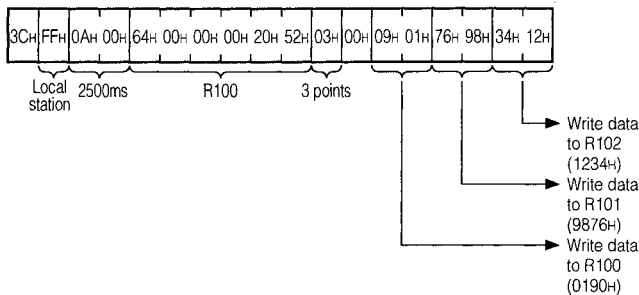
Set to "00H" when specifying the number of devices as 256.

Response format



Example: When the contents of extension file register R100 to 102 data are written in the PLC CPU installed in the E71 are read.

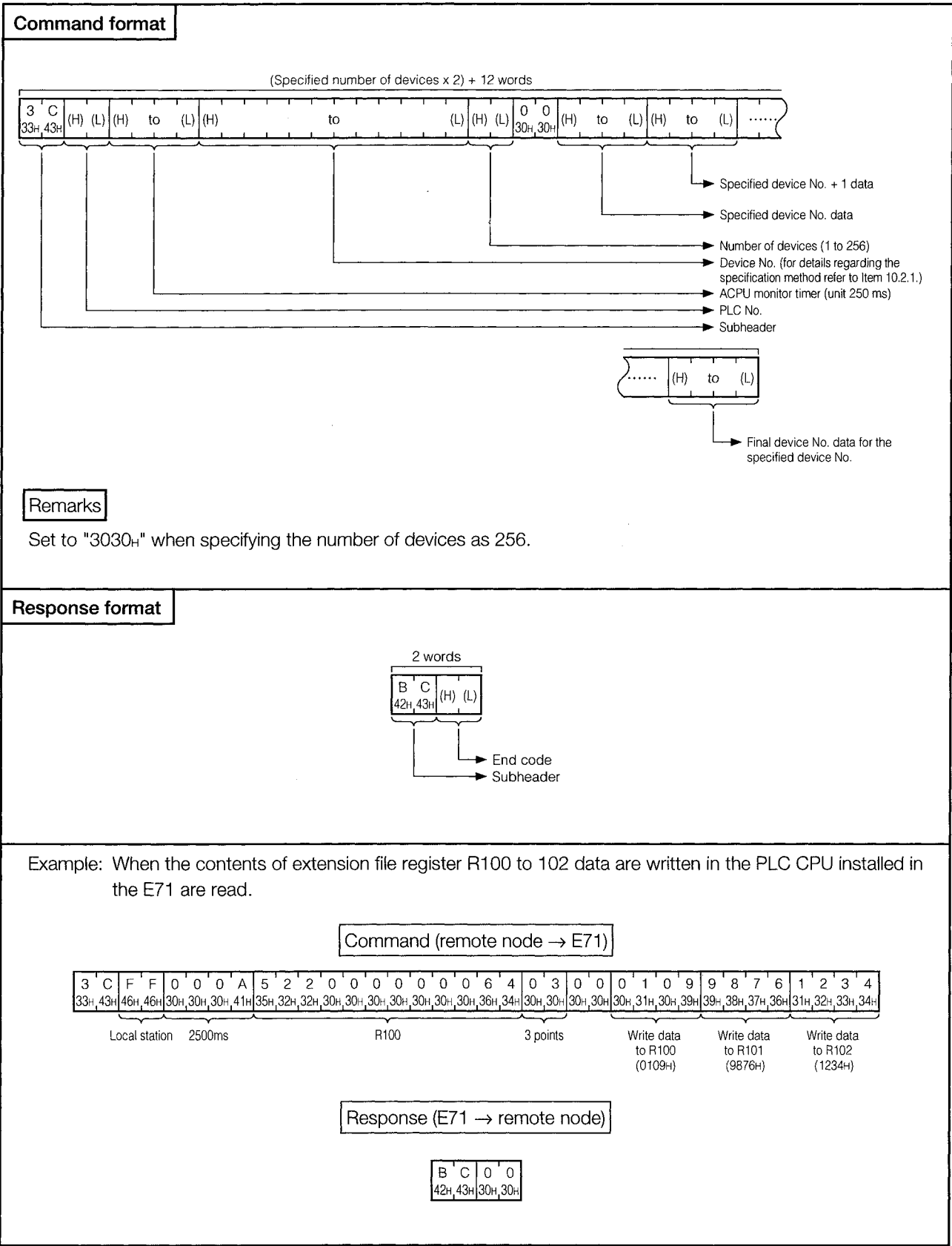
Command (remote node → E71)



Response (E71 → remote node)



(b) When exchanging using ASCII code



10.4 Special Function Module Data Read and Write

This section explains the control procedure specification contents, method, and example specification when reading contents from the special function module buffer memory area or writing data to this buffer memory area.

This command accesses the special function module buffer memory in byte units.

10.4.1 Command and Data Item Specification Method

(1) The functions used to read from and write to the special function module are shown in Table 10.4.

Table 10.4 Functions List

Item	Command/ response classification	Processing description	Number of processing points con- ducted in one exchange	PLC CPU status		
				Stopped	Running Write possible setting	Write impossible setting
Batch read	OEh	Reads the special function mod- ule buffer memory contents.	256 bytes (128 words)	○	○	○
Batch write	OFh	Writes data to the special func- tion module buffer memory.		○	○	×

In the PLC CPU status column in the above table the “○” means execute possible and the “×” means execute not possible.

(2) Link possible special function module model name, buffer memory head address, and module No.

Special function module model name	Buffer memory head address (hexadecimal)	Module No. installed in slot 0
Model AD61 (S1) high speed counter module	80H	01H
Model A616AD analog-digital conversion module	10H	01H
Model A616DAI digital-analog conversion module	10H	01H
Model A616DAV digital-analog conversion module	10H	01H
Model A616TD temperature-digital conversion module	10H	01H
Model A62DA (S1) digital-analog conversion module	10H	01H
Model A68AD (S2) analog-digital conversion module	80H	01H
Model A68ADN analog digital conversion module	80H	01H
Model A68DAV/DAI (S1) digital-analog conversion module	10H	01H
Model A68RD3/4 temperature-digital conversion module	10H	01H
Model A84AD analog-digital conversion module	10H	02H
Model A81CPU PID control module	200H	03H
Model A61LS position detection module	80H	01H
Model A62LS (S5) position detection module	80H	02H
Model AJ71 (P) T32 (S3) MELSECNET/MINI master module	20H	01H
Model AJ61BT11 CC-Link system master-local module	2000H (*2)	01H
Model AJ71C22 (S1) multiple drop link module	1000H	01H
Model AJ71C24 (S3/S6/S8) computer link module	1000H	01H
Model AJ71UC24 computer link module	400H	01H
Model AD51 (S3) intelligent communication module	800H	02H
Model AD51H (S3) intelligent communication module	800H	02H

Special function module model name	Buffer memory head address (hexadecimal)	Module No. installed in slot 0
Model AJ71C21 (S1) terminal interface module	400H	01H
Model AJ71B62 (S3) B/NET interface module	20H	01H
Model AJ71P41 SUMINET interface module	400H	01H
Model AJ71E71 (S3) Ethernet interface module	400H (*3)	01H
Model AD51FD (S3) external problem diagnostic module	280H	02H
Model AD57G (S3) graphic controller module	280H	02H
Model AS25VS vision sensor module	100H	02H
Model AS50VS vision sensor module	100H	02H
Model AS50VS-GN vision sensor module	80H	02H
Model AD59 (S1) memory card interface module	1800H (*1)	01H
Model AJ71ID1(2)-R4 ID interface module	280H	01H
Model AD70 (D) (S2) positioning module	80H	01H
Model AD71 (S1/S2/S7) positioning module	200H	01H
Model AD72 positioning module	200H	02H
Model AD75P1/P2/P3 (S3), AD75M1/M2/M3 positioning module	800H	01H
Model A1SD61, A1SD62 (E/D (S1)) high speed counter module	10H	01H
Model A1S62DA digital-analog conversion module	10H	01H
Model A1S62RD3/4 temperature-digital conversion module	10H	01H
Model A1S64AD analog-digital conversion module	10H	01H
Model A1SJ71 (U) C24-R2 (R4/PRF) computer link module	400H	01H
Model A1SJ71E71-B2/B5 (S3) Ethernet interface module	400H (*3)	01H
Model A1SD51S intelligent communication module	800H	01H
Model A1SJ71ID1(2)-R4 ID interface module	280H	01H
Model A1SD70 single axis positioning module	80H	01H
Model A1SD71-S2 (S7) positioning module	200H	02H
Model A1SD75P1/P2/P3 (S3) positioning module	800H	01H
Model A1SD75M1/M2/M3 positioning module	800H	01H
Model A1S63ADA analog I/O module	10H	01H
Model A1S64TCTT (BW)-S1 temperature adjustment module	20H	01H
Model A1S64TCRT (BW)-S1 temperature adjustment module	20H	01H
Model A1S62TCTT (BW)-S2 temperature adjustment module	20H	01H
Model A1S62TCRT (BW)-S2 temperature adjustment module	20H	01H
Model A1S68DAV/DAI digital-analog conversion module	20H	01H
Model A1S68AD analog-digital conversion module	20H	01H
Model A1S68TD temperature-digital conversion module	20H	01H
Model A1SJ71PT32-S3 MELSECNET/MINI master module	20H	01H
Model A1SJ61BT11 CC-Link system master-local module	2000H (*2)	01H

- *1 Only the memory area for memory card access can be read from/written to when the memory card bank is switched by the I/O signal Y10, Y11 between the PLC CPU and AD59 (S1).
- *2 By switching banks of the buffer memory by the input/output signal Y1C/Y1D between the PLC CPU and AJ61BT11/A1SJ61BT11, the buffer memory of the corresponding bank can be read/written.
- *3 By switching banks of the buffer memory by the input/output signal Y1C between the PLC CPU and E71, the fixed buffer of the corresponding bank and the buffer for random access can be read/written.

3

Thinking regarding a special function module buffer memory

Special function module buffer memory is configured of 1 address of 16 bits (1 word) and is read from and written to using a FROM/TO instruction between the PLC CPU and the special function module. When the special function module buffer memory is read from or written to from a remote node via the E71, the read/write is conducted using 1 address = 8 bits (1 byte) units.

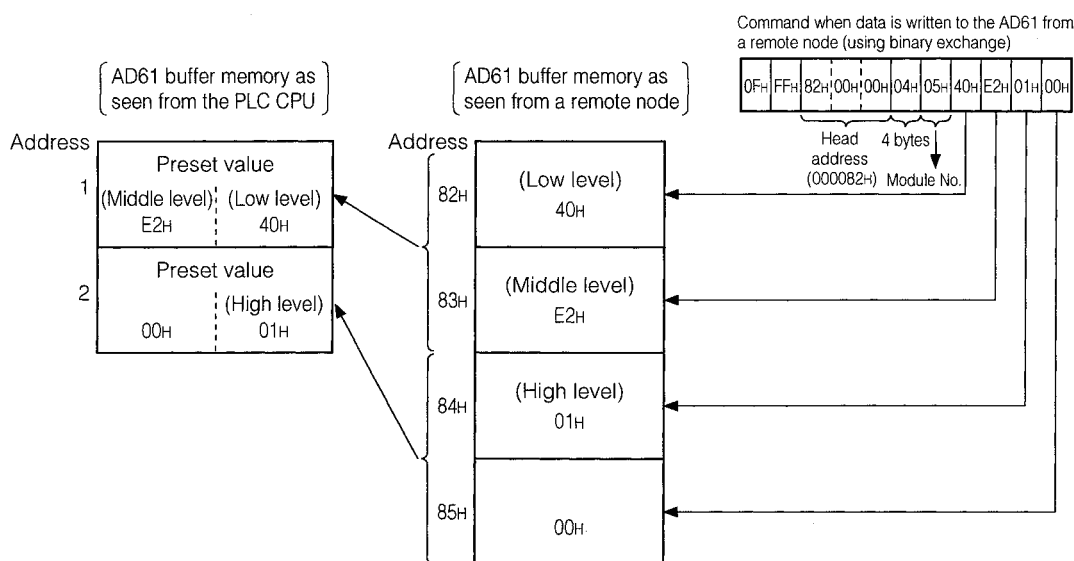
The address (hexadecimal) specified by a remote node is calculated using the FROM/TO instruction address as shown below.

Specified address (hexadecimal) = [(FROM/TO instruction address x 2)]
is made hexadecimal + each module head address

Example: When the model AD61 high speed counter module FROM/TO instruction address 1 (preset value) is specified

Specified address	FROM/TO instruction address 1 x 2	Head address
82 _H	= 2 _H	+ 80 _H

Following is an explanation of an example using the AD61 for the data format when the special function module buffer memory is accessed from a remote node via the E71.

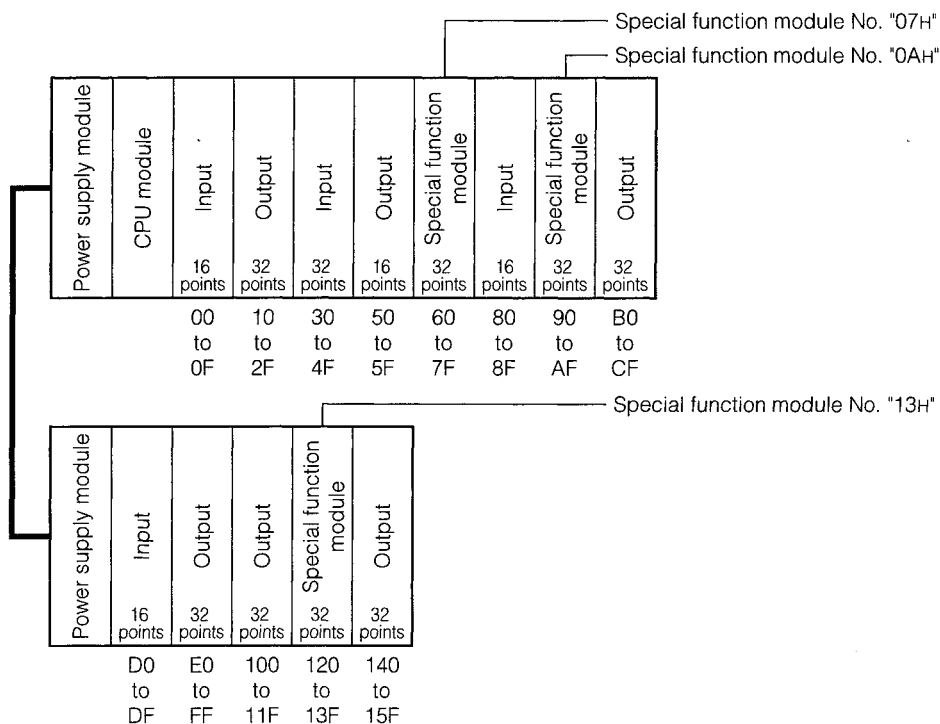
**Point**

The special function module buffer memory contains a read/write possible area, read only area, write only area, and an OS user usage not possible area for each module. Execute this function in accordance with the explanations given in each module manual. Conducting a mistake in read/write will cause an error to occur in the PLC CPU or the special function module.

4

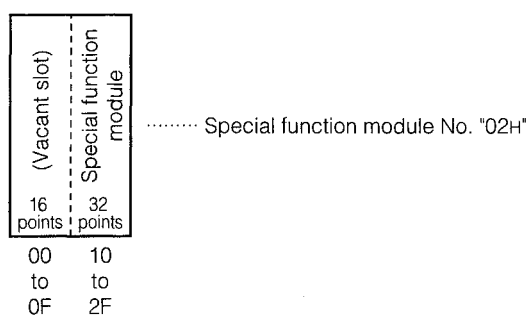
Thinking regarding special function module Nos. occurring in commands**(a) Module Nos. of special function modules that occupy one slot**

For special function module Nos. that are specified by control procedures, if the special function module I/O address final address is expressed in 3 digits, then only the first 2 digits are used.

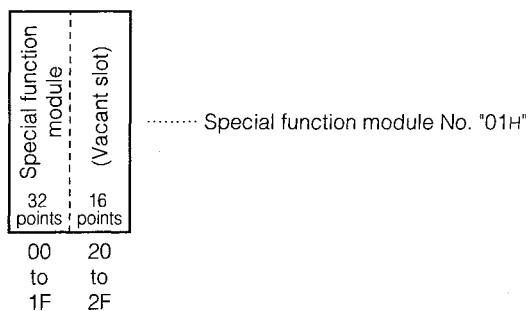
**(b) Module Nos. of special function modules that occupy 2 slots**

For special function modules that occupy 2 slots, the number of occupied points for each slot of each module is set. Special function module Nos. for which the final address of a slot allocated as a special function module is expressed in 3 digits, only the first 2 digits are used. For information regarding the allocation of each slot of each module, refer to the Special Function Module Users Manual.

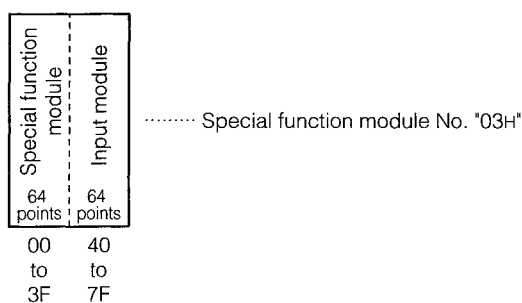
- ① Module when the first half of the slots are allocated as vacant slots. (AD72, A84AD, etc.)



- ② Module when the last half of the slots are allocated as vacant slots. (A61LS, etc.)



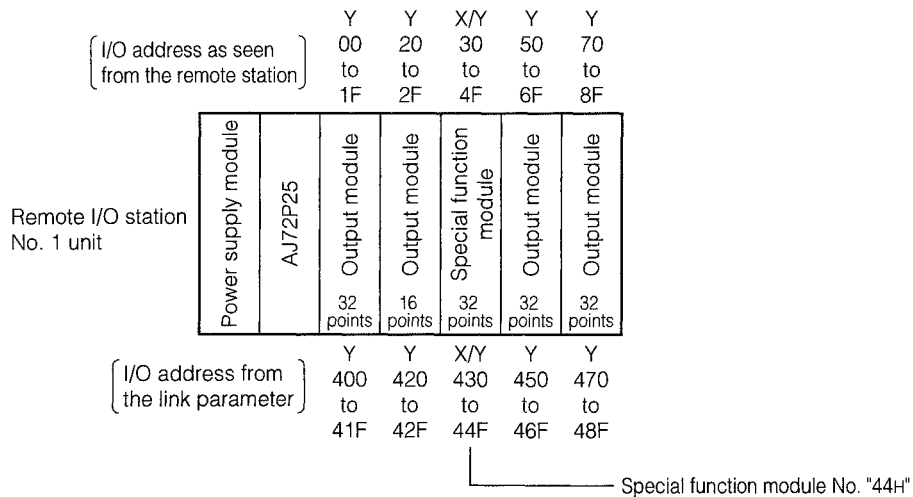
- ③ Module when the special function module allocation I/O allocation is mixed. (For the A81CPU)



- (c) MELSECNET(II) and MELSECNET(B) remote station special function module's module No.

The remote station special function module's module No. is set by the contents of the link parameter set in the master station.

L/R No.	M←L		M→R	M←R	M→L/R		M←L/R	
	B	W	W	W	Y	X/Y	X	Y/X
R1	---	---	29C-309	0F9-15E	400-48F	000-08F	430-44F	030-04F
R2	---	---	215-24F	080-0A3	510-67	010-17F	500-65F	000-15F
R3	---	---	1B6-214	15F-1B5	270-32F	050-10F	220-28F	000-06F
	-	-	-	-	-	-	-	-
	-	-	-	-	-	-	-	-
	-	-	-	-	-	-	-	-
	-	-	-	-	-	-	-	-
	-	-	-	-	-	-	-	-



(d) MELSECNET/10 remote I/O station special function module's module No.

For remote I/O station special function module's module Nos., when the final address of all of the following "I/O address as seen from the remote I/O station" are 3 digits then only the first two digits are used. Set using the "I/O addresses as seen from the remote I/O station" regardless of the common parameter contents set in the MELSECNET/10 remote I/O net master station.

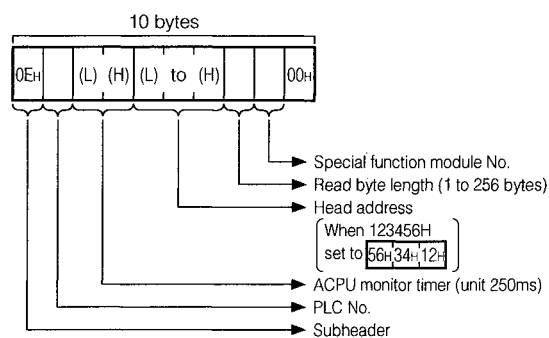
		Special function module No. "04H"				
		Y	Y	X/Y	Y	Y
		00	20	30	50	70
		to	to	to	to	to
		1F	2F	4F	6F	8F
Remote I/O station No. 1 unit	Power supply module					
	AJ72LP25					
	Output module					
	Output module					
	Special function module					
		32 points	16 points	32 points	32 points	32 points
		Y	Y	X/Y	Y	Y
		400	420	430	450	470
		to	to	to	to	to
		41F	42F	44F	46F	48F

10.4.2 Special Function Module Buffer Memory Read

This section explains the command/response format when reading data from the special function module buffer memory.

1 When exchanging using binary code

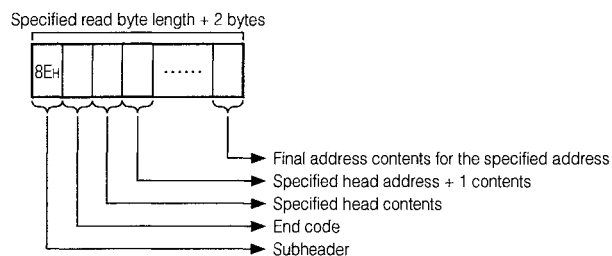
Command format



Remarks

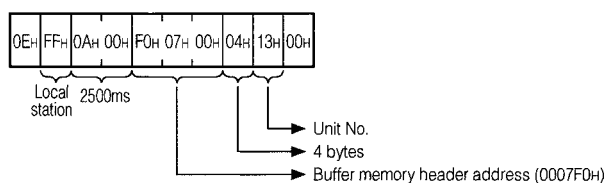
Set to "00H" when specifying the byte length as 256 bytes.

Response format

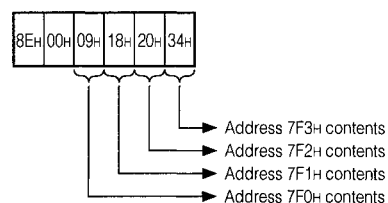


Example: When reading 7F0H to 7F3H of the special function module (X.Y120 to 13F(module No. 13H)) in the same station installed in the E71.

Command (remote node → E71)



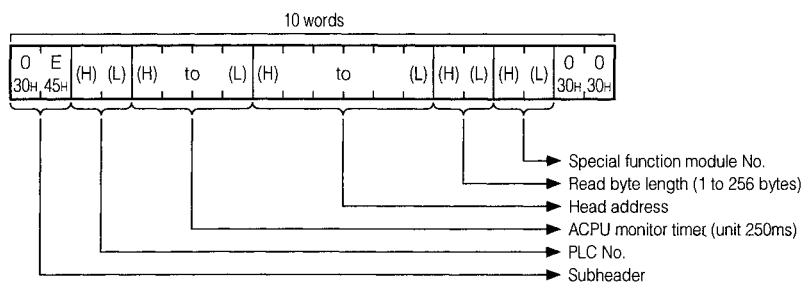
Response (E71 → remote node)



2

When exchanging using ASCII code

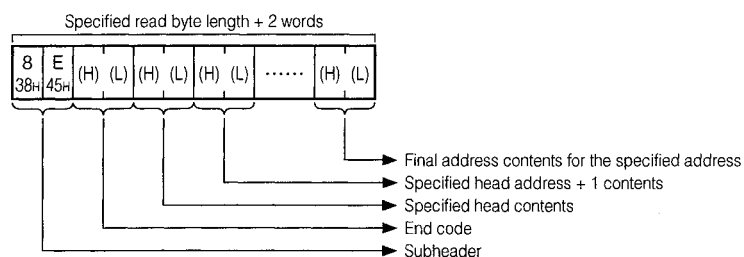
Command format



Remarks

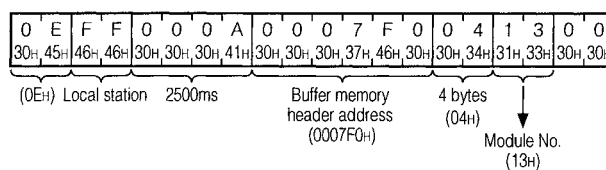
Set to "3030H" when specifying the byte length as 256 bytes.

Response format

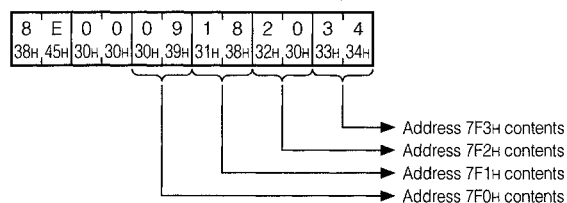


Example: When reading 7F0H to 7F3H of the special function module (X.Y120 to 13F(module No. 13H)) in the same station installed in the E71.

Command (remote node → E71)



Response (E71 → remote node)



10.4.3 Special Function Module Buffer Memory Write

This section explains the command/response format when writing data to the special function module buffer memory.

1 When exchanging using binary code

Command format

Specified byte length + 10 bytes

0FH

(L)

(H)

(L)

to

(H)

00H

.....

Write data to the final address for the specified address

Write data to the specified head address + 1

Write data to the specified head address

Special function module No.

Write byte length (1 to 256 bytes)

Head address (When 123456H set to 56H 34H 12H)

ACPU monitor timer (unit 250ms)

PLC No.

Subheader

Remarks

Set to "00H" when specifying the byte length as 256 bytes.

Response format

2 bytes

8FH

End code

Subheader

Example: When writing 750H to 753H of the special function module (X.Y120 to 13F(module No. 13H)) in the same station installed in the E71.

Command (remote node → E71)

0FH

FFH

0AH

00H

50H

07H

00H

04H

13H

00H

01H

23H

45H

67H

Local station

2500ms

4 bytes

Module No.

Buffer memory header address (000750H)

Write data to the address 753H

Write data to the address 752H

Write data to the address 751H

Write data to the address 750H

Response (E71 → remote node)

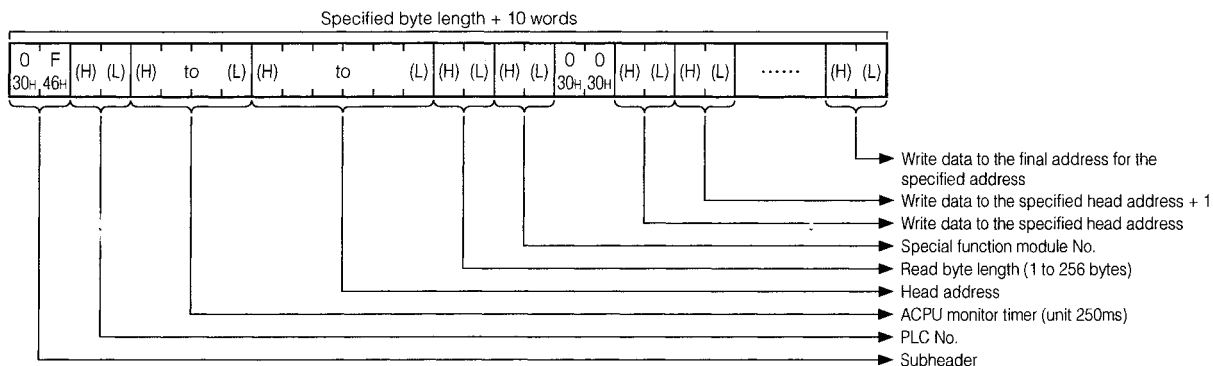
8FH

00H

10 - 67

2 When exchanging using ASCII code

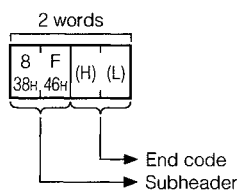
Command format



Remarks

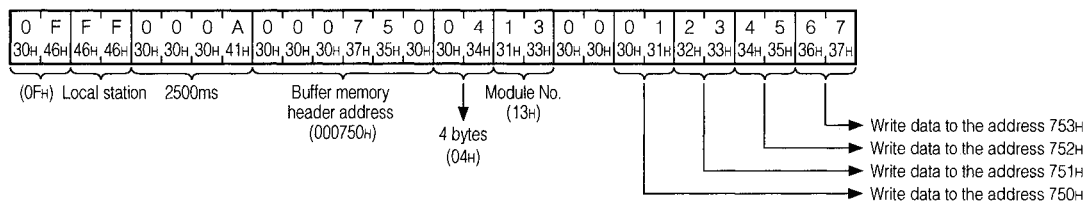
Set to "3030_H" when specifying the byte length as 256 bytes.

Response format

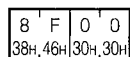


Example: When writing 750_H to 753_H of the special function module (X.Y120 to 13F(module No. 13_H)) in the same station installed in the E71.

Command (remote node → E71)



Response (E71 → remote node)



10.5 Remote RUN/STOP and CPU Model Name Read

This function makes it possible to conduct remote RUN/STOP of the PLC CPU from a remote node and to read what is the model name of the PLC linked to a remote node.

This section explains the control procedure specified contents, method, and example specification when conducting this function.

10.5.1 Commands and Functions

- (1) The commands and functions for remote RUN/STOP and PLC model name read are shown in Table 10.5.

Table 10.5. Functions List

Item	Command/ response classification	Processing description	PLC CPU status		
			Stopped	Running	
				Write possible setting	Write impossible setting
Remote RUN	13 _H	Request remote RUN of the PLC CPU.	○	○	○
Remote STOP	14 _H	Request remote STOP of the PLC CPU.	○	○	○
PLC model name read	15 _H	Reads what the PLC CPU is and whether it is a remote station.	○	○	○

In the PLC CPU status column in the above table the “○” represents execution possible.

Point

- (1) When a remote RUN/STOP is conducted for the PLC CPU (local station) installed in the E71, use the data exchange function for when the PLC CPU is stopped and conduct this function. (Refer to Item 5.6)
If the data exchange function for when the PLC CPU is stopped is not used, the initial process request signal (Y19) and the open process request signal (Y8 to F) will turn off when the local station's CPU is stopped, which will make it no longer possible to exchange between the remote node and the E71.
- (2) When remote RUN/STOP is conducted for a PLC CPU other than one installed in the E71 (remote station), this function can be executed regardless of whether or not the data communication function for when the PLC CPU is stopped is used.

10.5.2 Remote RUN/STOP

1 Remote RUN/STOP control contents

- (a) The PLC CPU status from the remote RUN/STOP from the remote node or the conditions of the RUN/STOP key switch on the front of the PLC CPU are shown in the following table.

		Status of the key switch on the front of the PLC CPU			
		RUN	STOP	PAUSE	STEP-RUN
Specification contents from the remote node	Remote RUN	RUN	STOP	PAUSE	STEP-RUN
	Remote STOP	STOP	STOP	STOP	STOP

Remarks

- ① The PLC CPU will not enter the RUN status when remote RUN is conducted via local station E71 when the corresponding PLC CPU has already been put in the remote STOP status via a special function module, such as another E71.
- ② When conducting a remote RUN, whether or not to RUN is determined after the data memory is cleared by the remote relay M9016 and M9017 status.

Special relay		Data memory status
M9016	M9017	
OFF	OFF	Run without conducting clear.
OFF	ON	Clear other than the latch range specified by the parameter (However, the link X image is not cleared.)
ON	ON/OFF	RUN after clearing all.

Remarks

When conducting remote RUN as described in the above table and the data memory is not cleared, it is necessary to reset (off) the special relay M9016 and M9017.

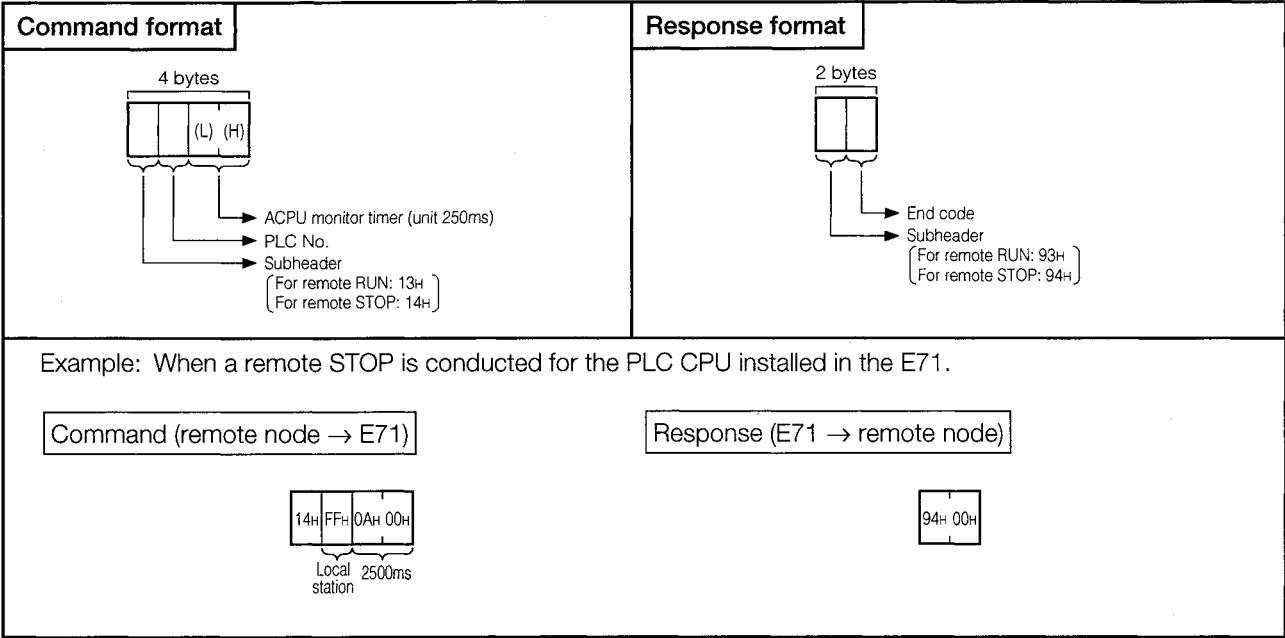
Point

When the power supply has been turned from off to on or the PLC CPU has been reset after a remote RUN/STOP has been conducted from a remote node, delete the remote information.

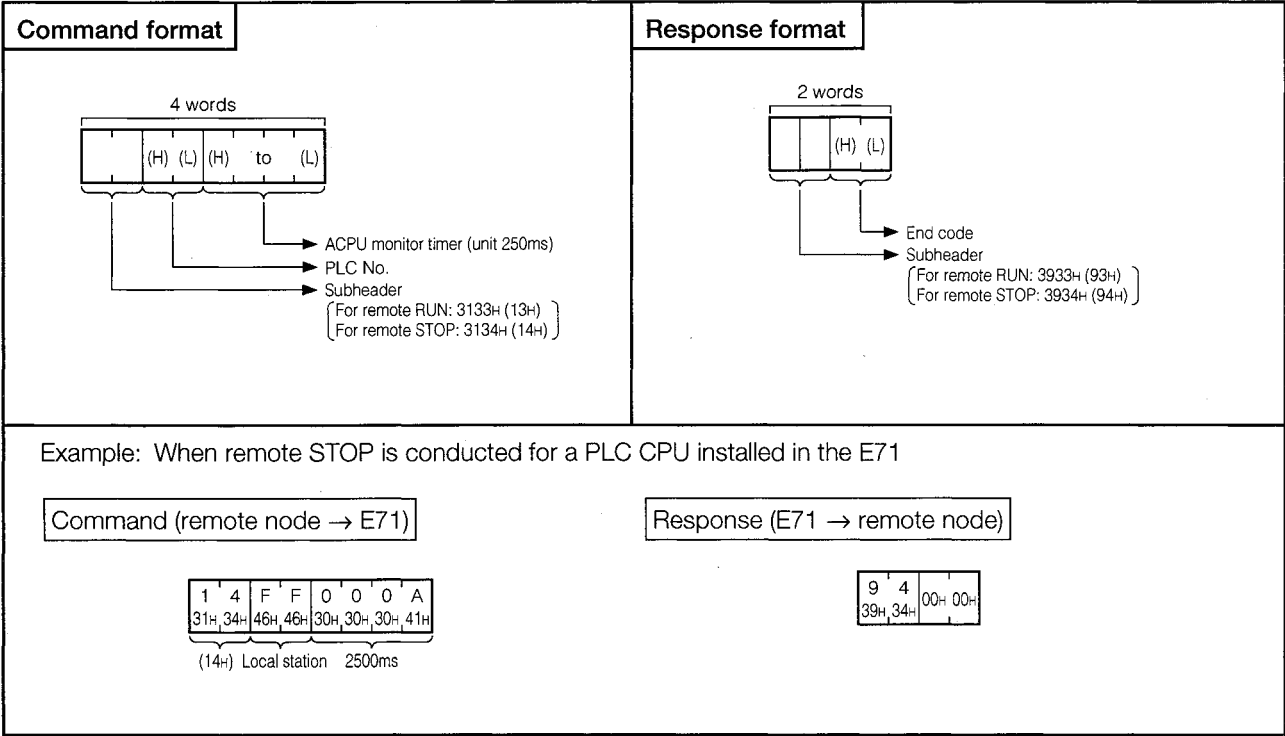
2 Command/response format

This section explains the command/response format when conducting a PLC CPU remote RUN/STOP from a remote node.

(a) When exchanging using binary code



(b) When exchanging using ASCII code



10.5.3 PLC CPU Model Name Read

This function reads the model name of the PLC CPU with which the remote node is communicating via the E71.

1 PLC CPU model name and read code

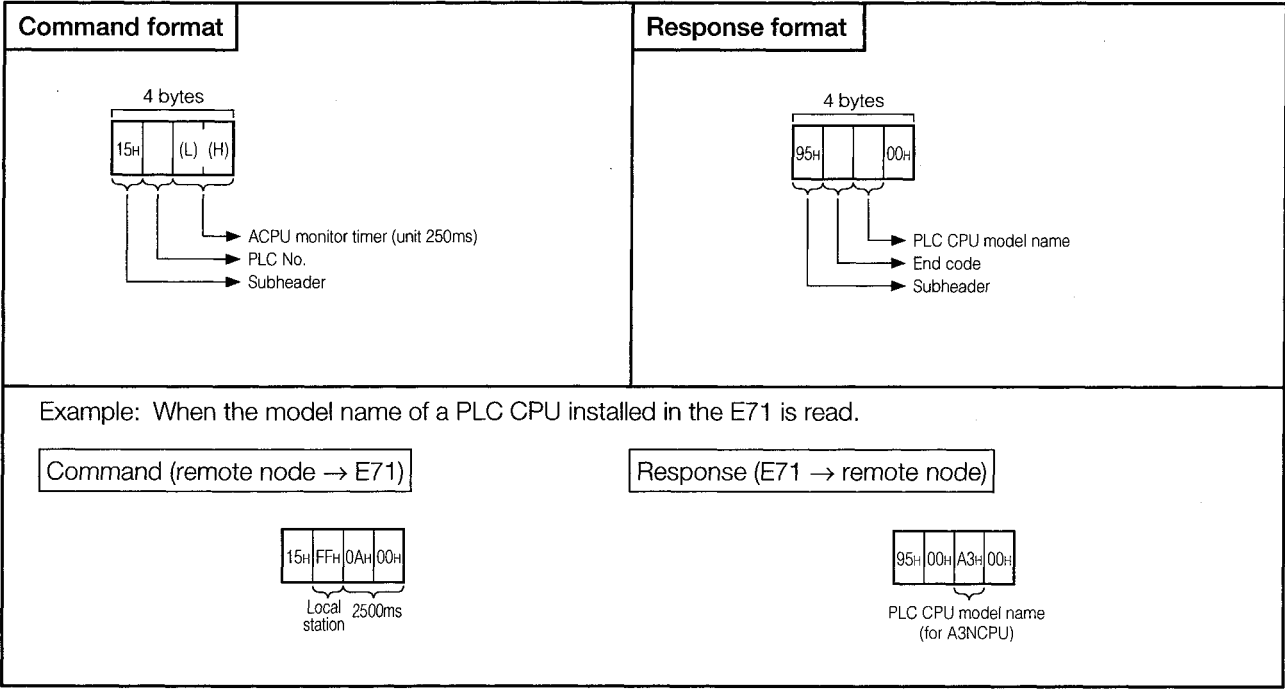
PLC CPU name	Read code (hexadecimal)
A1CPU, A1NCP	A1 _H
A2CPU, A2CPU-S1, A2NCP, A2NCP-S1, A2SCPU, A2SCPU-S1	A2 _H
A3CPU, A3NCP, A1SHCP, A1SJHCP, A2SHCP, A2SHCP-S1	A3 _H
A2ACPU, A2UCPU, A2ASCP, Q2ACPU, Q2ASCP, Q2ASHCP	92 _H
A2ACPU-S1, A2UCPU-S1, A2ASCP-S1, Q2ACPU-S1 Q2ASCP-S1, Q2ASHCP-S1	93 _H
A3ACPU, A3UCPU, A4UCPU, Q3ACPU, Q4ACPU, Q4ARCP, AJ72LP25/BR15, AJ72QLP25/QBR15	94 _H
A0J2HCP, A1SCPU, A1SCPU-S1, A1SJCP	98 _H
A2CCPU	9A _H
AJ72P25/R25	AB _H

2

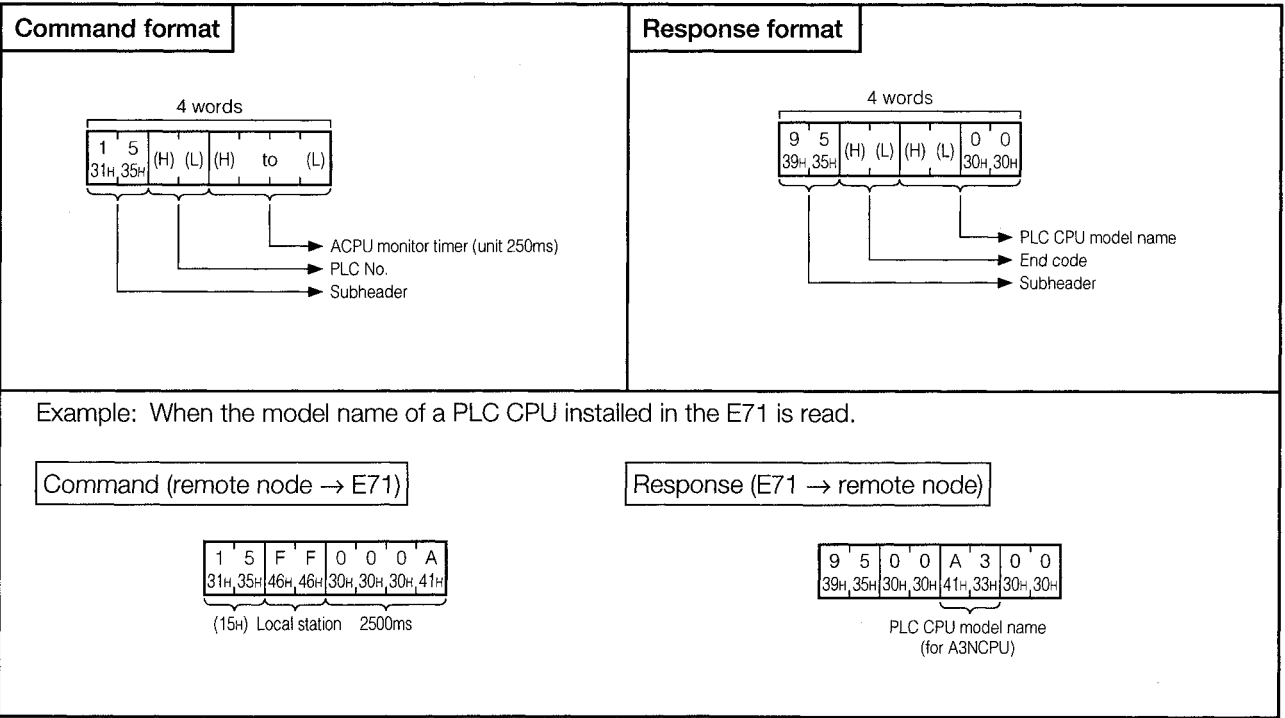
Command/response format

This section explains the command/response format for reading the name of the PLC CPU being used from the remote node.

(a) When exchanging using binary code



(b) When exchanging using ASCII code



10.6 Sequence Program Read/Write

This function is used to control the read and storage of various programs (main/sub sequence program, main/sub microcomputer program), parameter data, and comment data to and from the PLC CPU by a remote node; and to write programs, parameter data, and comment data from a remote station in accordance with the control contents to the PLC CPU.

10.6.1 Precautions When Reading/Writing Programs

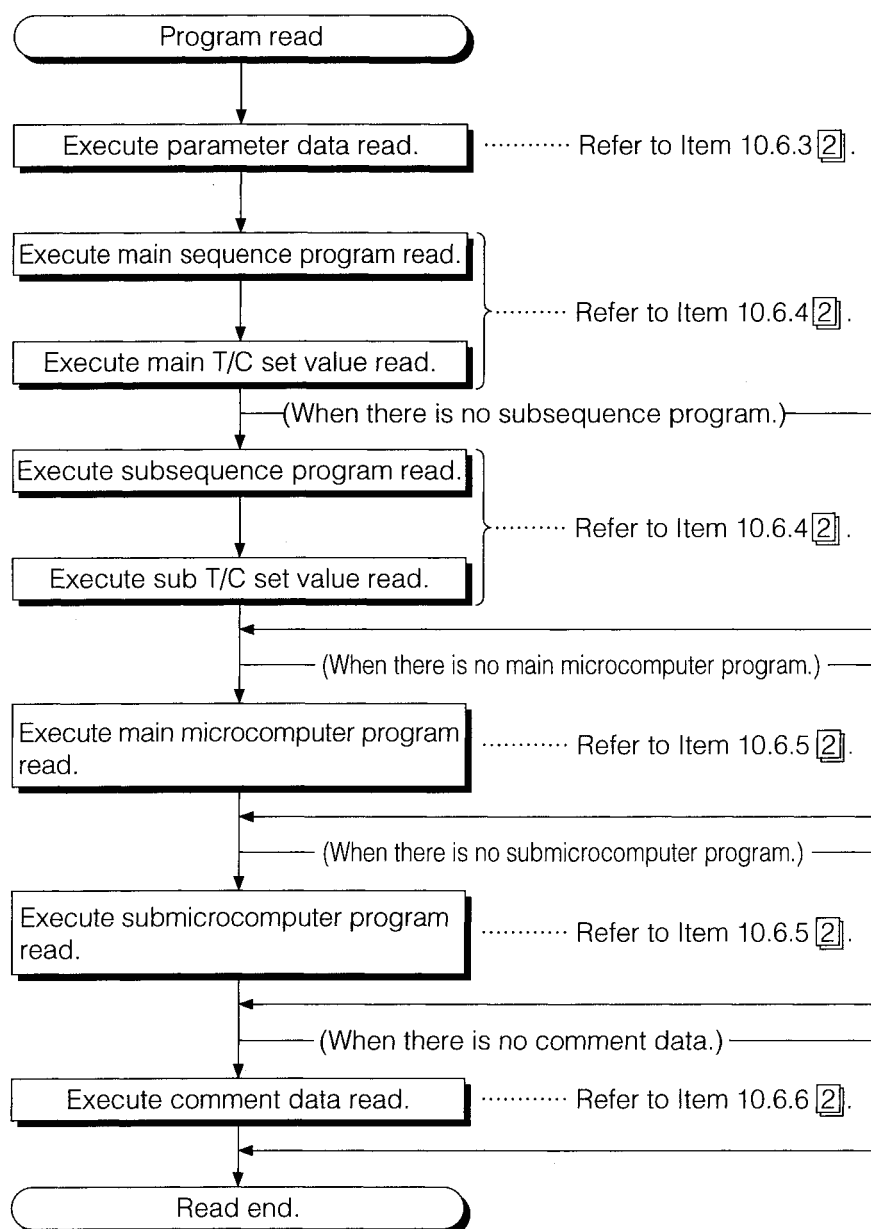
This section explains precautions when reading/writing programs.

- (1) When conducting a program read, read all of the sequence programs, microcomputer programs, parameter data, and comment data areas in the PLC CPU. When writing, write all of the read and stored data to the PLC CPU. If all areas are not written the PLC CPU will not operate correctly.
- (2) Before writing programs, write parameter data and execute a parameter analysis request. Otherwise, the parameters in the PLC CPU user memory will be changed but the parameters stored in the work area by the ACPU for operation will remain unchanged. Therefore, if a peripheral device is loaded and operated after the parameters are changed, processing will be carried out with the previous parameters, which are still stored in the work area.
- (3) The number of points which can be processed per communications is fixed. When reading or writing data, divide the data into several groups to read or write the entire area. Parameter data should be divided into 3k bytes. Other data should be divided into units of data determined by parameter setting.
- (4) When the PLC CPU, which reads or writes the sequence program, is an AnUCPU, network parameters are read or written together with other parameters.
To read or write the network parameters, use the parameter memory read/write function described in Section 10.6.3.
- (5) A program read by this function into the remote node cannot be modified on the remote node. Keep it as a backup copy.
- (6) When the PLC CPU is an AnA or AnUCPU, the SFC program is read or written by the main microcomputer program read/write function.
To read or write SFC program, use the microcomputer program read/write function described in Section 10.6.5.
- (7) Conduct read/writer for the A4UCPU subsequence program for the sub 1. Read and write cannot be conducted for sub 2 to sub 4.

10.6.2 Program Read/Write

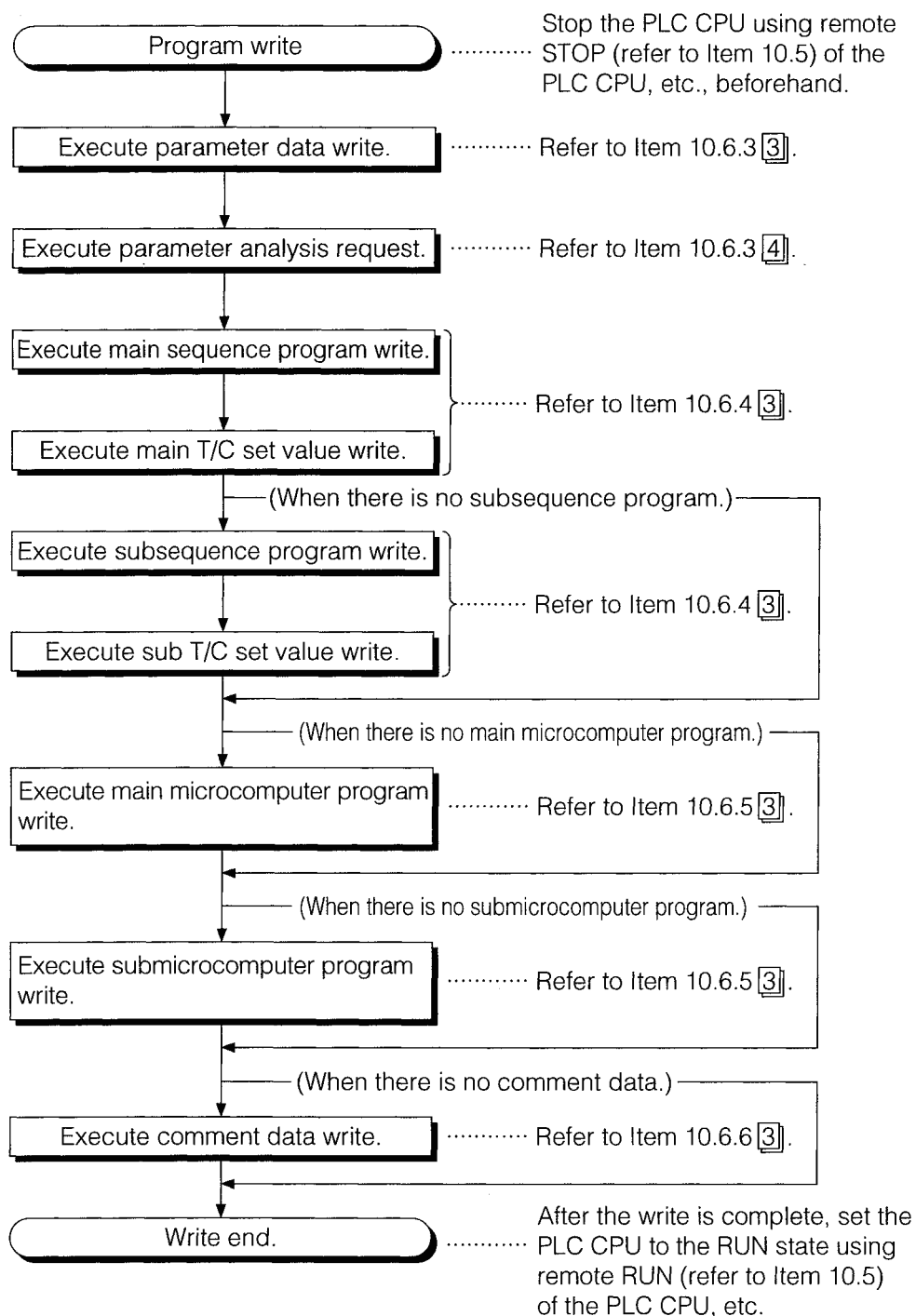
This section explains the processing procedure when conducting program read/write.

1 Read procedure



2

Write procedures



10.6.3 Parameter Memory Read, Write, and Analysis Request

This section explains the control procedure specification contents, method, and example specification when reading or writing the parameter memory contents of the PLC CPU.

1

Commands and addresses

(a) The functions used to read/write parameters are shown in Table 10.6

Table 10.6 Functions List

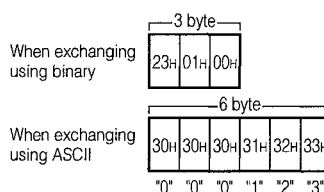
Item	Command/ response classification	Processing description	Number of processing points con- ducted in one exchange	PLC CPU status		
				Stopped	Running Write possible setting	Running Write impossible setting
Batch read	10 _H	Reads the PLC CPU parameter contents.	256 bytes	○	○	○
Batch write	11 _H	Writes the PLC CPU parameter contents.		○	×	×
Analysis request	12 _H	Causes the PLC CPU to recognize and check the switching parameter contents.		○	×	×

In the PLC CPU status column in the above table the "○" represents executable and the "x" represents not executable.

(b) Parameter addresses

The parameter memory area is the 3k bytes from 0_H to BFF_H. As shown below the address specification is 3 bytes when exchanging using binary and 6 bytes when exchanging using ASCII.

Example: When specifying address 123_H.



Point

When changing the parameter memory contents, be sure to conduct a parameter analysis request after writing all of the data to be changed.

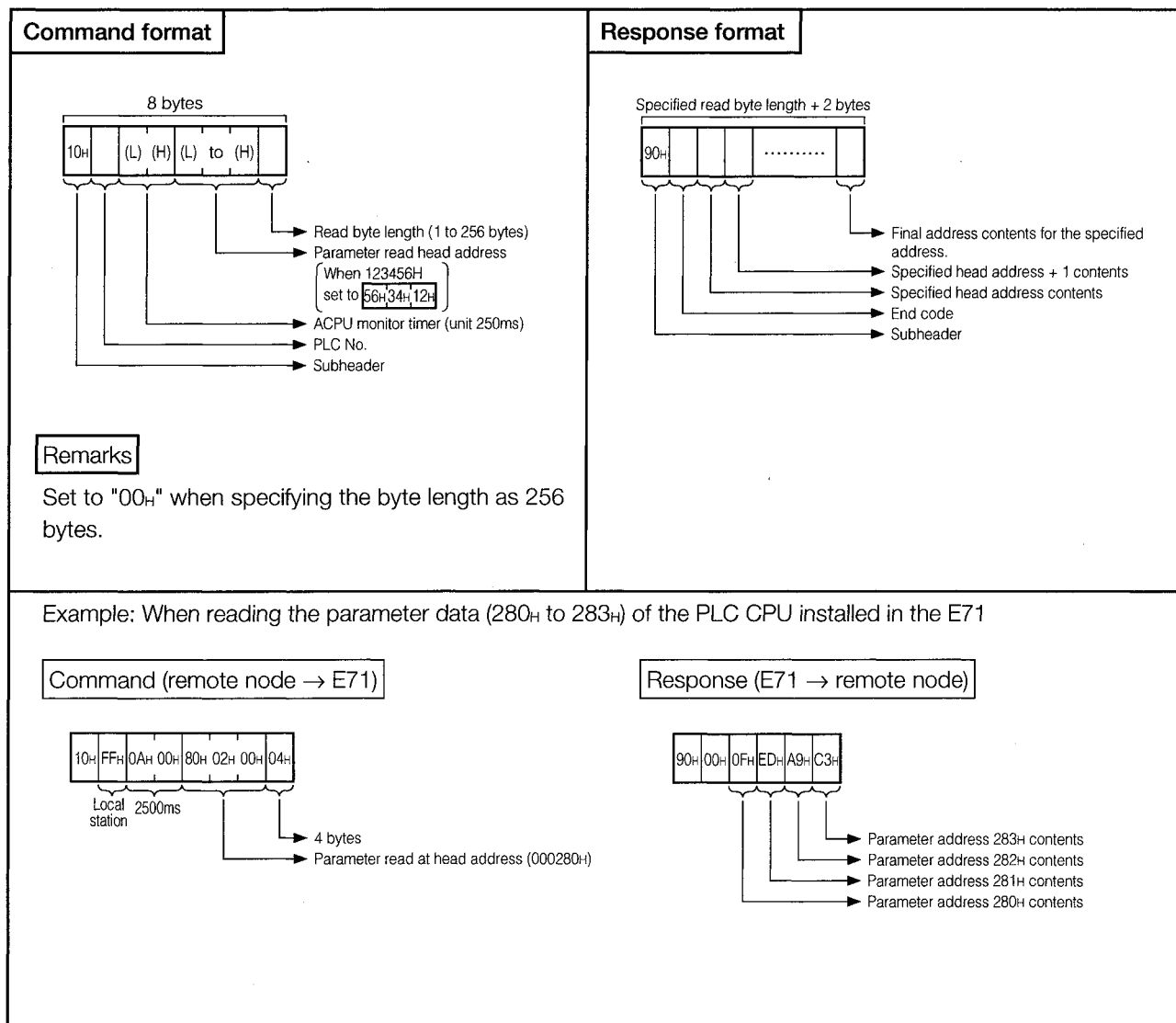
If this is not done the parameters in the CPU user memory will be changed but the parameter contents stored in the work area used by the PLC CPU for operation will not be changed, so processing will be conducted using the parameter contents before the change (contents stored in the work area) even if the peripheral equipment is installed and operated after the change.

2

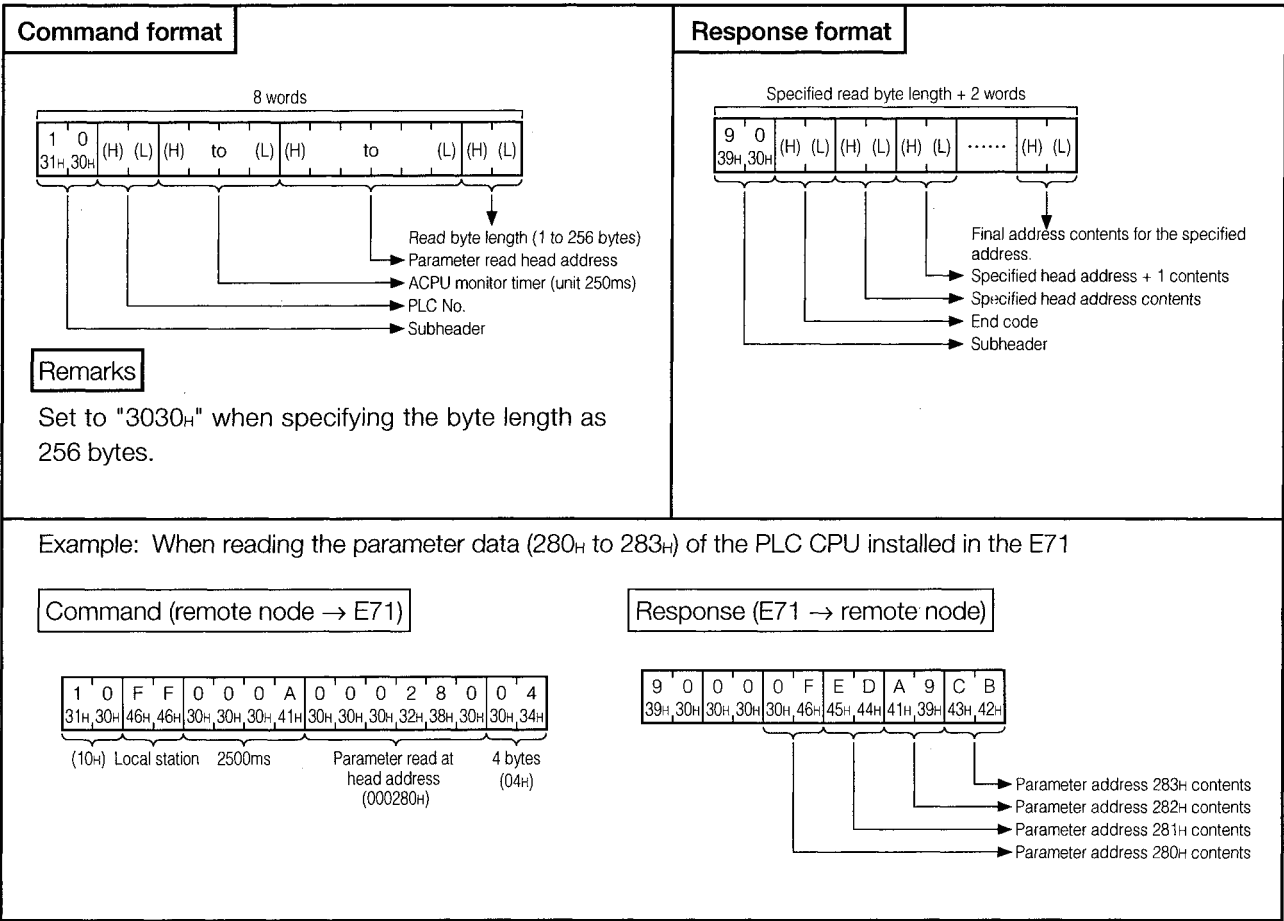
Batch read

This section explains the command/response format when reading the PLC CPU parameter memory contents.

(a) When exchanging using binary code



(b) When exchanging using ASCII code

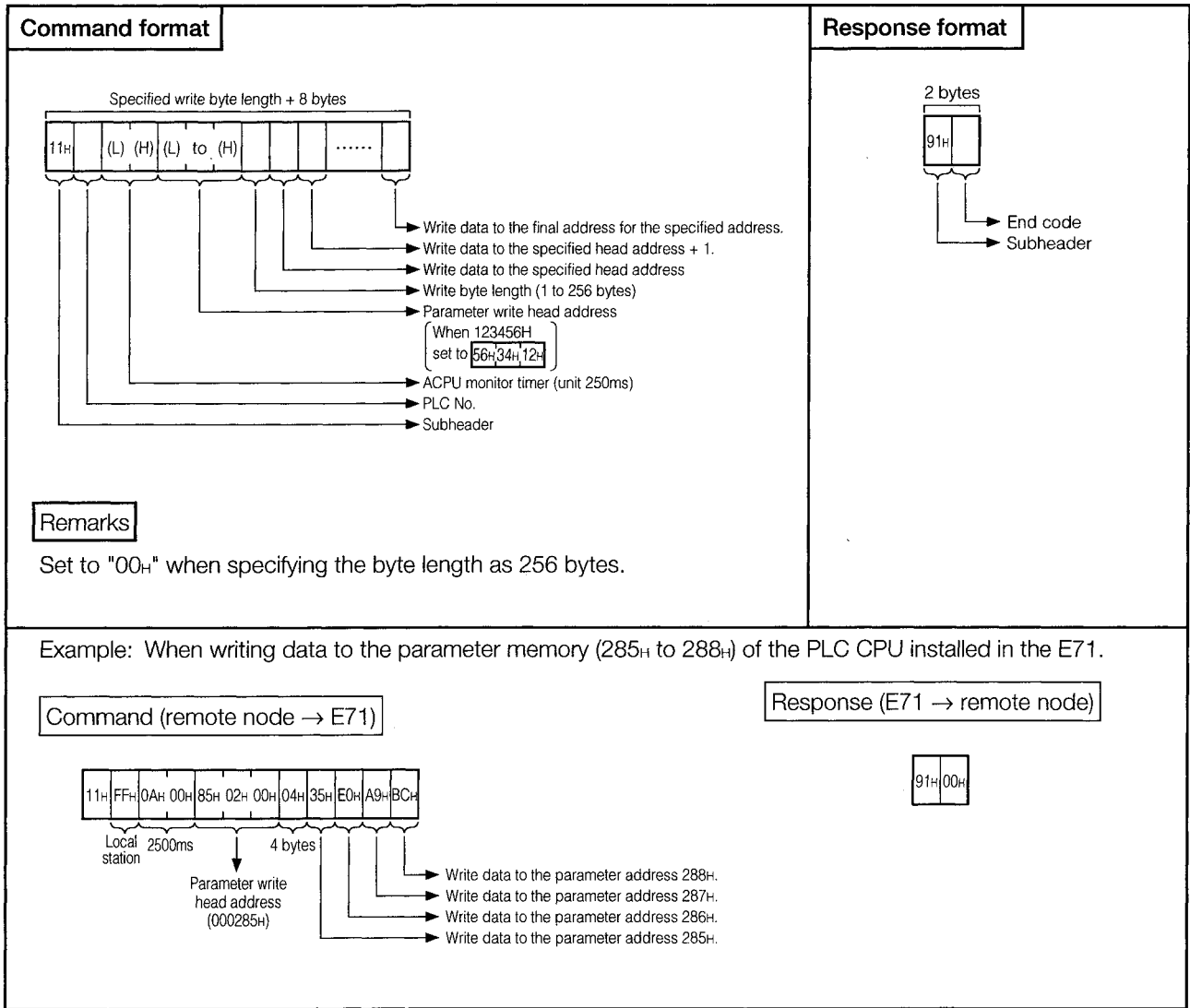


3

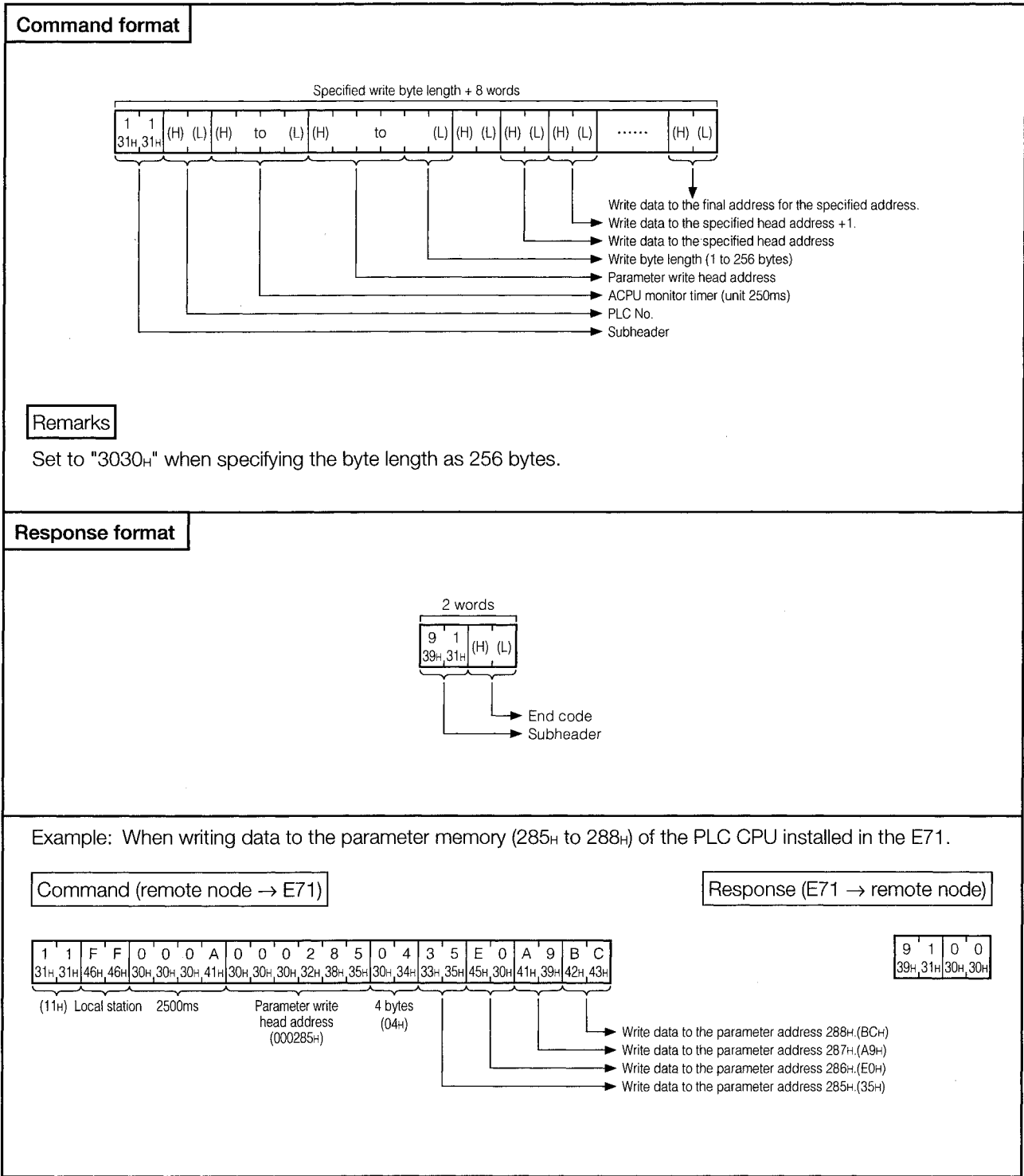
Batch write

This section explains the command/response format when writing data to the PLC CPU buffer memory.

(a) When exchanging using binary code



(b) When exchanging using ASCII code

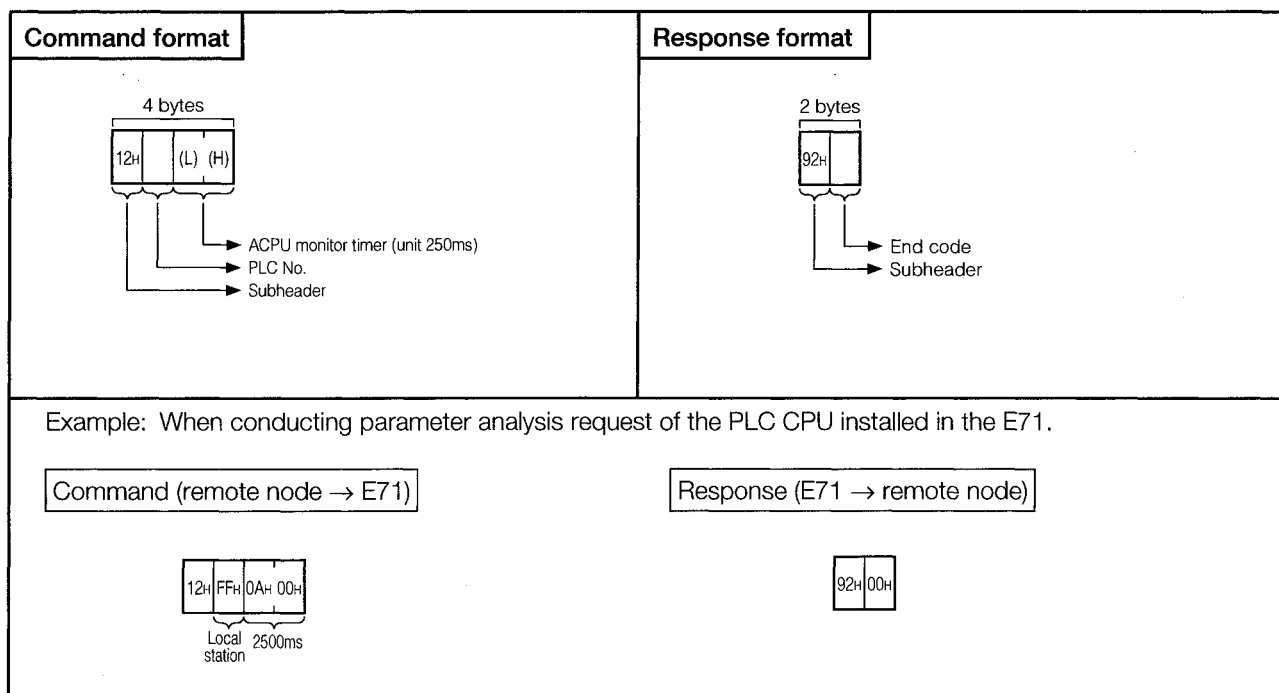


4

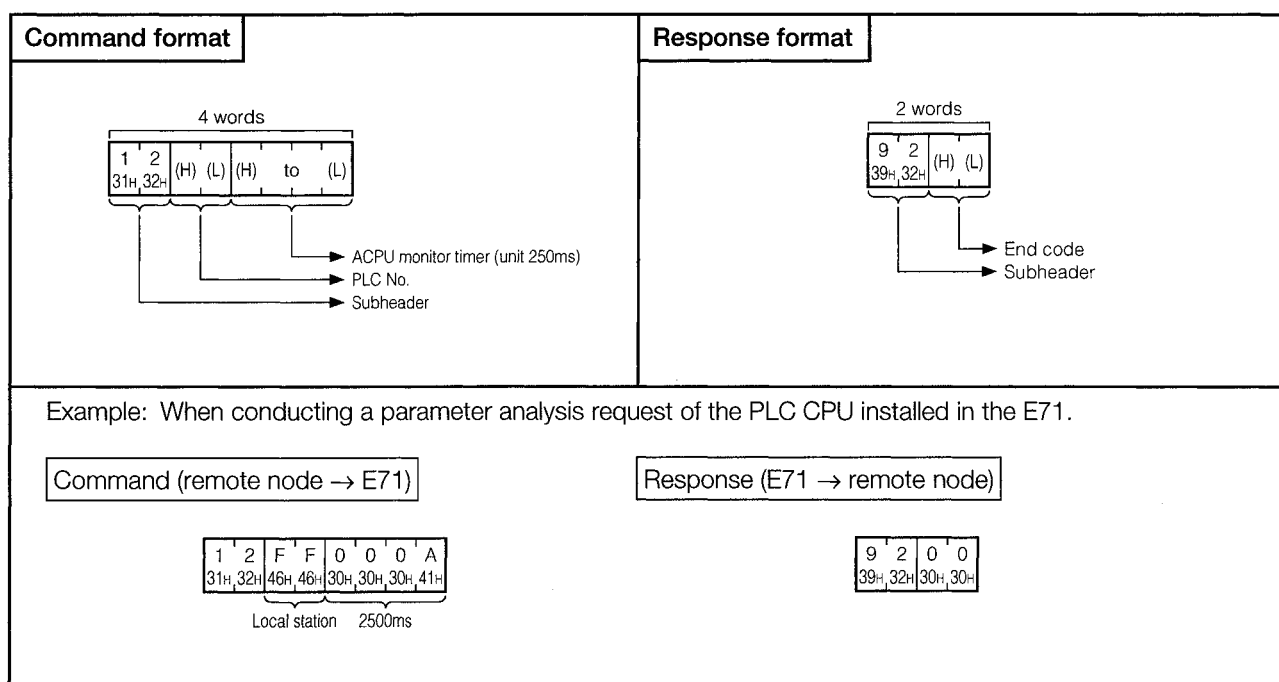
Analysis request

This section explains the command/response format when conducting a parameter data analysis request of the PLC CPU. The analysis request makes the PLC CPU recognize the parameter changes conducted when the parameter data is changed and is a command that causes the changed parameters to be written to the CPU. If analysis request is not conducted the PLC CPU will not operate with the changed parameters.

(a) When exchanging using binary code



(b) When exchanging using ASCII code



10.6.4 Sequence Program Read/Write

This section explains the control procedure specification contents, method, and example specification when reading and writing the PLC CPU's sequence program.

1

Commands and setting method

(a) The functions used for reading/writing the sequence program are shown in Table 10.7.

Table 10.7 Function List

Item			Command/ response classification	Processing description	Number of processing points con- ducted in one exchange	PLC CPU status		
						Stopped	Running	
							Write possible setting	Write impossible setting
Batch read	Main	Program	0A _H	Reads the main sequence program.	256 steps	○	○	○
		T/C set value		Reads the T/C set values used by the main sequence program.	256 points			
	Sub	Program	0B _H	Reads the sub sequence program.	256 steps	○	○	×
		T/C set value		Reads the T/C set values used by the subsequence program.	256 points			
Batch write	Main	Program	0C _H	Writes the main sequence program.	256 steps	○	○*	×
		T/C set value		Writes the T/C set values used by the main sequence program.	256 points	○	○	×
	Sub	Program	0D _H	Writes the subsequence program.	256 steps	○	○*	×
		T/C set value		Writes the T/C set values used by the subsequence program.	256 points	○	○	×

In the PLC CPU status column in the above Table the "○" represents execution possible and the "x" represents execution not possible.

* All of the following conditions must be met when conducting program write during RUN.

- ① The PLC CPU is an A3, A3N, A3A, A3U, or A4U
- ② A program that is not operating. (Shows subprogram when the main program is running.)
- ③ The PLC CPU special relay is in the following state.
 - Ⓐ M9050 (Signal flow replacement point) Off (A3CPU only)
 - Ⓑ M9051 (CHG instruction execution prohibited) .. On

* The A4U subsequence program read/write is conducted for sub 1. Read/write is not conducted for sub 2 to sub 4.

(b) Sequence program step No. specification

The sequence program step Nos. are specified using hexadecimal numbers as shown in Table 10.8.

Table 10.8 Step Nos.

Step No.	Set value
Step 0	0000 _H
Step 1	0001 _H
to	to
Step 30719 (30k)	77FE _H

(c) Device No. specification when reading/writing T/C set values

The device Nos. used when reading/writing T/C set values are set using the codes shown in Table 10.9. Reading/writing of T/C set values is done in the range of T0 to 255 and C0 to 255. The T256 to 2047 and C256 to 1023 set values cannot be read/written. To read/write setting values, conduct a device memory read/write.

Table 10.9 T/C Set Value Specification

Device No.	Setting code
T0's set value	FE00 _H
T1's set value	FE01 _H
to	to
T255's set value	FEFF _H
C0's set value	FF00 _H
C1's set value	FF01 _H
to	to
C255's set value	FFFF _H

The relationship between the device No. and the setting code is shown below.

Timer: $T_m = FE00H + n$

Counter: $C_m = FF00H + n$

m : Device No.

n : The device No. is converted into a hexadecimal value.

(d) T/C set value contents

The T/C set value is exchanged in hexadecimal numbers as shown in Table 10.10. When writing over the T/C set values from a remote node via a E71, specify the setting data shown in Table 10.10.

Example: Setting data when the T10's set value K10 is changed to K20..... 0014_H

Setting data when the T11's set value D30 is changed to D10..... 8014_H

Table 10.10 T/C Set Value Data Specification

Example circuit in the program	Setting contents in the program	Set value
	K0 K1 to K9 K10 to K32767	0000 _H 0001 _H to 0009 _H 000A _H to 7FFF _H
	D0 D1 D2 to D1023	8000 _H 8002 _H 8004 _H to 87FE _H

The relationship between the setting contents and setting data in the program is as follows.

$K_m = 0000_H + n$

$D_m = 8000_H + 2n$

m : Device No.

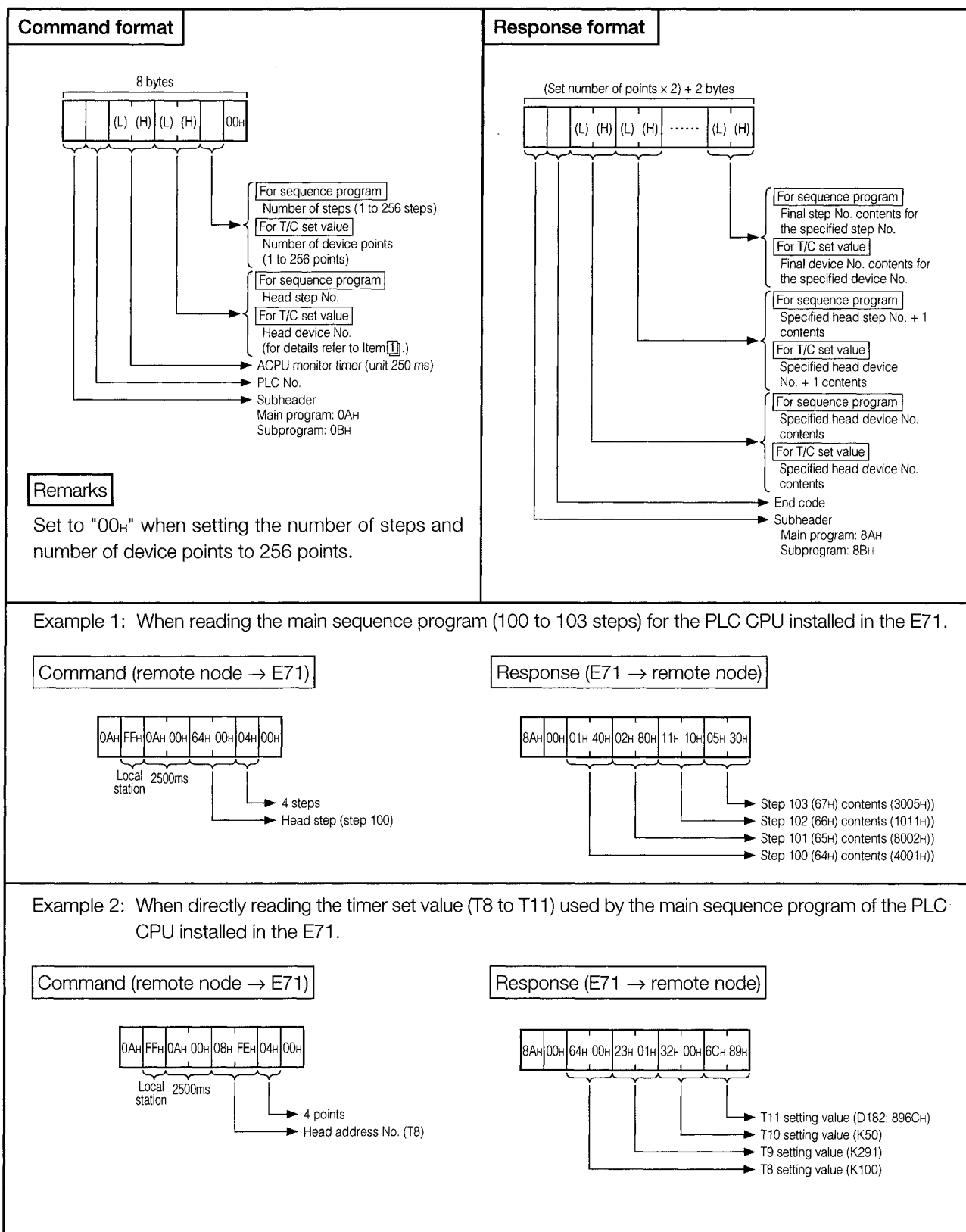
n : The device No. is converted into a hexadecimal number value.

2

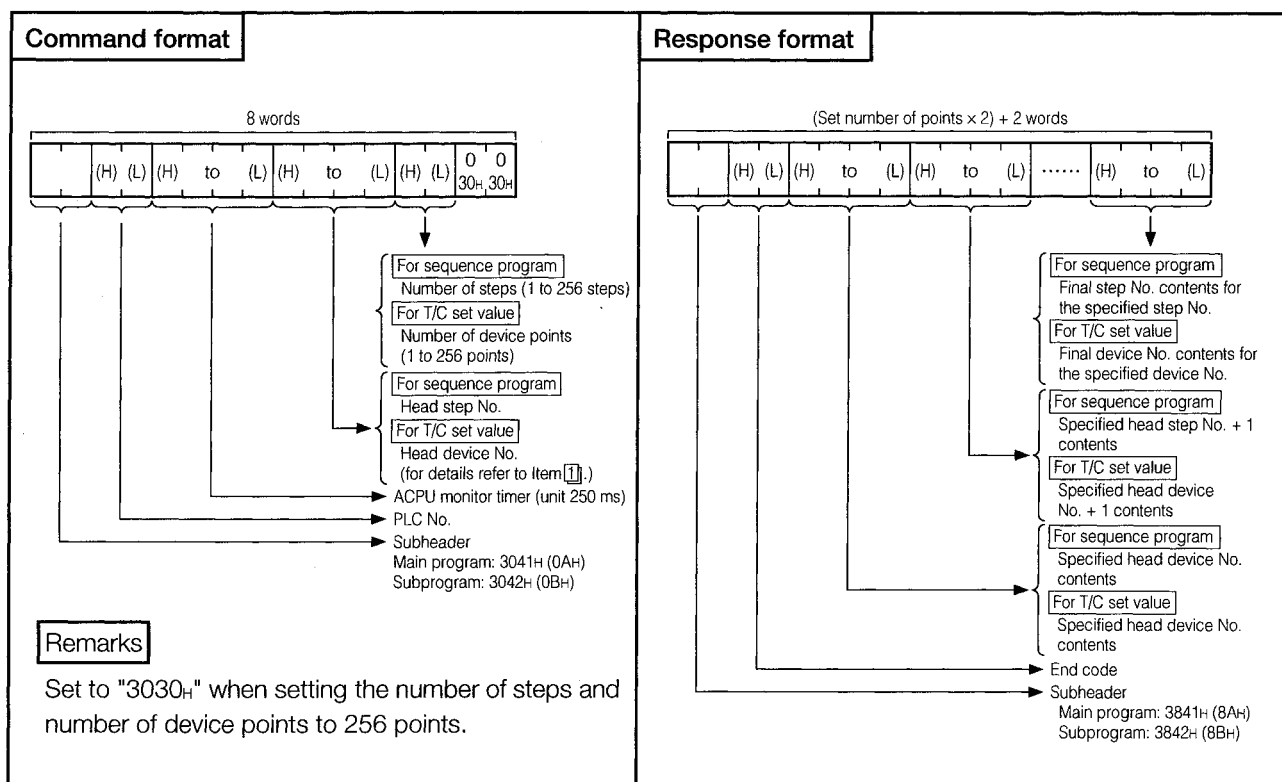
Batch read

This section explains the command/response format when batch reading the sequence program contents (machine language), timer (T), and counter (C) set values.

(a) When exchanging using binary code

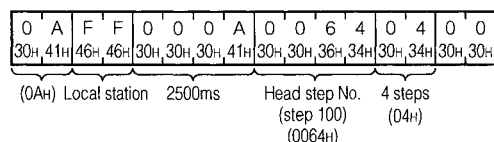


(b) When exchanging using ASCII code

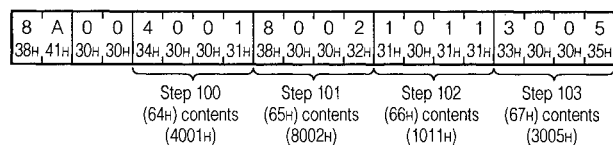


Example 1: When reading the main sequence program (100 to 103 steps) for the PLC CPU installed in the E71.

Command (remote node → E71)

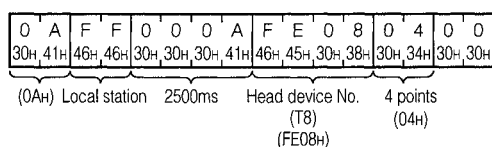


Response (E71 → remote node)

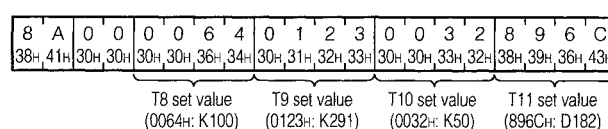


Example 2: When directly reading the timer set value (T8 to T11) used by the main sequence program of the PLC CPU installed in the E71.

Command (remote node → E71)



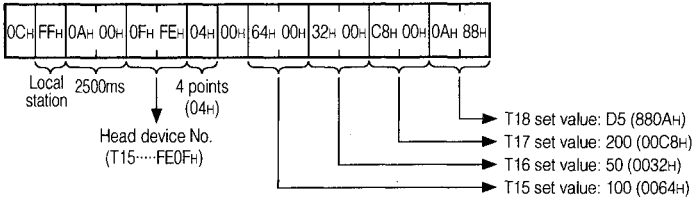
Response (E71 → remote node)



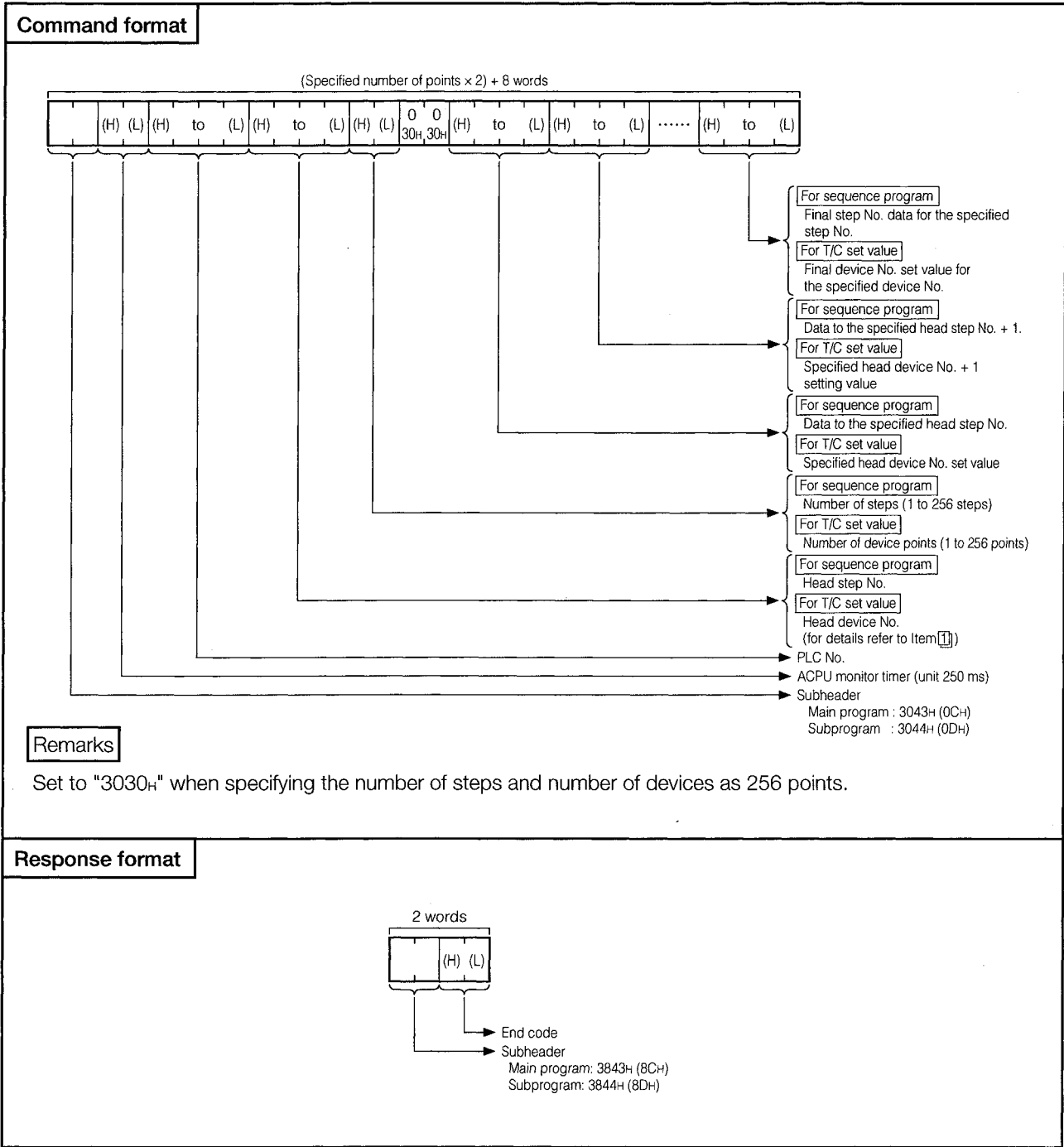
Example 2: When changing the set value of the timer (T15 to T18) used by the main sequence program of the PLC CPU installed in the E71.

Command (remote node → E71)

Response (E71 → remote node)



(b) When exchanging using ASCII code

**Remarks**

Set to "3030H" when specifying the number of steps and number of devices as 256 points.

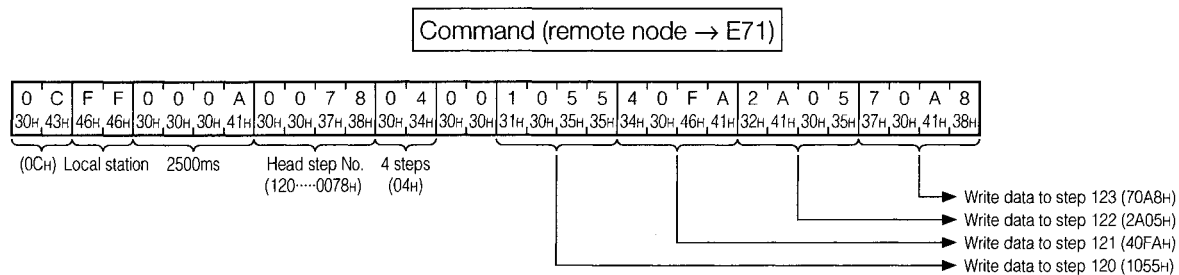
Response format

2 words

(H)	(L)
-----	-----

End code
Subheader
Main program: 3843H (8CH)
Subprogram: 3844H (8DH)

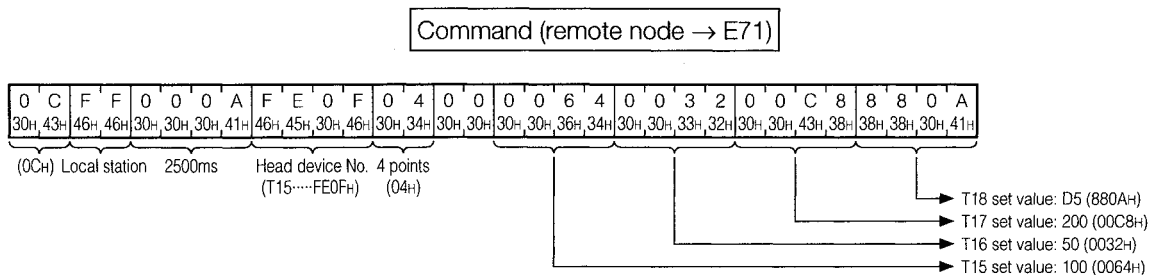
Example 1: When writing the main sequence program (steps 120 to 123) for the PLC CPU installed in the E71.



Response (E71 → remote node)

8	C	0	0
38H	43H	30H	30H

Example 2: When changing the set value of the timer (T15 to T18) used by the main sequence program of the PLC CPU installed in the E71.



Response (E71 → remote node)

8	C	0	0
38H	43H	30H	30H

10.6.5 Microcomputer Program Read/Write

This section explains the control procedure specification contents, method, and example specification when reading/writing a PLC CPU microcomputer program.

1 Commands and addresses

This section explains the command/response classification and program addresses when reading/writing microcomputer programs.

(a) The functions used to read/write microcomputer programs are shown in Table 10.11.

Table 10.11 Functions List

Item		Command/ response classification	Processing description	Number of processing points con- ducted in one exchange	PLC CPU status		
					Stopped	Running	
						Write possible setting	Write impossible setting
Batch read	Main	1E _H	Reads the main sequence microcomputer program.	256 bytes	○	○	○
	Sub	1F _H	Reads the subsequence microcomputer program.				
Batch write	Main	20 _H	Writes the main sequence microcomputer program.		○	○*	×
	Sub	21 _H	Writes the subsequence microcomputer program.				

In the PLC CPU status column in the above table the "○" represents execution possible and the "x" represents execution not possible.

* All of the following conditions must be met in order to conduct program write during RUN.

- ① PLC CPU is an A3, A3N, A3A, A3U, or A4U.
- ② A program that is not operating. (Shows the sub program if the main program is running.)
- ③ The PLC CPU special relay is in the following status.
 - Ⓐ M9050 (Signal flow replacement point) Off (A3CPU only)
 - Ⓑ M9051 (CHG instruction execution prohibited) On

Point

When the PLC CPU is an AnA/AnUCPU, the SFC program reads/writes the main microcomputer program using the read/write functions. (Write cannot be done while the PLC CPU is running.) When reading from or writing to the SFC program, conduct the read/write in the microcomputer program capacity and microcomputer program address range shown in 1 (b). The microcomputer program capacity is the capacity that is set in the GX Developer memory capacity setting or the MELSAP-II function's SFC area capacity setting.

(b) Microcomputer program addresses

The microcomputer program addresses that are specified by the control procedures are conducted by the contents shown below.

① The address range that can be specified by each CPU is shown in the following table.

CPU name	Microcomputer program capacity	Microcomputer program addresses
A1SCPU (S1) A1SJCPU A1SHCPU A1SJHCPU A0J2HCPU A2CCPU A2CJCPU	Maximum 14k bytes	0000 _H to 37FE _H
A1CPU A1NCPU	Maximum 10k bytes	0000 _H to 27FE _H
A2SCPU (S1) A2SHCPU (S1) A2ASCPU (S1) A2CPU (S1) A2NCPU (S1) A2ACPU (S1) A2UCPU (S1)	Maximum 26k bytes	0000 _H to 67FE _H
A3CPU A3NCPU A3ACPU A3UCPU A4UCPU	Maximum 58k bytes for both main and sub	0000 _H to E7FE _H

② When exchanging using ASCII code, the address is converted to ASCII code with a four digit hexadecimal number.

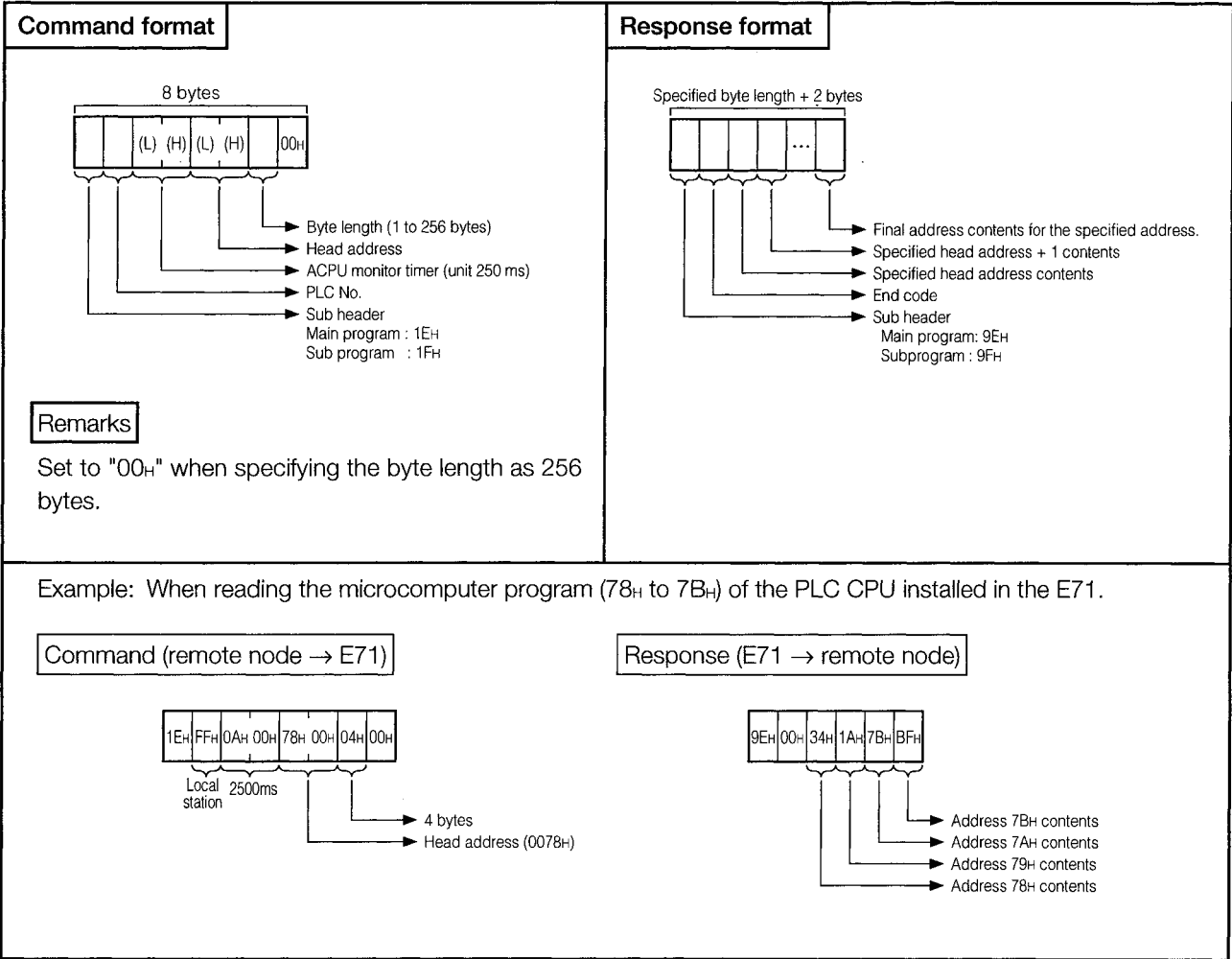
③ When the head address + number of bytes – 1 ≤ is not the microcomputer program capacity, an error (End error 57_H) will occur.

2

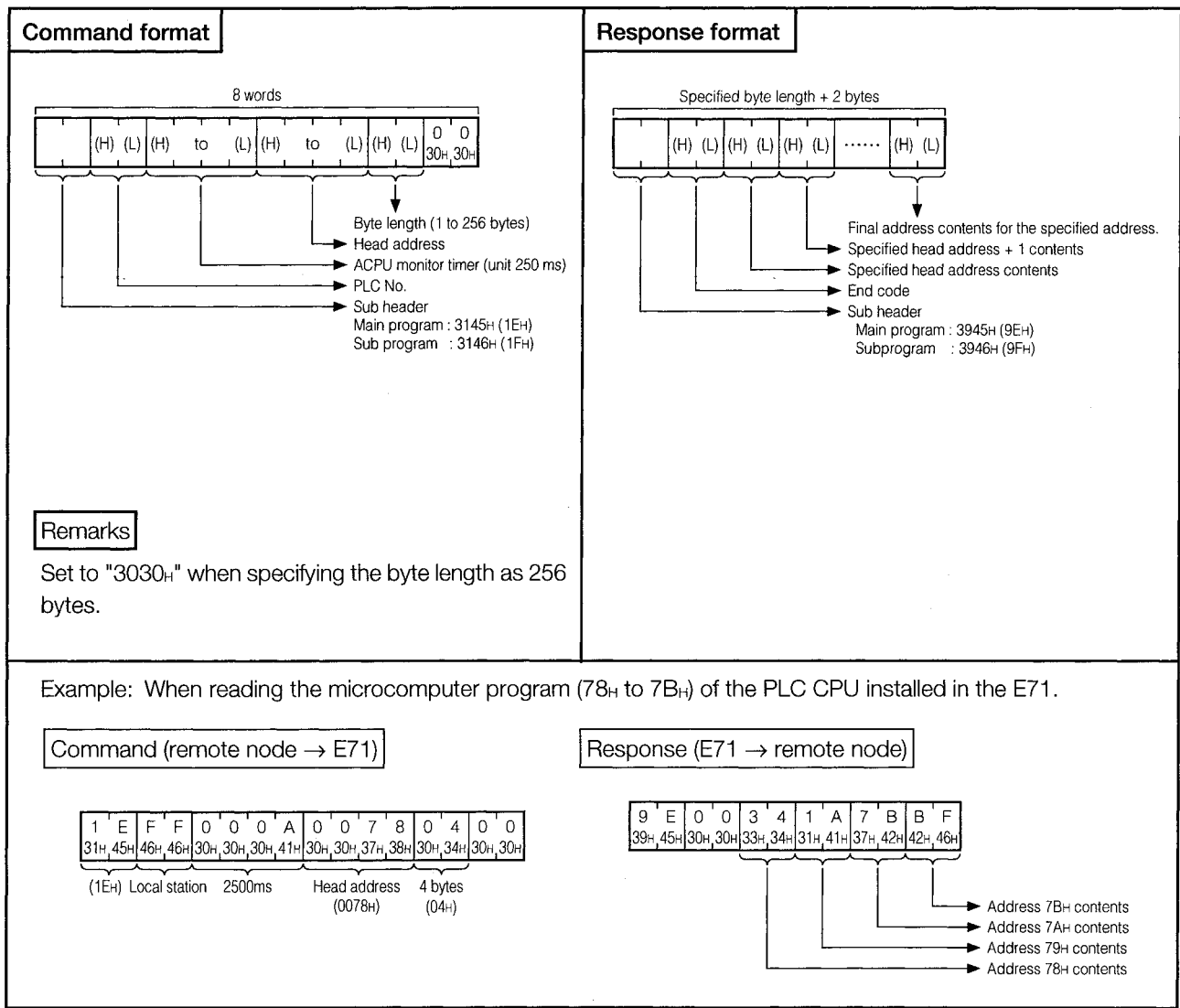
Batch read

This section explains the command/response format when batch reading the microcomputer program contents.

(a) When exchanging using binary code



(b) When exchanging using ASCII code



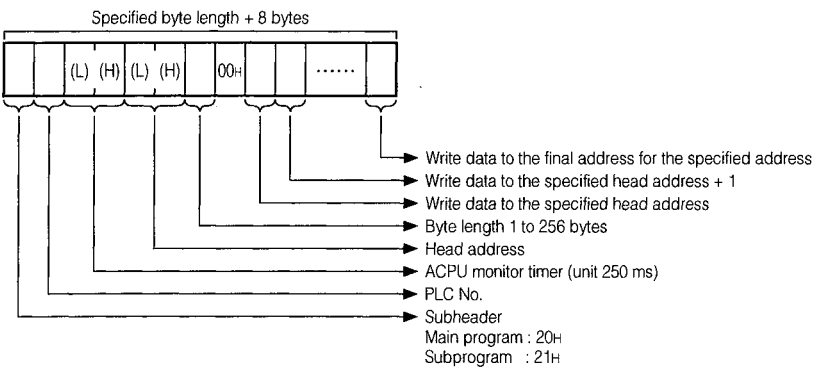
3

Batch write

This section explains the command/response format when batch writing the contents of the microcomputer program.

(a) When exchanging using binary code

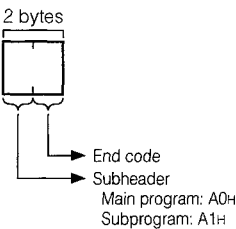
Command format



Remarks

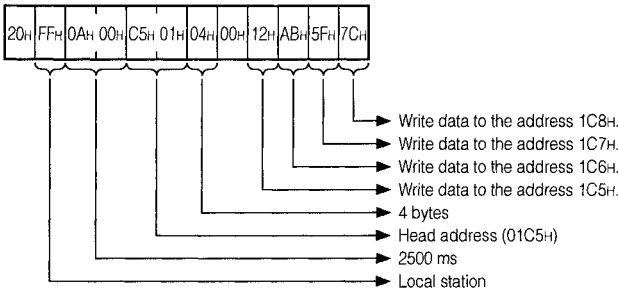
Set to "00H" when specifying the byte length to 256 bytes.

Response format



Example: When writing the main microcomputer program (1C5H to 1C8H) of the PLC CPU installed in the E71

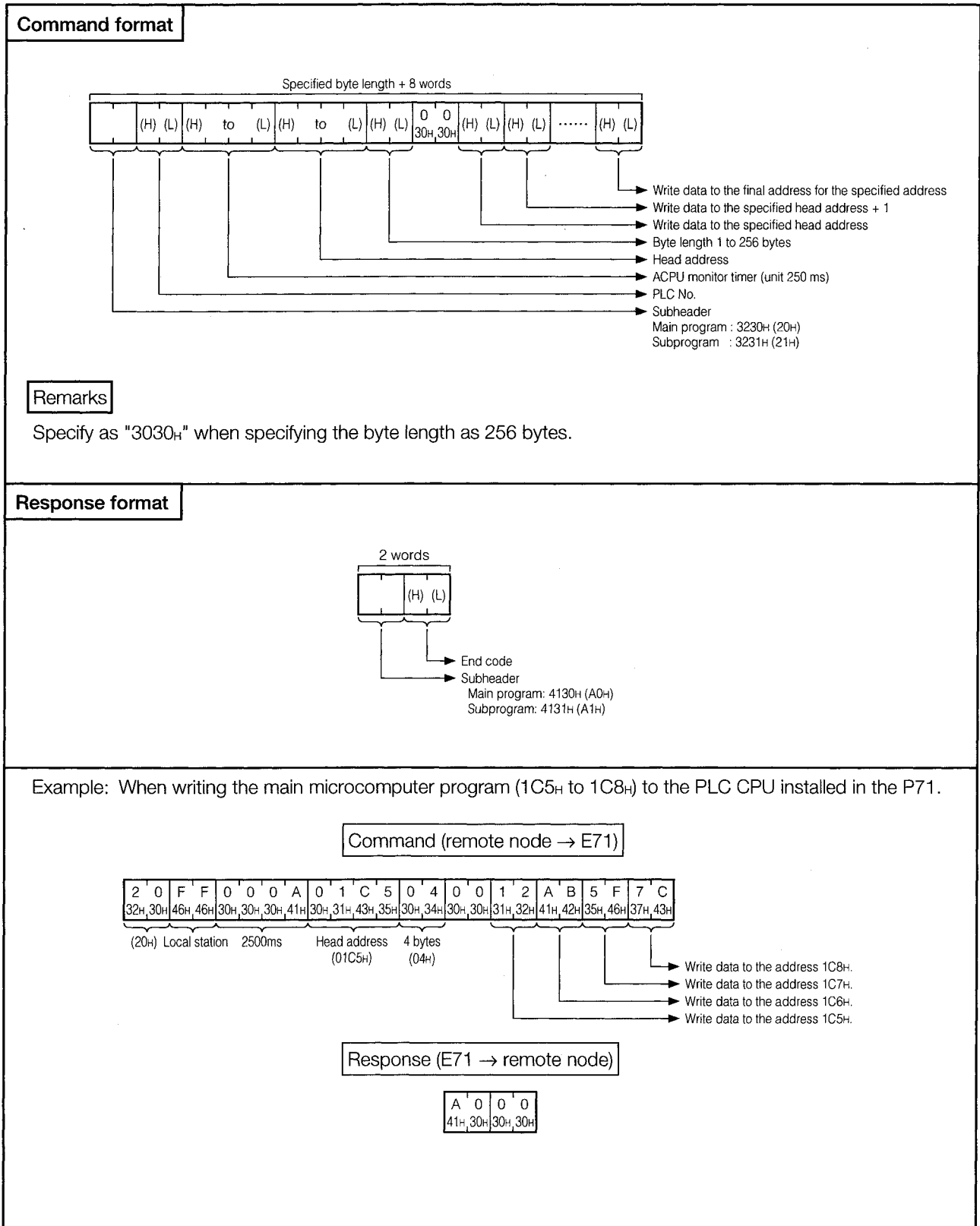
Command (remote node → E71)



Response (E71 → remote node)



(b) When exchanging using ASCII code



10.6.6 Comment Read/Write

This section explains the control procedure specification contents, method, and example specification when reading/writing PLC CPU comment data.

1

Commands and addresses

This section explains the command/response classification and comment data addresses when reading/writing comment data.

(a) The functions used to read/write comment data are shown in Table 10.12.

Table 10.12 Function List

Item	Command/ response classification	Processing description	Number of processing points con- ducted in one exchange	PLC CPU status		
				Stopped	Running	
					Write possible setting	Write impossible setting
Batch read	1C _H	Reads the comment memory contents.	256 bytes	○	○	○
Batch write	1D _H	Writes the data in the comment memory.		○	○	×

In the status column for the PLC CPU in the above table the capital "○" represents execution possible and the capital "x" represents execution not possible.

(b) Comment memory address

The comment data storage area can be controlled using the corresponding address for a head address of 00_H. For example, if the parameter comment capacity is 2k bytes, the range that can be specified by the head address is 00_H to 7FF_H.

- ① The comment memory has a maximum capacity of 64k bytes. The comment data address range is determined by the parameter setting capacity.
- ② The comment memory address specification is done in hexadecimal numbers.
- ③ If the head address + specified number of bytes – 1 ≤ is not the comment memory capacity, an error (end code 57_H) will occur.

Point

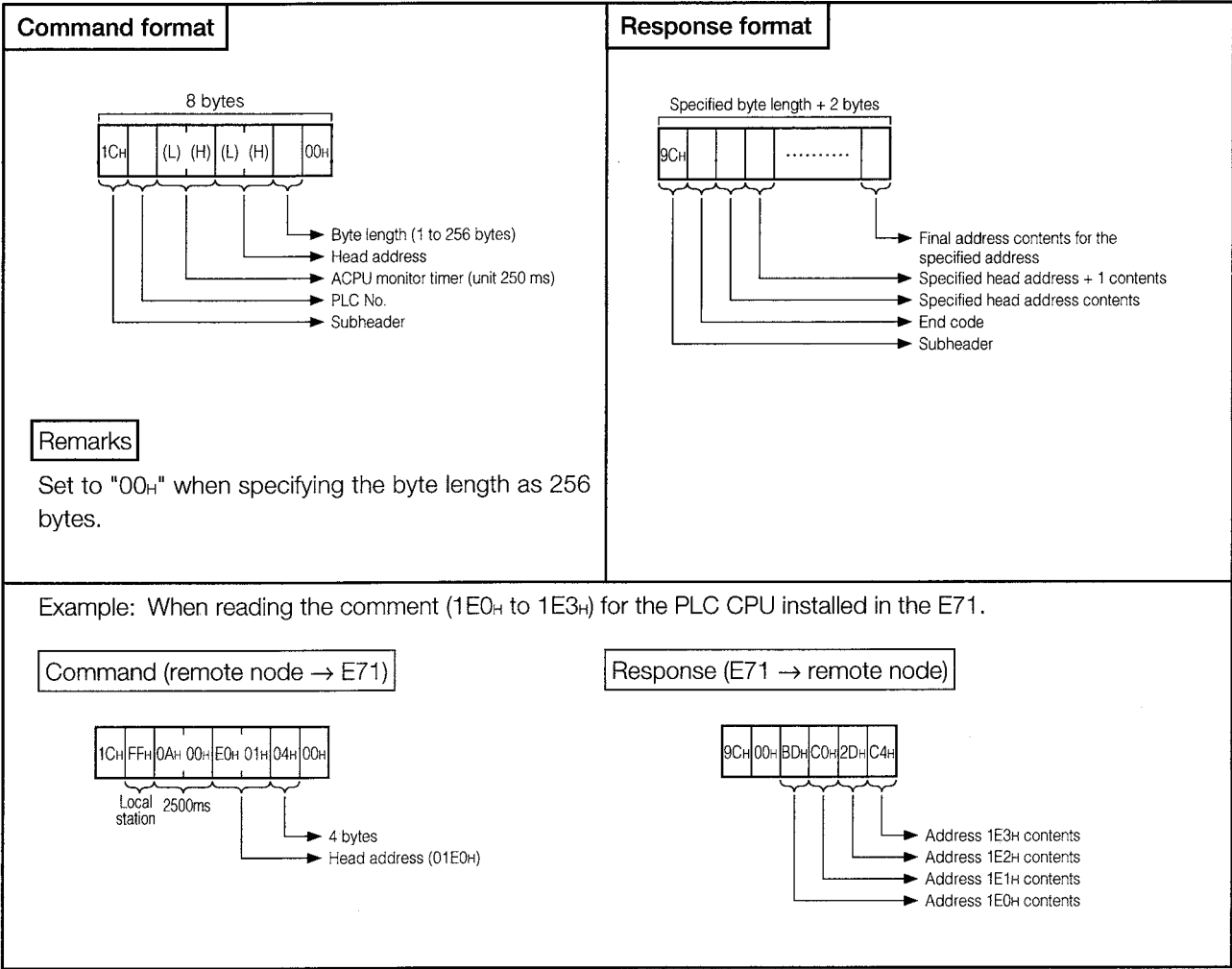
The comment data cannot be read or written by specifying the special device or device No. Be sure to read/write all the data from 0_H.

2

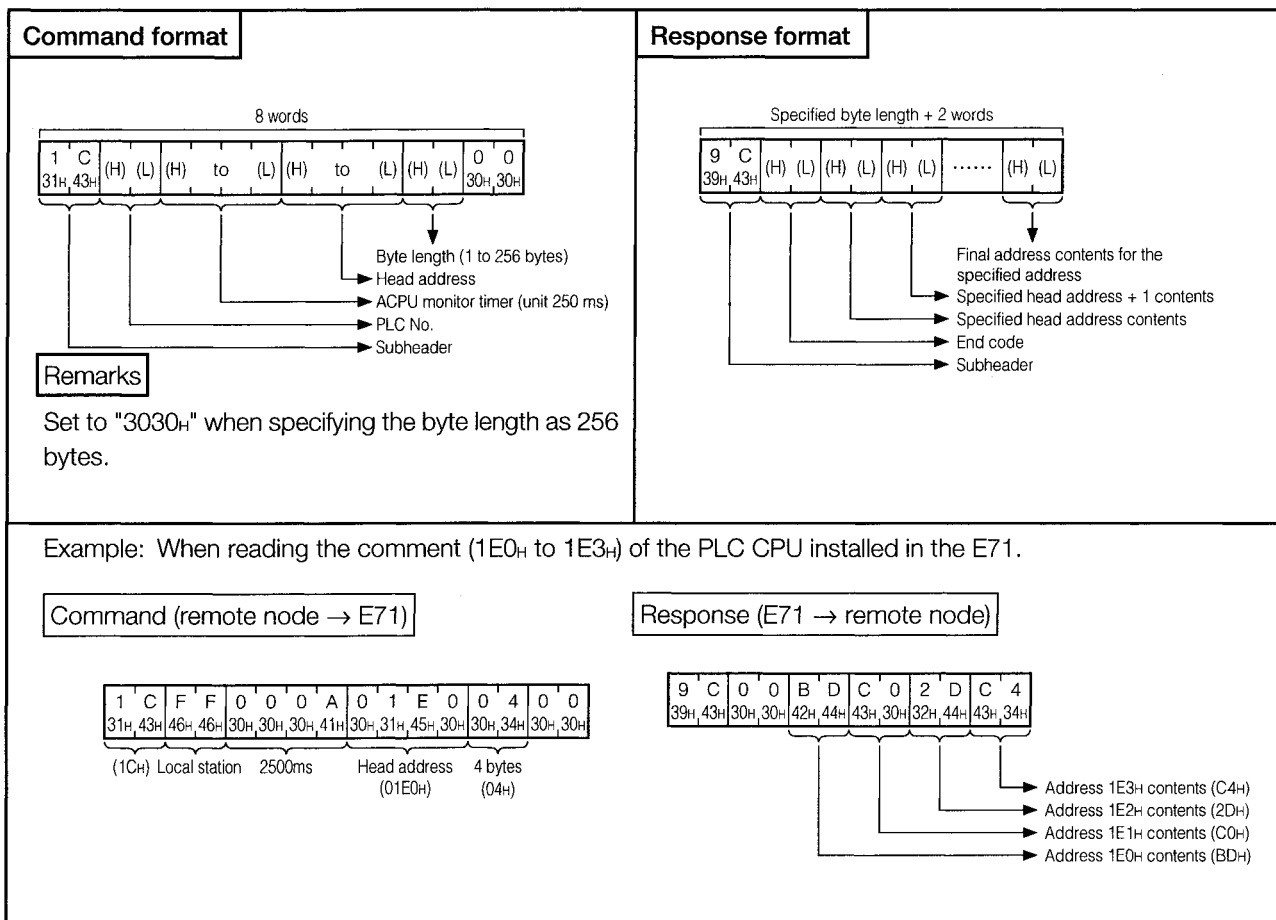
Batch read

This section explains the command/response format when conducting comment memory batch read.

(a) When exchanging with binary code



(b) When exchanging using ASCII code



3

Batch write

This section explains the command/response format when batch writing comment memory.

(a) When exchanging using binary code

Command format

Specified byte length + 8 bytes

1D _H	(L)	(H)	(L)	(H)	00 _H		
-----------------	-----	-----	-----	-----	-----------------	--	-------	--

Write data to the final address for the specified address

Write data to the specified head address + 1

Write data to the specified head address

Data length (1 to 256 bytes)

Head address

ACPU monitor timer (unit 250 ms)

PLC No.

Subheader

Remarks

Specify to "00_H" when specifying the byte length as 256 bytes.

Response format

2 bytes

9D _H	
-----------------	--

End code

Subheader

Example: When writing the comment area (78H to 7BH) data to the PLC CPU installed in the E71.

Command (remote node → E71)

Response (E71 → remote node)

1D _H	FF _H	0A _H	00 _H	78 _H	00 _H	04 _H	00 _H	BD _H	00 _H	2D _H	C4 _H
-----------------	-----------------	-----------------	-----------------	-----------------	-----------------	-----------------	-----------------	-----------------	-----------------	-----------------	-----------------

Write data to address 7BH

Write data to address 7AH

Write data to address 79H

Write data to address 78H

4 bytes

Head address (0078H)

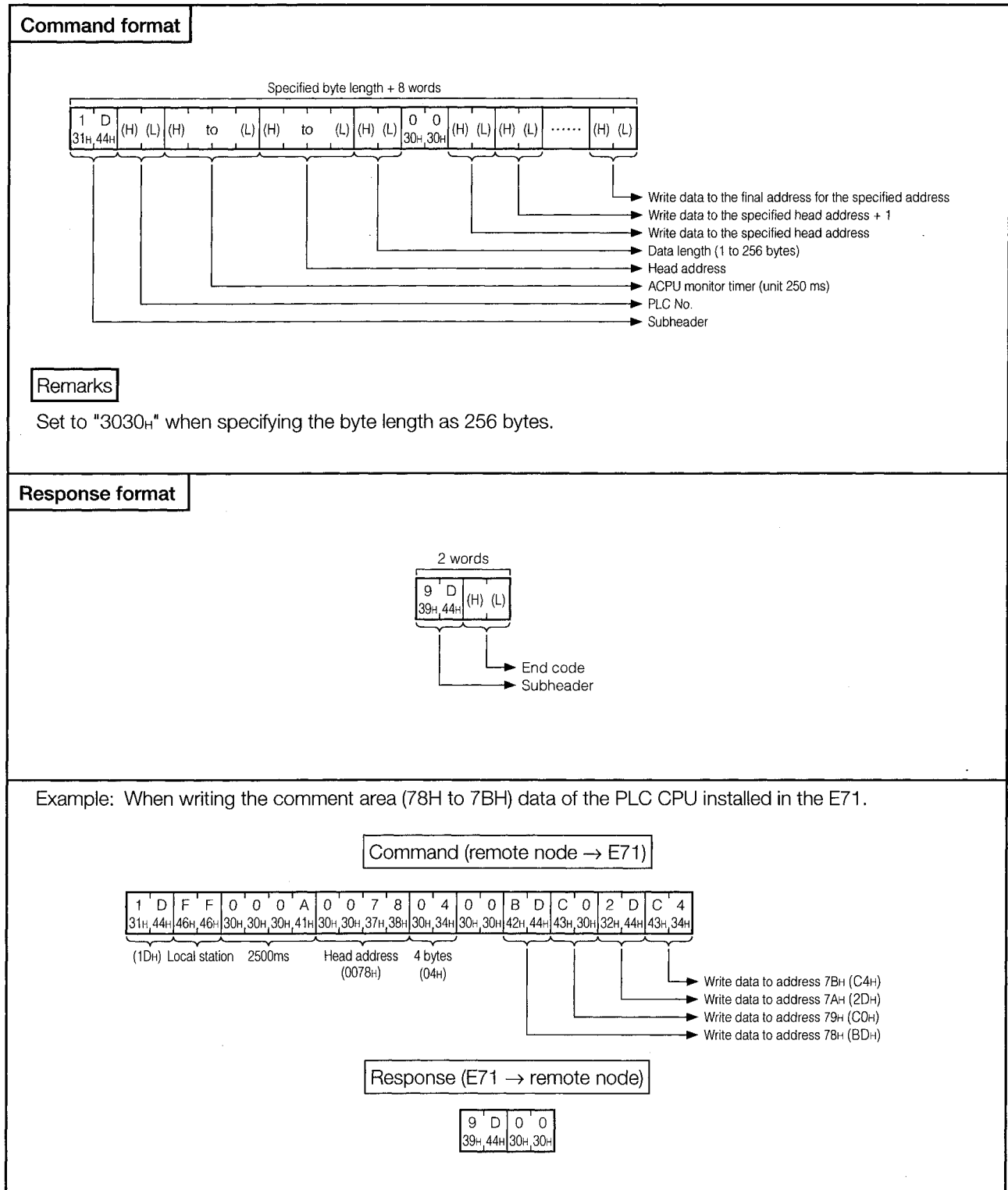
2500 ms

Local station

9D _H	00 _H
-----------------	-----------------

10 - 100

(b) When exchanging using ASCII code



10.6.7 Extension Comment Read/Write

This section explains the control procedure specification contents, method contents, and example specification when reading/writing PLC CPU extension comment data.

1

Command and addresses

This section explains the command/response classification and extension comment data addresses when reading/writing extension comment data.

(a) For functions used to read/write extension comment data are shown in Table 10.13.

Table 10.13 Functions List

Item	Command/ response classification	Processing description	Number of processing points con- ducted in one exchange	PLC CPU status		
				Stopped	Running Write possible setting	Write impossible setting
Batch read	39 _H	Reads the extension comment memory contents.	256 bytes	○	○	○
Batch write	3A _H	Writes the data to the extension comment memory.		○	○	×

In the PLC CPU status column in the above table the capital "○" represents execution possible and the capital "×" represents execution not possible.

(b) Extension comment memory address

Extension comment data storage area can be managed by using the corresponding address for the head address 00_H. For example, when the parameter extension comment capacity is 2k bytes, the range that can be sent by the head address is 00_H to 7FF_H.

- ① The maximum capacity of the extension comment memory is 63k bytes. The extension comment data address range is determined by the parameter setting capacity.
- ② Extension comment memory address specification is done using hexadecimal numbers.
- ③ If the head address + specified number of bytes – 1 ≤ is not the comment memory capacity, an error (end code 57_H) will occur.

Point

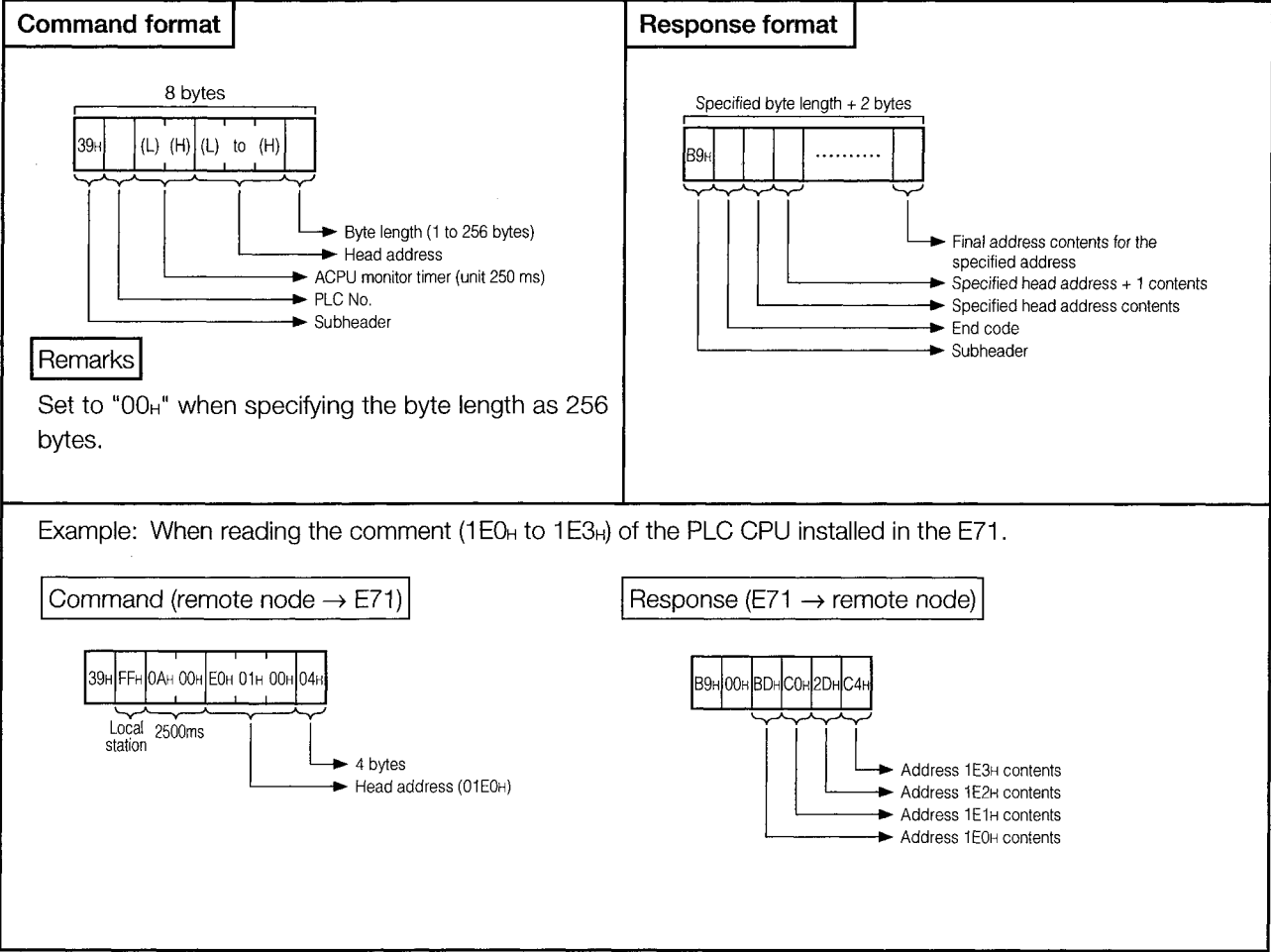
- (1) Read/write of the extension comment data cannot be done by specifying the special device or device No. Be sure to read all of the data from 0_H when reading/writing.
- (2) Reading/writing of extension comments can only be conducted for the AnACPU and the AnUCPU.

2

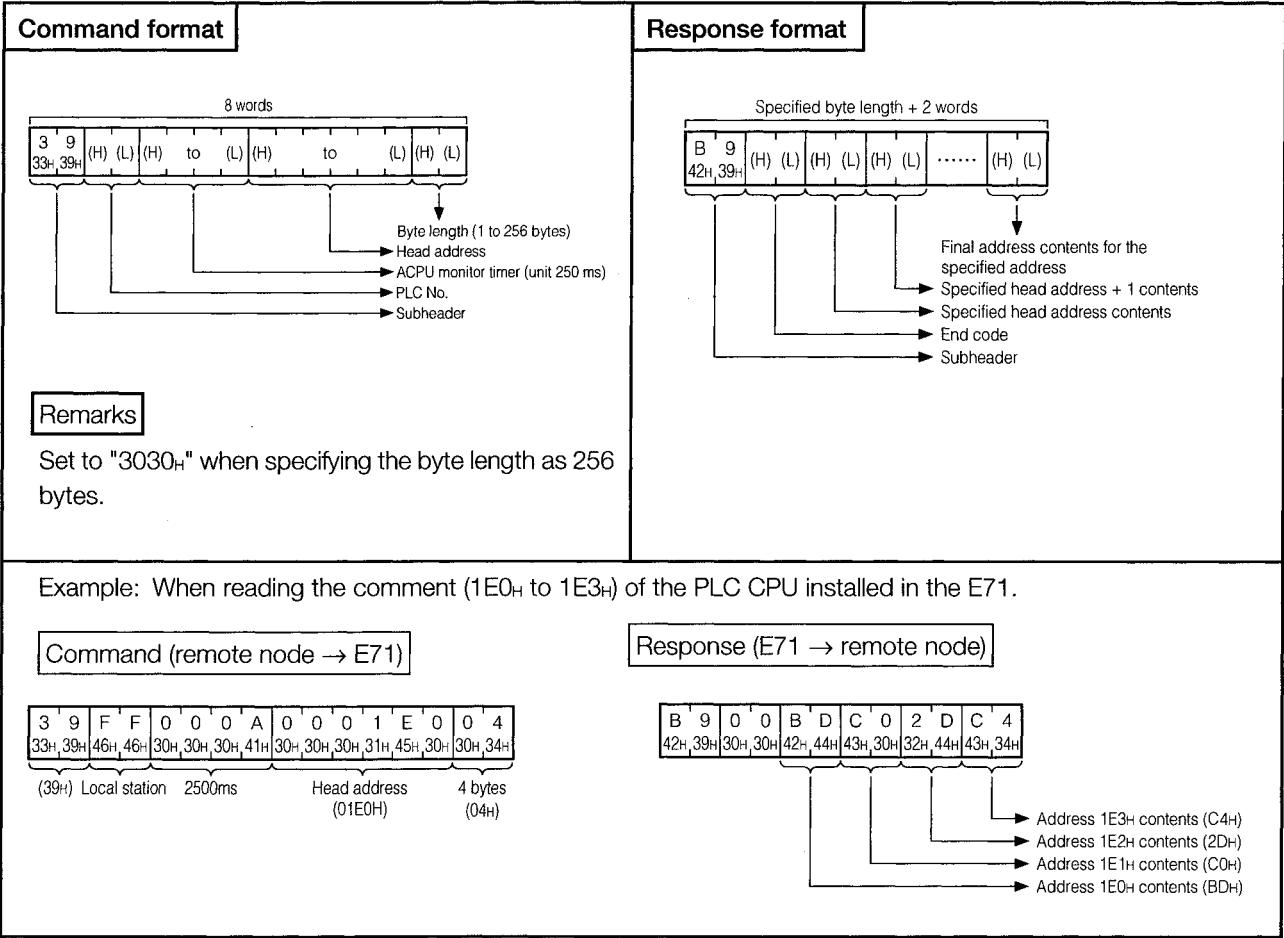
Batch read

This section explains the command/response format for batch reading form the extension comment memory.

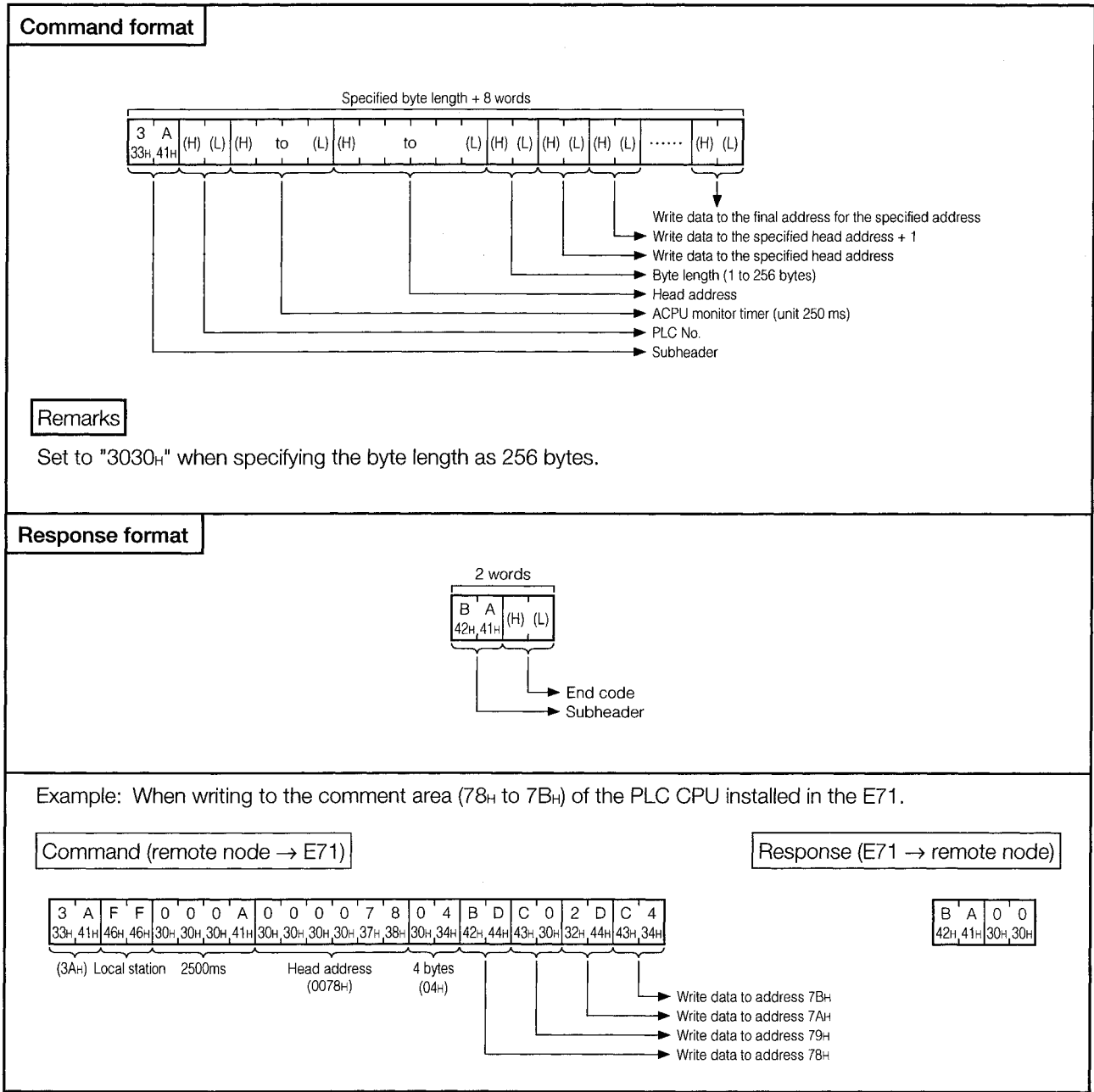
(a) When exchanging using binary code



(b) When exchanging using ASCII code



(b) When exchanging using ASCII code



10.7 Loopback Test

The loopback test is a function that tests whether or not the exchange between a remote node and the E71 is normal. The data transmitted from a remote node is returned as a response unchanged to the transmission origination station from the E71.

(1) The function used for the loopback test are shown in Table 10.14.

Table 10.14 Functions List

Item	Command/ response classification	Processing description	Number of processing points con- ducted in one exchange	PLC CPU status		
				Stopped	Running Write possible setting	Write impossible setting
Loopback test	16 _H	The characters received from the remote node are returned unchanged to the remote node.	256 bytes	○	○	○

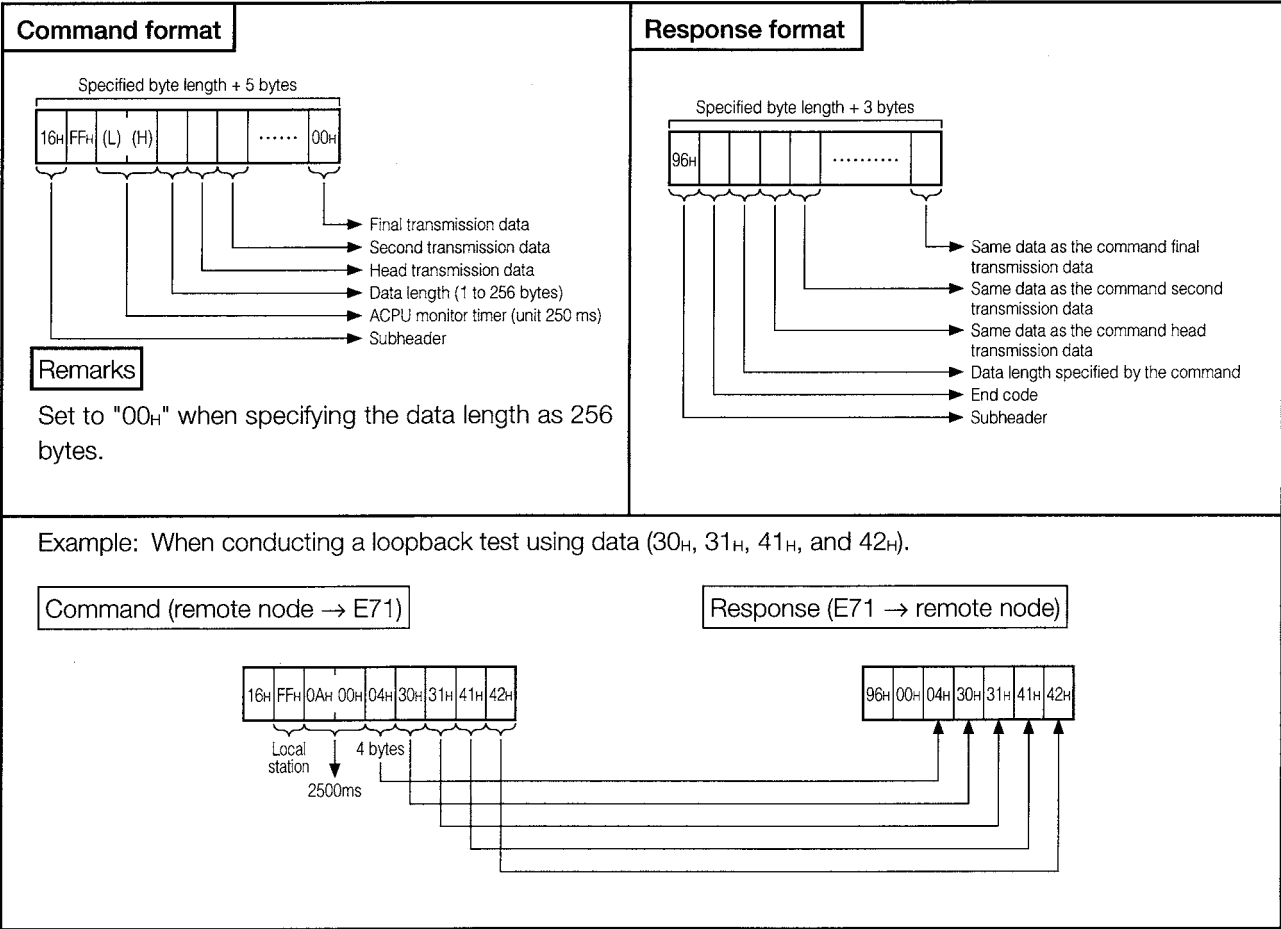
Point

For the transmission/reception text during the loopback test, for the transmission data portion transmit the following data as the header portion.

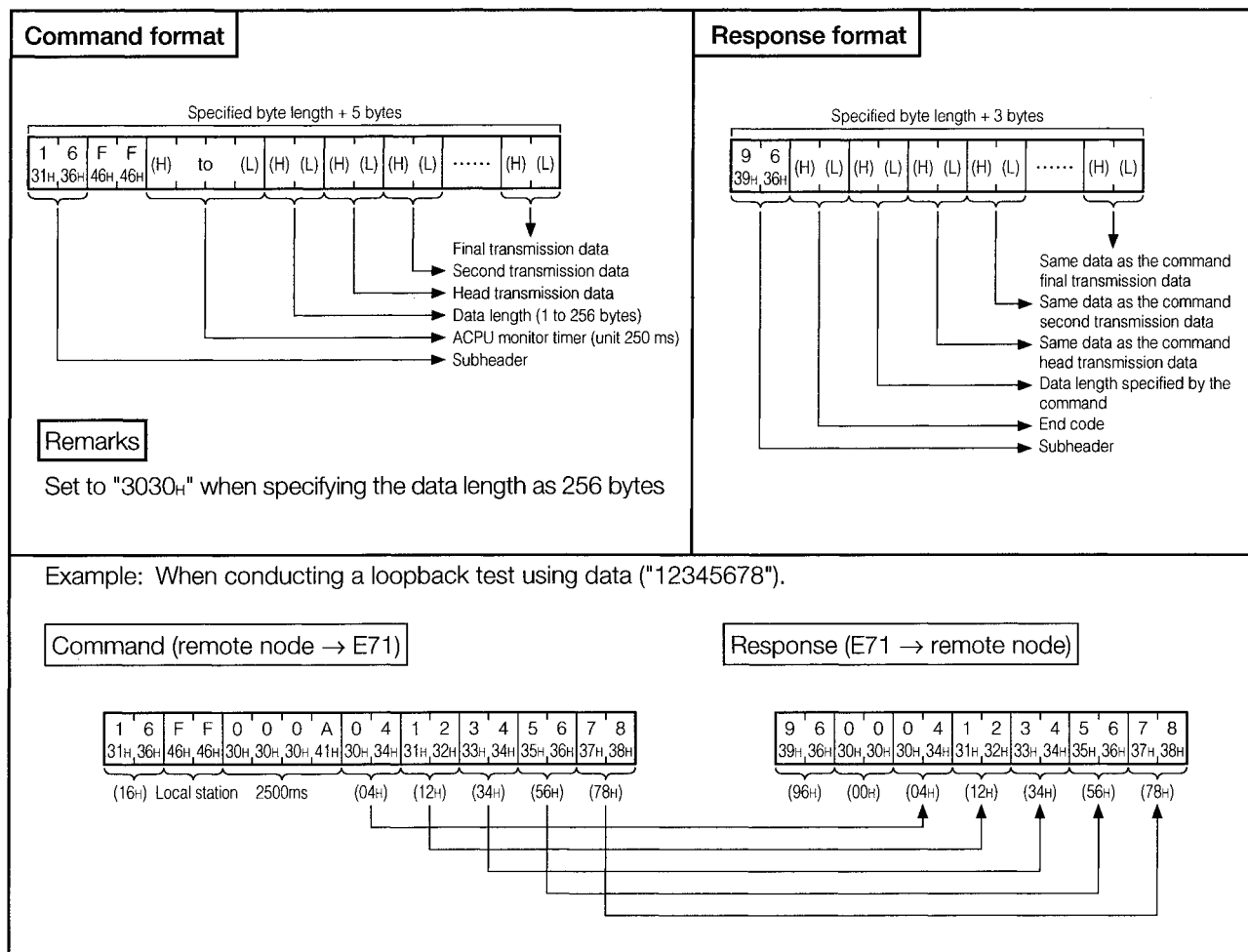
- ① When exchanging using binary code
The maximum 256 byte portion numerical (00H to FFH) data.
- ② When exchanging using ASCII code
The maximum 256 character portion half width characters ("0" to "9," "A" to "F") data.

This section explains the command/response format when conducting a loopback test.

(a) When exchanging using binary code



(b) When exchanging using ASCII code



SPECIAL FUNCTIONS SECTION

The special functions section gives a function summary and explains the usage method for the special functions used by the E71 by dividing the section into one chapter per function.

The user only needs to read the chapter that explains the function to be used.

11. WHEN SETTING A SUBNET MASK

When multiple nodes are connected to one network and that network is divided and managed as virtual multiple subnetworks, then subnet mask must be created for the affected nodes. This chapter explains how to set subnet masks.

11.1 Subnet Mask

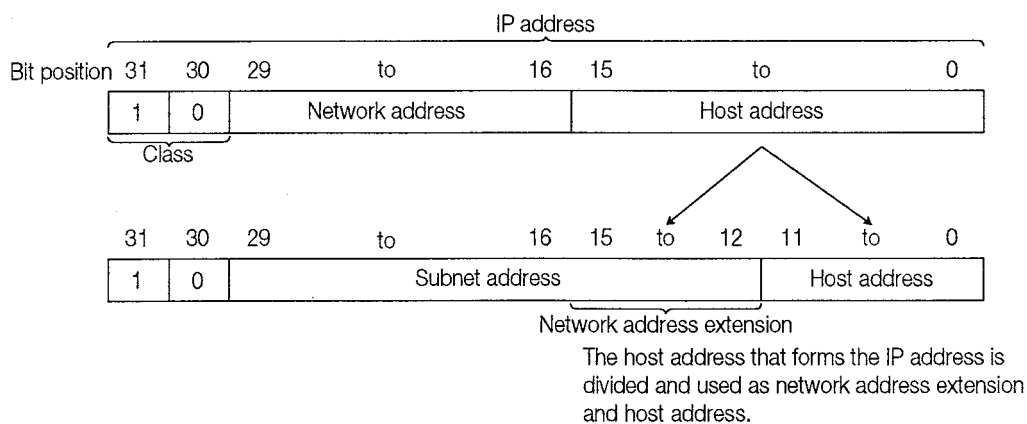
Networks build with Ethernet consist of small scale network systems where multiple nodes are connected to one Ethernet and medium and large size network systems where these smaller networks are connected using multiple routers.

The IP address of nodes connected to the Ethernet show the nodes address on that network, so three classes from class A through class C are provided to make it possible to select the address system that meets the needs of a particular network size, and the corresponding IP address is expressed using 32-bit numerals. (Refer to Item 11.3)

Subnet masks make it easy to theoretically divide one network that has many nodes connected to it into multiple subnetwork units that are easy to manage.

In particular this is information that uses a part of the host address as a network address extension as recognized it as the subnet address shown below.

(Example using class B)



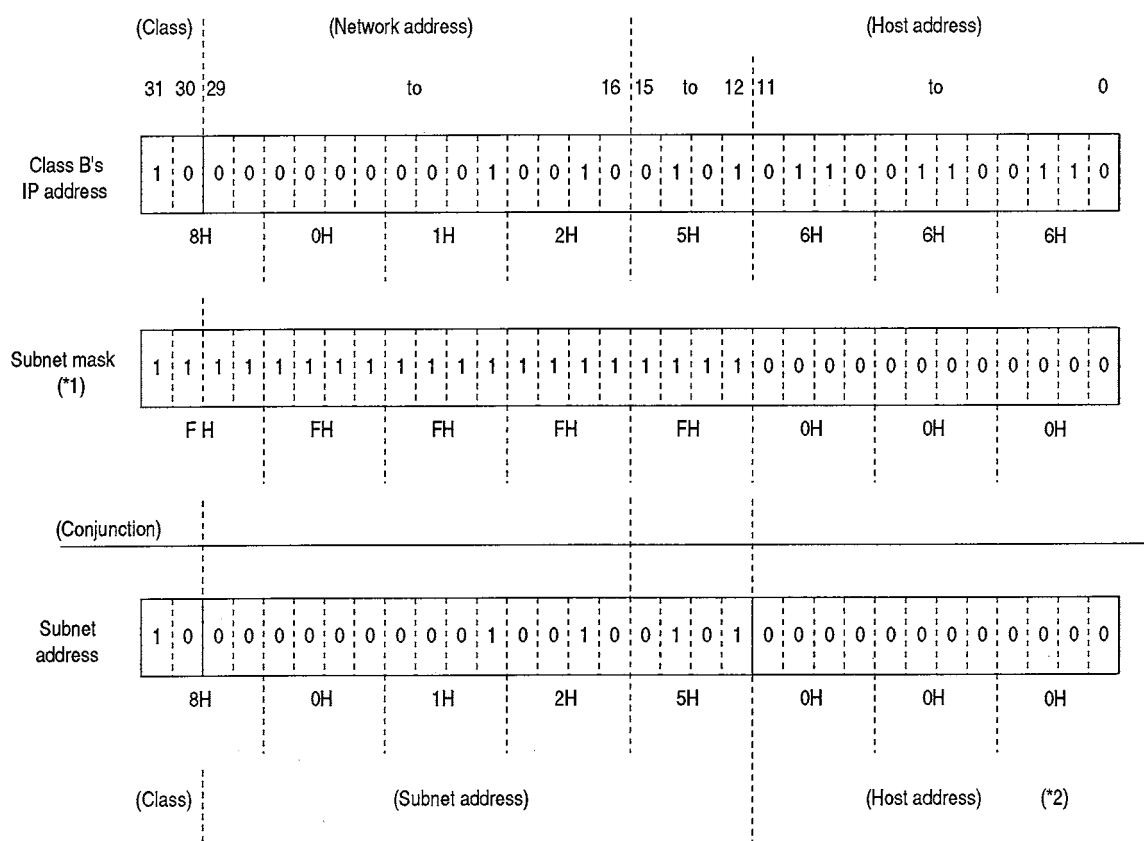
Point

- (1) All nodes on the same subnetwork must have a common subnet mask.
Refer to Item 12.3 "Summary of Router Relay Processing" for details on the transmission from E71 when the subnet address of the data transmission destination node differs from the local station.
- (2) If not managed as a subnetwork then each node does not have to have a subnet mask.

For the E71 the subnet mask is set using the buffer memory subnet mask setting area (Subnet mask field) and is specified how far a host address like that below is extended.

- ① The location up to where one subnet mask field is created becomes the network address + network address extension and the E71 handles this portion as a network address.
 - ② If the subnet address differ, they will be viewed as separate networks.
- * When Ethernets are connected using routers, specifying a subnet address makes it possible to see which router is managing which network.

(Example) When FFFF000H is used as the class B subnet mask



*1 The subnet mask is specified as a bit masked value where "1"s signify the network address and its extended portion and "0"s signify the host address.

*2 001H to FFEH can be used as the host address.

11.2 Data for Setting the Subnet Mask

This section explains the subnet mask setting area which is used to divide the network to multiple virtual subnetworks to make management of one network easier.

The network manager (the person who plans the network or manages the IP addresses, etc.) needs to set the values for creating this area before initial processing.

Buffer Memory		
(Address)	Subnet Mask Setting Area	Default Value
1C0 to 1C1H (448 to 449)	Subnet mask field (2 Words)	0H (0)

1 Subnet mask field (default value = 0H) address 1C0H to 1C1H (448 to 449)

(a) Set the subnet mask. (Setting range: C0000000H to FFFFFFFFH)

Consult the network administrator for the setting.

(b) When not using the subnet mask, set any of the following table values according to the class.

Class	Mask Value
Class A	FF000000H
Class B	FFFF0000H
Class C	FFFFFF00H

(c) If a subnet mask field value where all of the local station network address cannot be masked is specified, all of the network address will automatically be given masked values.

(Example) When the Local Station is Class B

Following shows an example specification to the subnet mask field and the corresponding actual subnet mask's value. (Refer to Item 11.3 for the allocation of class B IP address)

Specified value to the subnet mask field	Actual subnet mask value
FF000000H	FFFF0000H
FF008000H	FFFF8000H
FFFF8000H	FFFF8000H

(To mask all network address subnet mask field of FFFF0000H or higher is required.)

(d) When setting subnet masks, perform the following settings besides the setting for the subnet mask setting area:

- "Use router relay function" setting in the special function settings (address 2)
- Settings for the router relay function (address 450 to 472)

11.3 Ethernet IP Address

This section explains in summary the Ethernet IP address and the classes, network address, and host address it contains.

1 Nodes connected to the Ethernet have both a fixed Ethernet address and a voluntary IP address for just that node.

Users do not need to be aware of the Ethernet address because it is handled by the ARP (Address Resolution Protocol).

The IP address shows the address on the network for each node connected to the Ethernet, so the user must remember this ID.

The IP address is divided into three classes of class A through class C to allow selection of the address system that is most suitable for the size of the network system.

A network manager (the person who plans the network, manages IP addresses, etc.) must set a 32-bit numerical value for each node following the standard IP address method used on a world wide scale.

2 The contents and role of each class, network address, and host address are as follows.

① Class A to Class C

Class A is for networks with many hosts, class C is for networks with few hosts, and class B is for networks of intermediate size. (Up to 254 hosts can be connected with class C.)

② Network address

This is used to identify the network to make it possible to handle multiple networks. Networks with different network address will be identified as separate networks.

③ Host address

This ID is used to identify hosts on a network.

When using the subnet mask described in this chapter, the host address portion can be eliminated and an extension added to the network address.

* If the IP address is compared to a telephone number, the network address, host address, and port No. specified during data exchange would play the following roles.

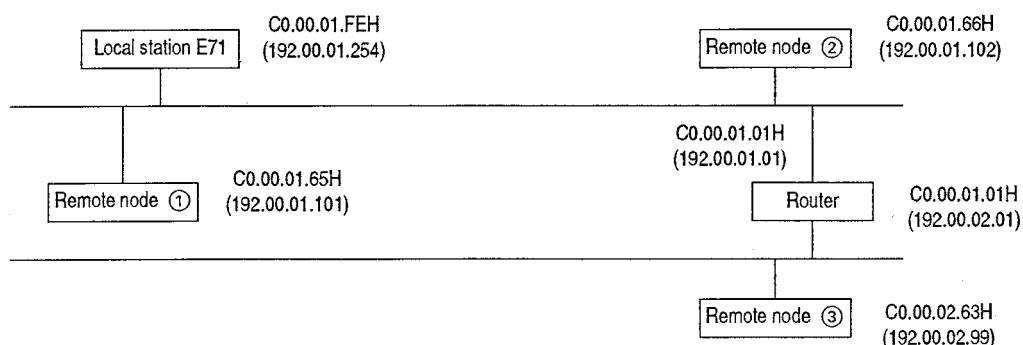
Network address : long distance number, Host address : telephone number,
Port No. : extension number

If the network address (long distance numbers) are the same then direct exchange is possible, but if they are different then exchange must be conducted by going through routers (telephone exchanges).

The function used for exchanging via routers (telephone exchanges) is the router relay function described in Chapter 12.

(Example) When the local station E71's IP address is class C (Refer to 3)

(IP address top stage: hexadecimal expression, bottom stage: decimal expression)



3 Shows the IP address allocation for each class.

When there are many nodes in the same network, then there must be many host address.

In addition, when there are few nodes in one network, but there are many networks, then there must be many network address.

(a) Class A, class B, and class C are identified by the first 2 bits of the IP address.

(b) In the class A IP address, the network address is allocated to the 7th bit and the host address is allocated to the 24th bit.

	31	30		to	24	23		to		0
Class A	0				Network address					Host address

(c) In the class B IP address, the network address is allocated to the 14th bit and the host address is allocated to the 16th bit.

	31	30	29			16	15		to		0
Class B	1	0				Network address				Host address	

(d) In the class C IP address, the network address is allocated to the 21st bit and the host address is allocated to the 8th bit.

	31	30	29	28			to		8	7	to	0
Class C	1	1	0					Network address				Host address

Point

Limitations when setting E71's IP address are shown below. There are no network address limitations.

(1) Set the class to class A to C.

(2) For the host address (*1), make it so that all of the host address range bits are not 0/1.

*1 When using the router relay function and the subnet mask, the host address masked the net mask will become the subject host address.

(3) Use IP addresses that do not overlap with remote node including those on other networks.

Remarks

Network address

A network address is an IP address for which a host address is 0.

	31	30	29	28		to	24	23		to	16	15		to	8	7		to		0
Class A	0							Network address												0
Class B	1	0																		0
Class C	1	1	0																	0

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12. WHEN USING ROUTER RELAY FUNCTIONS

This section explains when a PLC CPU and a remote node are exchanging via a specified router relay.

12.1 Router Relay Functions

Normally, in an Ethernet that uses IP, exchange can only be conducted between the local station and remote nodes connected to the same Ethernet (have the same subnet address).

To communicate with a remote node on a different Ethernet (different subnet address), a router relay must be used.

This router relay function is a static router relay function used for exchange with the remote node of a different Ethernet (Different subnet address).

Using this function makes it possible to exchange through routers and gateways using PLC CPU side TCP/IP active open and UDP/IP transmission.

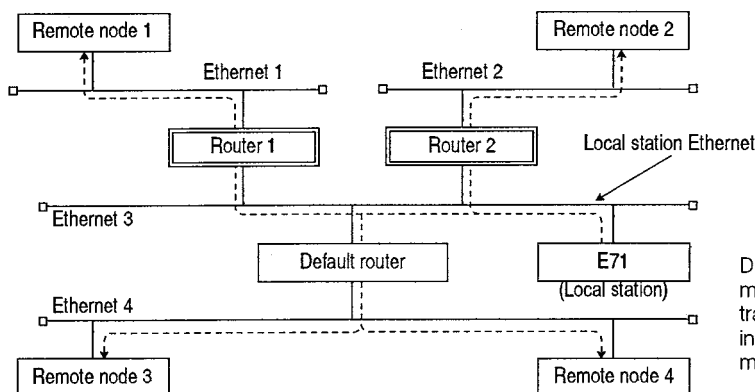
(The router relay function does not operate as a router.)

Exchange using the E71's router relay function is conducted when the buffer memory is given the following settings by the user initial processing.

- Special function setting: sets "using router relay function"
- Router relay parameter settings: set a default router and a maximum of 5 voluntary routers

When exchanging data, the E71 conducts router relay exchange with another Ethernet using the following method when the partner station subnet address in the message differs from the local station subnet address.

- ① The subject router relays the exchange when there is a partner station subnet address in the user set router relay parameter.
- ② Relay exchange is conducted using the default router when there is no partner station subnet address in the user set router relay parameter.



During transmission, the E71 does not transmit directly to the partner station, but instead transmits the data once to the router specified in the router relay parameter. The router transmits the received data to the partner station.

Point

- (1) It is not necessary to use the router relay function when the E71 is exchanging with a partner remote node using a router relay in passive open (TCP/IP).
In this case, exchange can be done even if the router relay function is not used.
- (2) The router relay function is not required in systems that use proxy routers.

12.2 Exchangeable Functions and Settable Range Using Router Relay Functions

This section explains the data exchange functions and settable range used to exchange data with remote Ethernets using router relay functions.

1

Exchangeable functions

When conducting the following data exchange, exchange with remote Ethernets can be done using the router relay function.

- Fixed buffer exchange (possible for either with procedure and without procedure)
- Random access buffer exchange
- Reading/writing data to the PLC CPU

2

Settable range

This makes it possible to exchange with remote nodes connected to Ethernet within the range accessible via the router. There is one default router and 5 voluntary routers set in the E71 through which exchange can be conducted.

12.3 Summary of Router Relay Processing

This section gives an overview of the router relay processing conducted by the E71 when data is transmitted to remote nodes on remote Ethernets via routers.

The E71 determines the transmission destination of the data at the time data is transmitted using the following procedure.

1

Checks whether there is a setting that uses the router relay function.

If the setting does not use the router relay function, exchange is done directly with the partner remote node.

2

Checks whether the partner remote node is connected to the same Ethernet as the local station by using the following two formulas. (Subnet address check)

If they are the same, then it is determined that it is the same Ethernet and exchange is done directly with the partner remote node. If they are different, it is judged that it is a remote Ethernet, and exchange to the partner node is conducted via a router.

(Formula-1) Partner station IP address and subnet mask file conjunction

(Formula-2) Local station ID address and subnet mask field conjunction

3

When it is recognized that the partner remote node is connected to a remote Ethernet, exchange with the partner remote node is conducted via a router.

(a) When the partner remote node is the same class as the local station

Compares the above (formula-1) with the valid subnet addresses 1 to 5 in the router relay parameter.

If the settings are the same, exchange is conducted to the router supporting the router IP address.

If the settings are different, exchange is conducted to the default router.

(b) When the partner remote node and the local station are different classes

The partner remote node's network address and the valid subnet addresses 1 to 5 in the router relay parameter are compared. If they are the same, then exchange is conducted to the router that supports the router IP address. If they are different, then exchange is conducted to the default router.

12.4 Data for Using Router Relay Functions

This section explains about the parameter setting area used to conduct exchange with remote nodes using the router relay function.

The network manager (the person who plans a network, manages IP addresses, etc.) sets the values for the area before initial processing.

Buffer Memory		Default Value
(Address)	Router Relay Parameter (36 Words)	
1C2 to 1C3H (450 to 451)	Default router IP address (2 Words)	0H (0)
1C4H (452)	Registered number of routers (1 Word)	0H (0)
1C5 to 1C6H (453 to 454)	Router 1 setting (2 Words)	0H (0)
1C7 to 1C8H (455 to 456)		
1C9 to 1CAH (457 to 458)	Router 2 setting (2 Words)	0H (0)
1CB to 1CCH (459 to 460)		
1CD to 1CEH (461 to 462)	Router 3 setting (2 Words)	0H (0)
1CF to 1D0H (463 to 464)		
1D1 to 1D2H (465 to 466)	Router 4 setting (2 Words)	0H (0)
1D3 to 1D4H (467 to 468)		
1D5 to 1D6H (469 to 470)	Router 5 setting (2 Words)	0H (0)
1D7 to 1D8H (471 to 472)		

1 Default router IP address (Default value = 0H) Address 1C2H to 1C3H (450 to 451)

Set the IP address of the router (default router) via which communication with a remote node on a remote Ethernet is made when the router is not the one specified with the subnet address (refer to section [3](#)).

Set the value that will satisfy the following conditions.

- Condition 1: The IP address class is any of A, B and C.
- Condition 2: The subnet address of the default router is the same as that of the local station E71.
- Condition 3: Bits of the host address should not be all "0" or all "1".

POINT

If the corresponding subnet address (refer to [3](#) in this section) does not exist when the connection is opened or data communication is made, communication is made via the default router.

2 Number of registered routers (Default value = 0H) Address 1C4H (452)

- Sets the number of subject routers (number of valid settings) using the following subnet address n and router IP address n when conducting exchange with a remote node on a remote Ethernet via anything other than the default router.
- Sets the setting value to 0 to 5. (If a value higher than 6 is set, it will be seemed as 5.)
- Sets the specified numbers portion of the subnet address n and router IP address n in the following [3](#) and [4](#) area.

3 Subnet address n (Default value = 0H) Address 1C5H....(453...)

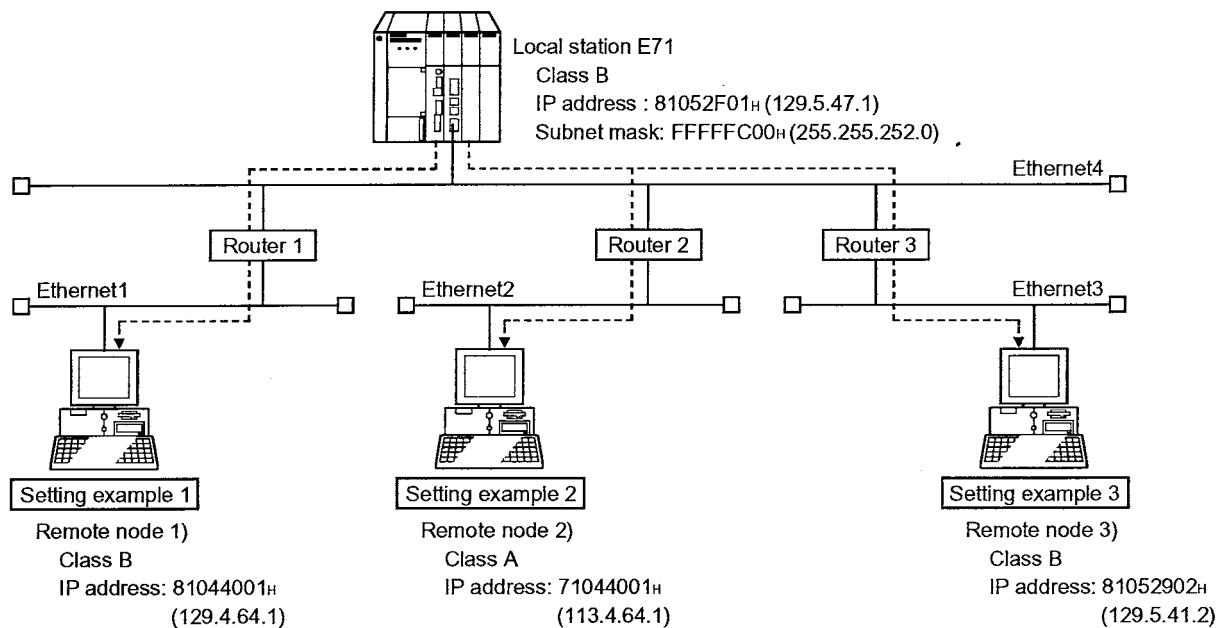
(a) Set the network address (*1) or subnet address (*2) of the remote node when the E71 communicates with the target device on another Ethernet via other than the default router. Set the value that satisfies the following conditions.

- Condition 1: The IP address class is any of A, B and C.
- Condition 2: The host address bits are all "0".

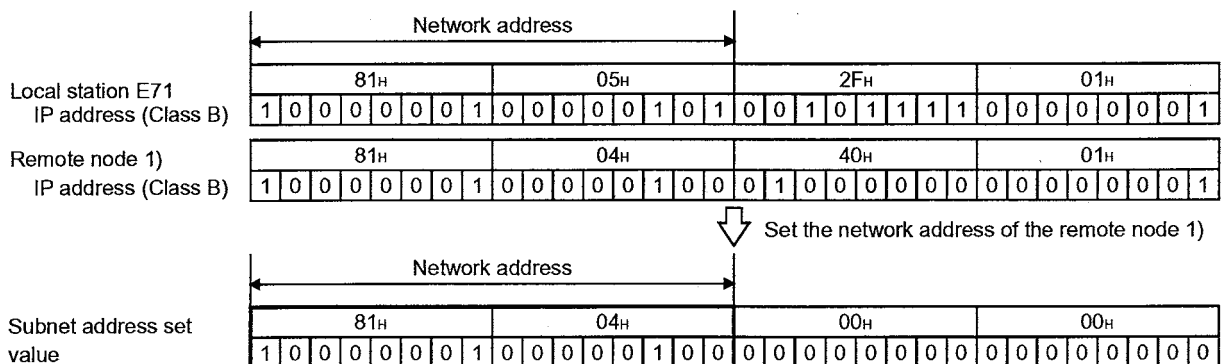
*1 If the class (network address) of the local station differs from that of the remote node, set the network address of the remote node.

*2 If the class (network address) of the local station is the same as that of the remote node, set the sub-net address of the remote node.

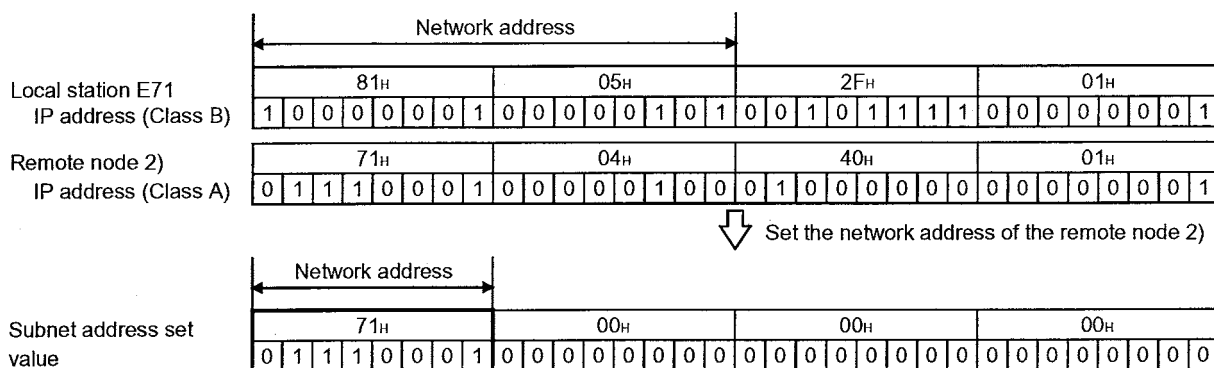
(b) Subnet address setting examples



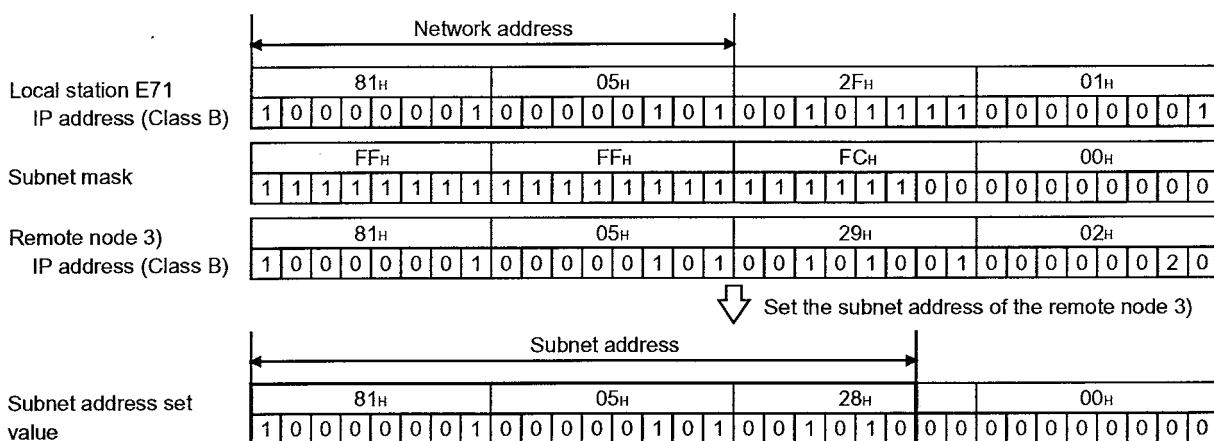
(Setting example 1) When the network addresses of the local station E71 and remote node differ



(Setting example 2) When the classes of the local station E71 and remote node differ



(Setting example 3) When the network addresses of the local station E71 and remote node are the same



4

Router IP address n (Default value = 0H) Address 1C7H....(455...)

Set the IP address of the router (default router) to be used when the E71 communicates with the remote node on another Ethernet via other than the router specified in the router information (refer to (4) below).

Set the value that satisfies the following conditions.

- Condition 1: The IP address class is any of A, B and C.
- Condition 2: The sub-net address of the default router is the same as that of the local station E71.
- Condition 3: The host address bits are not all "0" or all "1".

Point

When using the router relay function set the following settings in addition to settings in the router relay parameter setting area.

- * Set "using router relay function" (address 2H(2)) in the initial processing parameter setting area's special function setting area.

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TROUBLESHOOTING SECTION

The troubleshooting edition explains about the error codes corresponding to errors, error contents, error processing, and troubleshooting flow when trouble occurs during Ethernet interface module use.

13. TROUBLESHOOTING

This section explains about trouble that occurs when using the Ethernet interface module and covers error codes, error description, error processing, and trouble shooting flow for errors detected by the E71.

When trouble occurs that prevents normal exchange between the E71 and a remote node then the problem must be limited to whether the cause occurred on the E71 end, in the line, or at the remote node end.

Whether or not error occurs on the E71 side or the checking method for the error contents are described in Item 13.3. Refer to them as required.

Remarks

When a line error, etc., occurs when equipment from different manufacturers is connected, we ask that the user use a line analyzer, etc., to determine the location of the problem.

13.1 List of Error Codes

This section explains about the error codes (End code, Error code), error description, and error processing that are generated for each process when data is exchanged between the E71 and a remote node.

The types of errors that occur are shown below.

	Error Type	Description	Error Code Storage Buffer Memory Address	Reference Item
1	Errors that occur during Initial processing	<ul style="list-style-type: none"> Setting value error Initial processing error 	50H (80)	Item 13.1.1
2	Errors that occur during open processing	<ul style="list-style-type: none"> Setting value error Open processing error 	5DH (93) ... A9H (169) ...	
3	Errors that occur during fixed buffer transmission	<ul style="list-style-type: none"> Specified data error Transmission error 	5EH (94) ... 5FH (95) ...	
4	Errors that occur during field buffer exchange	<ul style="list-style-type: none"> Specified data error Exchange error (excluding 3 above) 	5FH (95) ...	
5	Errors that occur during exchange other than the above * Errors for which error codes are stored in the error log area	<ul style="list-style-type: none"> Specified data error Errors where the place of origin of the error cannot be determined Errors occurring during random access buffer exchange, reading/writing data to the PLC CPU 	A9H (169) ...	
6	Data exchange errors * Errors return to the request originating remote node	<ul style="list-style-type: none"> Errors returned by fixed buffer exchange (End code) 	—	Item 13.1.1
		<ul style="list-style-type: none"> Errors returned by random access buffer exchange (End code) 		—
		<ul style="list-style-type: none"> Errors returned by reading/writing data to the PLC CPU 		Item 13.1.1
		<ul style="list-style-type: none"> End code during E71 command use Error code during E71 command use 		Item 13.1.2

13.1.1 End Codes Returned to the Remote Node during Data Exchange

Error Codes Stored in the Buffer Memory

This section explains the error contents and error processing for end codes return to remote node and error codes stored in the E71's buffer memory when an error occurs during processing for data exchange between the E71 and a remote node.

Note that the buffer memory may store error codes of the messages returned from the external device. For error codes not shown in this manual, refer to the manual of the external device and check the returned messages.

End Code Error Code	Description	Processing						
00H	Normal end	—						
01H	<ul style="list-style-type: none">The exchange data length is below 0 or exceeds the set range.	Correct the transmission data data length. (Refer to Item 3.3.)						
50H	<div><div><div>Exchange Processing</div><div>Commands/ Responses</div></div><table><tr><td>Fixed buffer exchange</td><td>60H</td></tr><tr><td>Random access buffer exchange</td><td>61H, 62H</td></tr><tr><td>Reading and writing data in the PLC CPU</td><td>00H, 3CH</td></tr></table></div> <ul style="list-style-type: none">When the codes are other than those prescribed by the subheader commands and responses.During fixed buffer exchange, when the actual data quantity is less than the data length setting, the remaining data is determined to be second data and processed. In this case, a subheader command undefined error will occur.	Fixed buffer exchange	60H	Random access buffer exchange	61H, 62H	Reading and writing data in the PLC CPU	00H, 3CH	<ul style="list-style-type: none">Check in correction of the set commands and responses at the remote node.<div><div>The E71 automatically adds the commands and responses, so the user does not need to set these.</div></div>Refer to the remarks at the end of this page.Check and correct the data length.
Fixed buffer exchange	60H							
Random access buffer exchange	61H, 62H							
Reading and writing data in the PLC CPU	00H, 3CH							
51H	<ul style="list-style-type: none">For random access buffer exchange, the specified header address from the remote node is set outside the range of 0 to 6143.	<ul style="list-style-type: none">Check and correct the specified header address.						
52H	<ul style="list-style-type: none">For random access buffer exchange, <u>the specified header address from the remote node + number of data words (set during read) exceeds the range of 0 to 6143.</u>The data (text) for the specified number of words cannot be transmitted in one frame. (The transmission/reception data length value or text amount is not within the acceptable range.)	<ul style="list-style-type: none">Check and correct the header address and number of data words.(Refer to Item 3.3)Correct the number of read/write points.						
54H	<ul style="list-style-type: none">When the data code setting using the exchange condition setting switch (SW2) of the E71 is set to ASCII code, ASCII code data that cannot be converted to parity code by the remote node was transmitted.	<ul style="list-style-type: none">Check and correct the remote node transmission data.						
55H	<ul style="list-style-type: none">When the CPU exchange timing setting is set to write not possible during RUN using the exchange condition setting switch (SW7) of the E71, and the data write request from the remote node was made while the PLC CPU was running.A request from a remote node to write a parameter, sequence program, or microcomputer program was received while the PLC CPU was running. (Not related to the on/off state of the exchange condition setting switch (SW7) of the E71.)	<ul style="list-style-type: none">Conduct data write while the SW7 is on (write allowed during RUN). However, parameters, sequence programs, and microcomputer programs cannot be written while the CPU is running.Write the data after stopping the PLC CPU.						
56H	<ul style="list-style-type: none">There is a device setting error from a remote node.	<ul style="list-style-type: none">Correct the device setting.						

End Code Error Code	Description	Processing
57H	<ul style="list-style-type: none"> The number of command points specified by the remote node exceed that of the maximum processing number of points for each process (number of points processed during one exchange). The header address (header address No., header step No.) to the specified number of points, exceeds the maximum address for each process (device No., step No.). 	<ul style="list-style-type: none"> Correct the specified number of points and the header address (device No., step No.).
	<ul style="list-style-type: none"> The command byte length is longer than that prescribed. During data write, the specified write data number of points is different from the number of points specified value. 	<ul style="list-style-type: none"> Check and reset the command data.
	<ul style="list-style-type: none"> There was a monitor request even though monitor data is not registered. 	<ul style="list-style-type: none"> Conduct monitor data registration.
	<ul style="list-style-type: none"> When a microcomputer program was read/written it was set beyond the parameter setting range's final address. 	<ul style="list-style-type: none"> Reading and writing cannot be performed after the final address. Correct the specified address.
	<ul style="list-style-type: none"> During the extension file register block No. specification, a range of block No. were specified that exceed the subject memory cassette's capacity. 	<ul style="list-style-type: none"> Correct the block No.
58H	<ul style="list-style-type: none"> The command head address from a remote node (head device No., address step No.) from a remote node that exceeds the specifiable range was set. A microcomputer program for file register (R) read/write that is outside the PLC CPU's parameter settings was specified. 	<ul style="list-style-type: none"> Correct all processing to values in the specifiable range.
	<ul style="list-style-type: none"> The extension file register block No. specifications are for blocks that do not exist. 	<ul style="list-style-type: none"> Correct the block No.
	<ul style="list-style-type: none"> A file register (R) is set for the A1(N) CPU. 	<ul style="list-style-type: none"> The A1(N) CPU cannot use file registers.
	<ul style="list-style-type: none"> A word device is specified using bit device commands. The bit device's header No. is specified at values other than multiples of 16 using word device commands. 	<ul style="list-style-type: none"> Correct the commands or the specified device.
59H	<ul style="list-style-type: none"> An extension file register read/write request was made to the A1(N) CPU. 	<ul style="list-style-type: none"> The A1(N) CPU cannot use extension file registers.
5BH	<ul style="list-style-type: none"> Exchange cannot be done between the PLC CPU and the E71. The PLC CPU cannot process requests from remote nodes. 	<ul style="list-style-type: none"> Prepare the error location by adding an error code (Refer to Item 13.1.2) after the end code.
60H	<ul style="list-style-type: none"> The exchange time between the E71 and the PLC CPU exceeds the ACPU monitoring timer value. 	<ul style="list-style-type: none"> Lengthen the ACPU monitoring timer value.
62H	<ul style="list-style-type: none"> Returns other than "00H" (normal end) to the response end code from a remote node for fixed buffer transmission. 	<ul style="list-style-type: none"> Read and process the response end code (Buffer memory address 95, 105...) from the remote node for the fixed buffer transmission.

End Code Error Code	Description	Processing
70H	A recurring signal for the response does not arrive within the response monitor timer value. • All the data cannot be received by the allocation reception.	<ul style="list-style-type: none"> • Check the partner node operation. • Check if the connection cable is disconnected. • Check if there's a problem with the connection to the transceiver or with the terminator connection. • If the response monitor timer value is small then change it to a large value. • Check the transmission data on the partner node side.
71H	<ul style="list-style-type: none"> • The amount of data set in the data length cannot be received within the response monitor timer value. • The actual amount of data is smaller than the value set in the data length. • The remainder of the text allocated by the TCP/UDP level was not received within the response monitor timer value. 	<ul style="list-style-type: none"> • Correct the data length of the exchange data. • When connecting TCP exchange the interference of packets in the line can be suspected, so change the initial processing setting data. • When exchanging with UDP then conduct a retry of the transmission side program.
80H	The corresponding connection open processing is not completed.	Conduct open processing.
81H	An Ethernet address that does not exist is specified. (Only when the UDP I/P was used as a communication method.)	<ul style="list-style-type: none"> • Check the Ethernet address of the remote node with which exchange is being done. • For an ARP function then set the Ethernet address to 0H/FFFFFFFFH and conduct initial processing.
101H	There is an error with the E71 port No.	Correct the E71 port No. (Refer to Item 5.4.1 [2] (c) ① .)
102H	There is an error with the remote node port No.	Correct the remote node port No. (Refer to Item 5.4.1 [2] (c) ③ .)
103H	There is an error with the port specified to be opened for exchange using TCP/IP.	<ul style="list-style-type: none"> • Correct the exchange address setting area for each connection. • Did not specify the port that is open.
104H	Multiple connections are set with the E71 port No. when exchanging using UDP/IP.	Correct the exchange address setting area for each connection. (Refer to Item 5.4.1. [2] (c) ① .)
105H	The E71 initial processing is not finished.	Conduct E71 initial processing.
106H	The remote node IP address was set to 0 or FFFFFFFFH.	Set the remote node IP address to 1 to FFFFFFFFH.
107H	Open processing has already been conducted for the pairing open connection (For the next connection).	<ul style="list-style-type: none"> • Check if open processing has not been done for either of the connections for the pairing open. • Change the pairing open combination. (Refer to Item 5.4.1 [1] (b) ③.)
108H	An existence check for the partner remote node could not be done within the response monitor timer value.	<ul style="list-style-type: none"> • Check the operation of the partner remote node. • Review and change the set values for existence check. (Refer to Item 5.3.1 [5] to [7].) • Check if the connection cable is loose. • Check if there's a problem with the connection to the transceiver or with the terminal connection.
109H	There is a set value outside the allowable range in the timer set values during initial processing.	Review and correct the timer set values during initial processing. (Refer to Item 5.3.1. [5] to [13] .)
201H	<ul style="list-style-type: none"> • There is an error in the E71 IP address set value during initial processing. • There is an error in the subnet mask field set value when using the router relay function. 	<ul style="list-style-type: none"> • Correct the IP address. Make the class A, B, or C. (Refer to Item 5.3.1 [1], and Item 11.3.) • Correct the subnet mask. (Refer to Item 11.2.)
301H	There is an error in the subnet mask field set value.	Correct the subnet mask and reconduct initial processing. (Refer to Item 11.2.)

End Code Error Code	Description	Processing
302H	<ul style="list-style-type: none"> There is an error in the router relay function default router IP address set value. The default router IP address network address (Network address after subnet mask) differs from the local station E71 IP address network address. 	<ul style="list-style-type: none"> Correct the default router IP address and reconduct initial processing. (Refer to Item 12.4 [1].) Make it the same as the local station E71 IP address network address. (Refer to Item 11.2.)
303H	There is an error in the router relay function subnet address set value.	Correct the subnet address and reconduct initial processing. (Refer to Item 12.4 [3].)
304H	<ul style="list-style-type: none"> There is an error in the router relay function router IP address set value. The router IP address network address (Network address after subnet mask) differs from the local station E71 IP address network address. 	<ul style="list-style-type: none"> Correct the router IP address and reconduct initial processing. (Refer to Item 12.4 [4].) Make it the same as the local station E71 IP address network address. (Refer to Item 11.2.)
4000H to 4FFFH	An error occurred at QnACPU.	Refer to the manual for the QnACPU of the accessed station and take corrective action.
7004H	A connection is not established during TCP connection open processing.	<ul style="list-style-type: none"> Confirm the operation of the other partner node. Confirm the open processing of the other partner node. Correct the set value for the usage of the communication parameter. (See (1) in Section 5.4.1.) Review the port number of the E71 as well as the IP address/port number and the open method of other nodes. Check to see if the connection cable is securely connected. Check to see if the transceiver and terminator are correctly connected.
7010H	A transmission error has occurred.	Same as the corrective action for error code B000H. See the corrective action for B000H.
8001H	A remote node Ethernet address (Buffer memory address 28 to 30, ..., 77 to 79) of 20 digits or more outside the default (FFFFFFFFFH) has been registered.	Be sure to use a default value when using an ARP function.
8002H	End processing has not been conducted.	Conduct initial processing after conducting end processing.
8003H	The next transmission request was conducted even though the transmission processing has not been completed.	Conduct the next transmission request after the transmission end signal turns on.
8004H	A system error has occurred.	Conduct E71 initial processing.
8005H	An initialization error has occurred because of an error in the initial processing parameter.	Correct the initial processing parameter setting values.
9001H	The open processing for the corresponding connection has not been completed.	Conduct open processing.
9002H	A fixed buffer transmission has been conducted during closed processing (Before the open end signal (X10 to 17) is turned off) because the open request signal (Y8 to F) turned off.	Execute so that the corresponding connection open request signal (Y8 to F) and the opened end signal (X10 to 17) are on at the same time for fixed buffer transmission.
9003H	When using TCP/IP, the connection was already closed, close processing was being performed, or there was a close request during open processing.	Adjust the timing between open processing and close processing.
9005H	Insufficient internal resources for the TCP transmission request. Insufficient transmission buffer.	<ul style="list-style-type: none"> Retransmit the same data. It is possible that the next transmission is conducted without waiting for a response. Conduct the next transmission after receiving a response.
9006H	Reception data check some error when TCP/IP is used.	Check the partner remote node check sum calculation.

End Code Error Code	Description	Processing
9008H	Insufficient internal resources for the UDP transmission request. Insufficient transmission buffer.	<ul style="list-style-type: none"> Retransmit the same data. It is possible that the next transmission was conducted without receiving a response. Conduct the next transmission after a response is received.
9009H	Reception data check some error when using UDP/IP.	Check the partner remote node check sum calculation.
9059H	A TCP/ULP time out error occurs during exchange using TCP/IP. (An ACK is not returned from the remote node when using the TCP protocol.)	<ul style="list-style-type: none"> Check the partner remote node operation. Check if the connection cable is loose. Check if there is a problem with the connection to the transceiver or with the terminator connection. Correct the initial parameter TCP/ULP timer value.
A001H to A004H	An ICMP error packet is received.	<ul style="list-style-type: none"> Check if the IP address and port No. set for the E71 are correct. Check if the partner remote node IP address and port No. are correct. Check the partner remote node operation. Check if the connection cable is loose. Check if there's a problem with the connection with the transceiver or with the terminator connection.
A006H	An ICMP error packet is received. (An IP assembly time out occurs at the partner remote node.)	<ul style="list-style-type: none"> Check the partner remote node operation. A packet could be in the line, so transmit after the free time has passed. Check if the connection cable is loose. Check if there's a problem with the connection to the transceiver or with the terminator connection. Correct the partner remote node side IP assembly timer value when there is a time over for the existence time.
A007H	An IP assembly time out error occurs. (The remaining allocation data cannot be received and a time out occurs.)	<ul style="list-style-type: none"> Check the partner remote node operation. A packet could be in the line, so transmit from the remote node after the free time has passed. Check if the connection cable is loose. Check if there is a problem with the connection to the transceiver or with the terminator connection. Correct the IP assembly timer value and reconduct initial processing. (Refer to Item 5.3.1 [12]).
A009H	The set IP address remote node does not exist.	<ul style="list-style-type: none"> Review and correct the partner remote node IP address and Ethernet address. (Refer to Item 5.4.1 [2](c) ② ④.) Set the default value when there is an ARP function in the partner remote node or set the partner remote node Ethernet address when there is no ARP function. Check the partner remote node operation. There could be a packet in the line so transmit after the free time has passed. Check if the connection cable is loose. Check if there's a problem with the connection to the transceiver or with the terminator connection.
A00BH	<ul style="list-style-type: none"> An ICMP error packet was received. An ICMP error packet that was not supported was received. 	<ul style="list-style-type: none"> Check the partner remote node operation. There could be a packet in the line, so transmit after the free time has passed. Check if the connection cable is loose. Check if there's a problem with the connection to the transceiver or with the terminator connection. Correct the partner remote node IP assembly timer value when there is a time over of the existence time. For the current E71 only support a return signal for echo, times stamp, and information request. Make it so that any request other than these are not transmitted from the partner remote node.
A00CH		
A00DH	There is an error in the header check sum of the received IP packet.	<ul style="list-style-type: none"> Review and transmit the correct value for the check sum transmitted by the partner remote node. Investigate the environment's state in the line. (Noise environment, distance between the line and power lines, equipment grounds)

End Code Error Code	Description	Processing
A00E _H	Cannot transmit since no space is available in internal buffers such as IP header buffer.	<ul style="list-style-type: none"> • Transmit the same data once again and confirm the response returned.
	The number of communication destination nodes after initial processing exceeded 20 stations. (Refer to Item 5.2 Point)	<ul style="list-style-type: none"> • Reduce the number of external nodes to communicate. • End communication with all external nodes and perform the E71 initial processing once again.
A00F _H	The number of partner remote nodes with which exchange was done exceeded 20 stations after initial processing. (Refer to Item 5.2 Point)	<ul style="list-style-type: none"> • Reduce the number of remote nodes with which communication is done. • Reconduct E71 initial processing after communication with all remote nodes is completed.
A010 _H	<ul style="list-style-type: none"> • Transmission was requested to a remote node for which the class network address differs from that of the local station when the router relay function was not used. • There is an error in the router relay parameter setting area. 	<ul style="list-style-type: none"> • Set the router relay function to be used and conduct initial processing. • Set the correct data in the router relay parameter area and conduct initial processing. • Correct the transmission destination remote node IP address and conduct open processing. • Check if the network address is correct. When making a change reconduct initial processing.
A011 _H	<p>There is an error in the partner remote node side IP address setting value.</p> <ul style="list-style-type: none"> • Setting the IP address to FFFFFFFF_H is not possible when using TCP. 	<p>Correct the IP address. (Refer to Item 5.4.1 [2] (c) ②.)</p> <ul style="list-style-type: none"> • Simultaneous broadcast communication is not possible using TCP/IP.
B000 _H	A transmission error has occurred.	<ul style="list-style-type: none"> • Check the transceiver and partner remote node operation. • Use a transceiver for which the SQE test can be conducted. • There could be a packet in the line so transmit after the free time has passed. • Check if the connection cable is loose. • Check if there's a problem with the connection to the transceiver or with the terminator connection. • Conduct a loopback test. (Refer to Item 10.7) and check whether there's a problem with the line. • Conduct a self diagnostic test and check whether there's a problem with the E71.
B001 _H	Transmission processing could not be conducted because the cable is not connected or is loose.	

Remarks

The exchange data is sometimes divided and exchanged because of local station and partner station buffer restrictions.

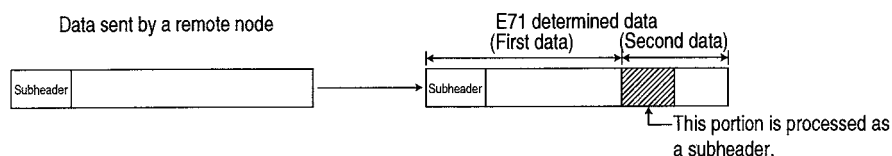
The divided reception data is restored (reassembled) by the E71 and is transmitted by the fixed buffer or the random buffer. The restoration (reassembly) of the received data is done based on the data length in the exchange data.

The following shows the processing by the E71 when the data length specified in the exchange data is incorrect:

1

When the data length specified immediately after the subheader is smaller than the text data amount

- ① The data immediately following the text equivalent to the data length specified immediately after the subheader will be treated as the second message.
- ② Since the head of each message will be a subheader, the E71 performs corresponding processing according to the subheader code.
- ③ If subheaders are other than the codes that can be handled by the E71, the E71 will send a response of abnormal completion to a remote node.



The response at this time is to return a code of 1 for the code's first bit position that was processed as a subheader.

For example, if the command subheader portion is 65H, the response subheader will become E5H.

2

When the data length specified immediately after the subheader is larger than the text data amount

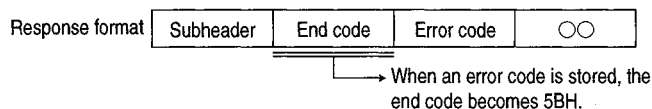
- ① The E71 waits for the reception of the remaining insufficient data.
 - ② If the E71 is able to receive the remaining data within the response monitor timer value, it performs corresponding processing according to the subheader code.
 - ③ If the E71 is not able to receive the remaining data within the response monitor timer value, it performs the following processing:
 - Sends an ABORT (RST) instruction to a remote node and closes the line.
 - Notifies the PLC CPU side of the occurrence of an open error.
(Open error detection signal = ON)
 - Stores an error code in the open error code storage area.
- * The E71 does not store error codes in the E71's error log storage area.

As for the "data length" specified in the application data section of the message to be sent from a remote node to the E71, specify the actual data size of the text section. The E71 does not send text having a data length different from the specified data length to remote nodes.

13.1.2 Error Codes Returned to the Remote Node by Reading and Writing Data in the PLC CPU

This section explains about the error codes that are attached to the responses to the data read/writes to the PLC CPU using E71 commands. (Error codes are only attached when the end code is 5B.)

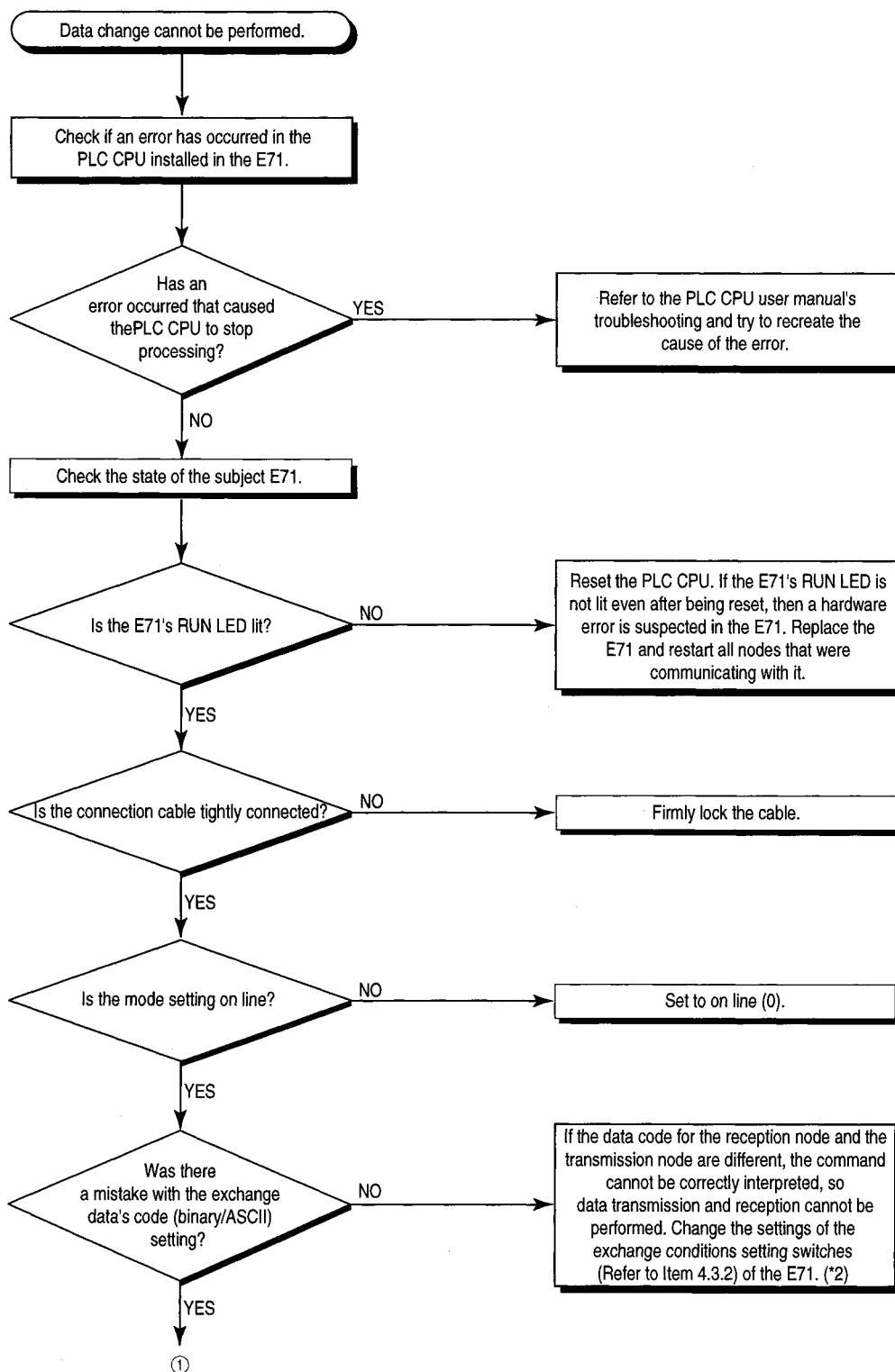
End codes (error codes) with responses attached are described in Item 13.1.1.

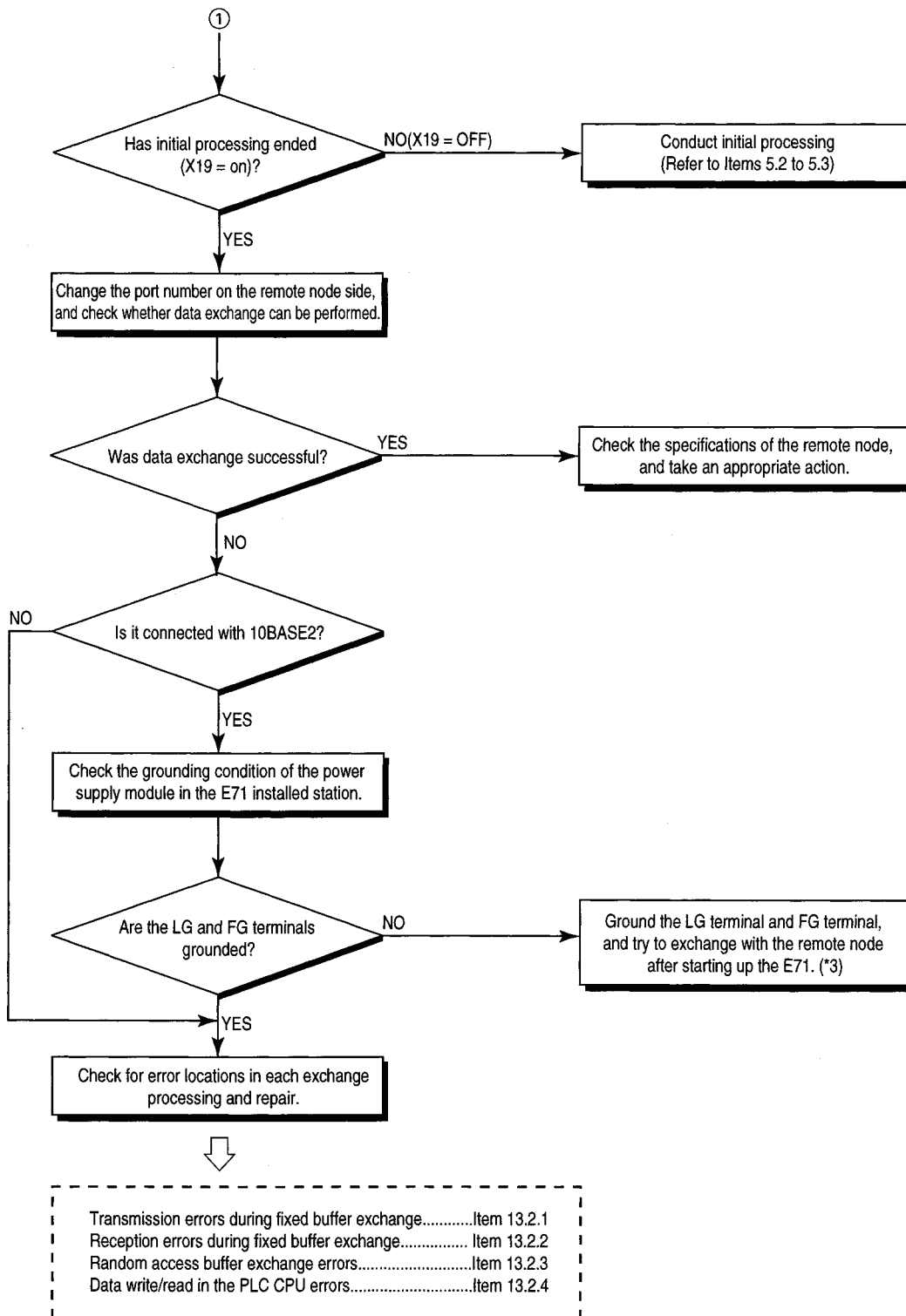


Error Code (Hexadecimal)	Error Item	Error Description	Processing Method
10H	PLC No. error	The PLC No. station does not exist. The PLC No. specified by a command is other than the station No. specified for the local station "FF" and the MELSECNET link parameter settings.	Change the PLC No. to the local station's "FF" or set station No. using the link parameter and reconduct the exchange.
11H	Mode error	Exchange defect between the E71 and PLC CPU After a request from a remote node has been received normally by the E71, for some reason (noise, etc.) normal exchange cannot be conducted between the E71 and the PLC CPU.	Reconduct the exchange. If an error occurs again, check for noise, etc., and then reconduct exchange with the E71.
12H	Special functions unit setting error	Special Functions Unit Error There is no buffer memory with an exchangeable special functions unit in the location specified for the special functions unit No. (For example, the location has an I/O unit or an open slot in the location.).	Change the control procedure specified data contents, or change the special functions unit installation position and reconduct exchange.
13H	Program step No. specification error	Sequence program program step No. specification error. A step No. that exceeds the program capacity range set by the PLC CPU parameter has been specified.	Set a step No. that is within the specified range or change the PLC CPU parameter contents and reconduct communication.
18H	Remote error	Remote STOP/PAUSE has already been conducted by another unit (another E71, etc.).	Check if remote STOP/PAUSE is working or not from other units, perform a delete, and reconduct the exchange.
1FH	Device error	There is an error in the device specification.	(1) Review the specified device. (2) Do not perform the access to the device not existed.
20H	Link error	The request destination CPU unit is disconnected from the data link.	Check if the PLC CPU of the station No. specified in the PLC No. is parallel off. After removing the parallel off cause, reconduct exchange.
21H	Special functions unit bus error	The special function unit's memory cannot be accessed. (1) There is a control bus error with the special functions unit. (2) The special functions unit is damaged.	There is a hardware error in the PLC CPU, base unit, special functions unit, or E71. Consult with your nearest service center, agency, or branch office.

13.2 Troubleshooting Flow

Following is a simple troubleshooting flowchart for when exchange cannot be conducted between the E71 and the remote node. (*1)





- *1 About turning on both the X1C of the input/output signal and display LED's COM. ERR
- (1) The E71 performs the following processing when an error such as a communication error is detected.
- Stores an error code in one of the following buffer memory areas (an area that corresponds to the detected error).
 - Turns on the X1C of the input/output signal (COM. ERR LED on signal).
 - Turns on the display LED's COM. ERR (communication error detection display).

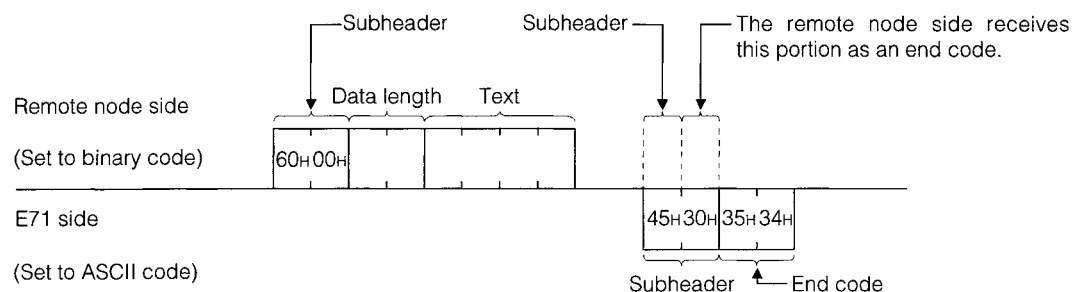
Area name	Address (Hexadecimal (Decimal))	Remarks
Initial error code storage area	50H (80)	—
Open error code storage area	5DH (93)	The addresses shown at left are for connection 1.
Fixed buffer transmission error code storage area	5EH (94)	
Fixed buffer communication end code storage area	5FH (95)	
Error log area	A9H (169) to B3H (179)	—

- (2) When the X1C of the input/output signal is on and the display LED's COM. ERR is on, check an error code stored in one of the above buffer memory areas, verify the error content according to the description in Item 13.1.1, and take an appropriate corrective action.
- (3) The Y17 of the input/output signals (COM. ERR LED off request signal) is used to turn off the X1C of the input/output signal and display LED's COM. ERR.
(Refer to Item 3.6.2(11))
After completing the following operation, turn off the X1C of the input/output signal and display LED's COM. ERR.
- Turn off all input signals (open error detection signal (X18), transmission error detection signal (X1), etc.) that are currently on by error detection.
 - Check the error code and error content.
- * Although it depends on the system specifications, it is recommended to turn off the X1C of the input/output signal and display LED's COM. ERR after a corrective action has been taken for the error.

- *2 If the communication data code setting of the E71 (see Section 4.3.2) and the data code setting of a remote node are different, an error code not listed in the error code list may be returned to the remote node side.

If the E71 receives data with a different data code, it cannot decode the command correctly. The E71 will return an error response according to the communication data code setting.

[Example] When communication is performed using a fixed buffer



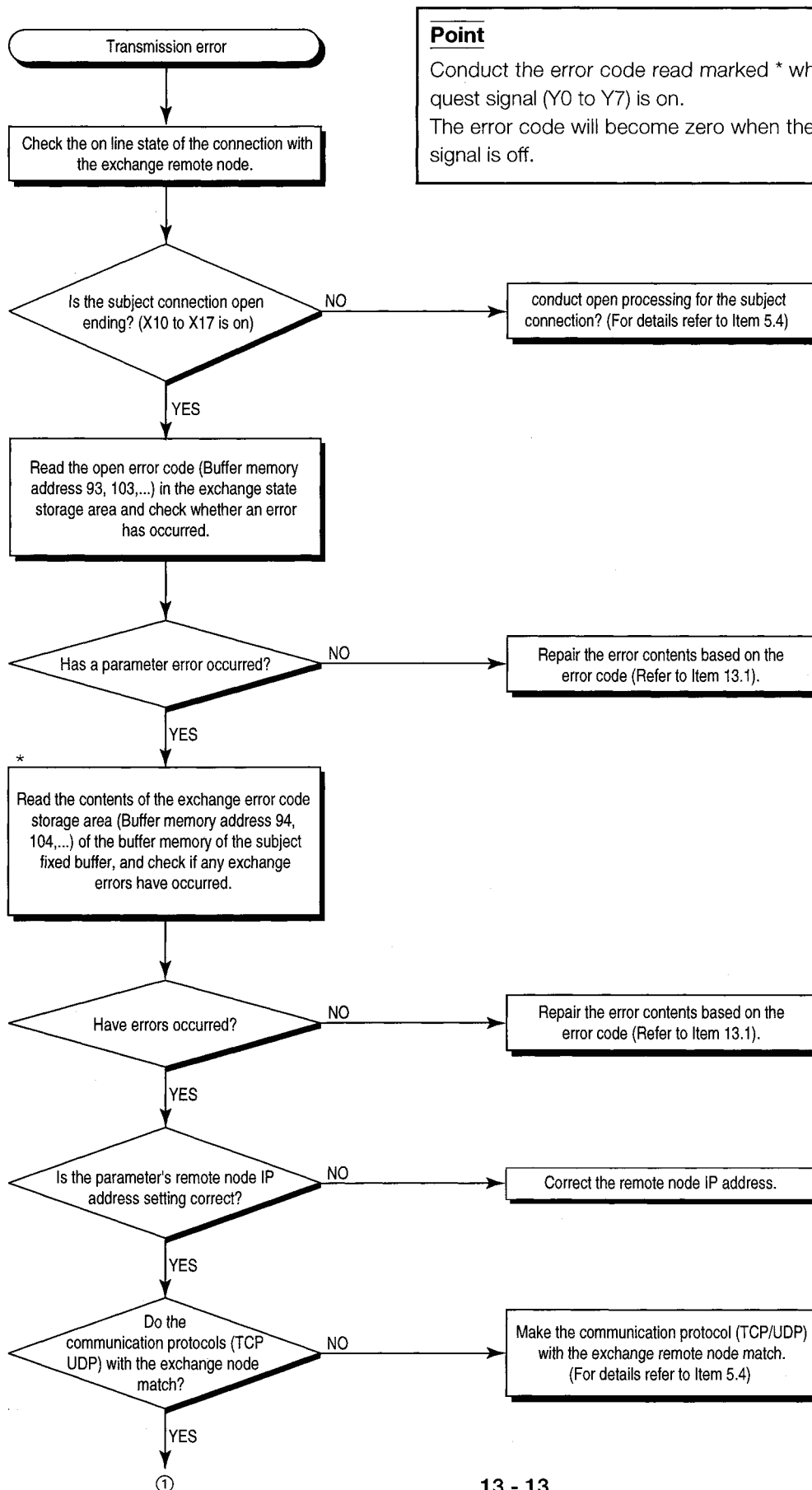
- *3 Be sure to ground the FG terminal and LG terminal of the power supply module in the E71 installed station. If it is not grounded correctly, you can not exchange with the remote node because the influence of noise close (disconnect) the communication line.

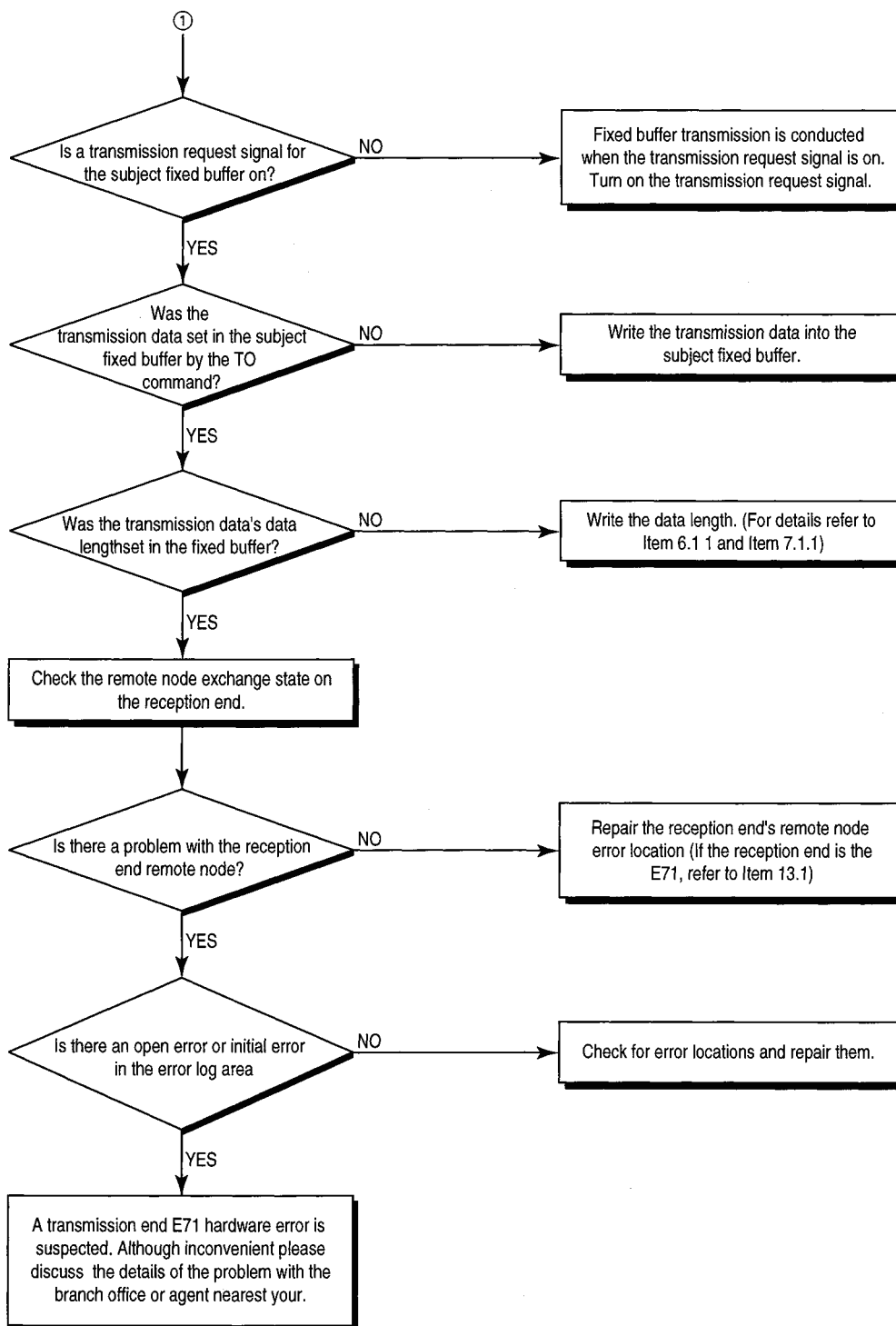
Read carefully the PLC CPU User's Manual that describes the procedure for the installation or wiring work. And then, turn off the E71 installed station power supply in order to reset the grounding of the LG terminal and FG terminal.

Point

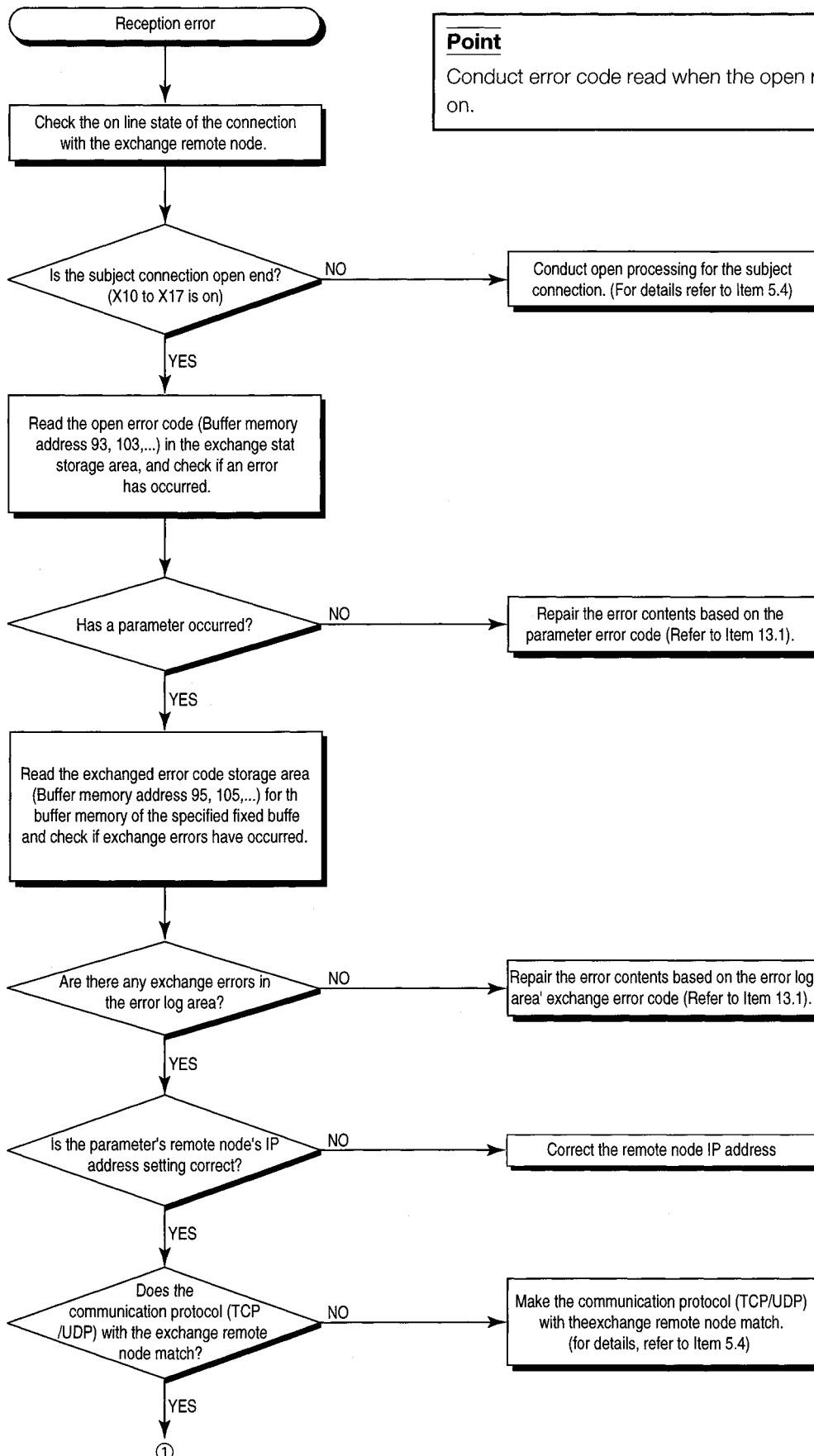
- (1) If the E71 has been replaced due to the occurrence of an abnormality, or the communication board/module on the remote node side that was performing data exchange with the E71 has been replaced, restart the related devices and resume data communication by referring to Point in Section 4.9.
- (2) Verify the required device and connection method by referring to the following when connecting the E71 to Ethernet.
Item 2.6: Verifying the Required Devices
Item 4.7: Verifying the Connection Method
- (3) If messages sent from the remote node cannot be received on the E71 side on a frequent basis, check the values stored in the following buffer memory:
 - Error log storage area (address: A9H...)If the frequency of error generation is high, it might be caused by a high load on the Ethernet line due to data sending/receiving among devices connected. If so, it is necessary to take measures such as separating the network or reducing the data sending count in order to reduce the load on the Ethernet line. Take necessary measures to reduce the load on the Ethernet line after consulting with the network manager.

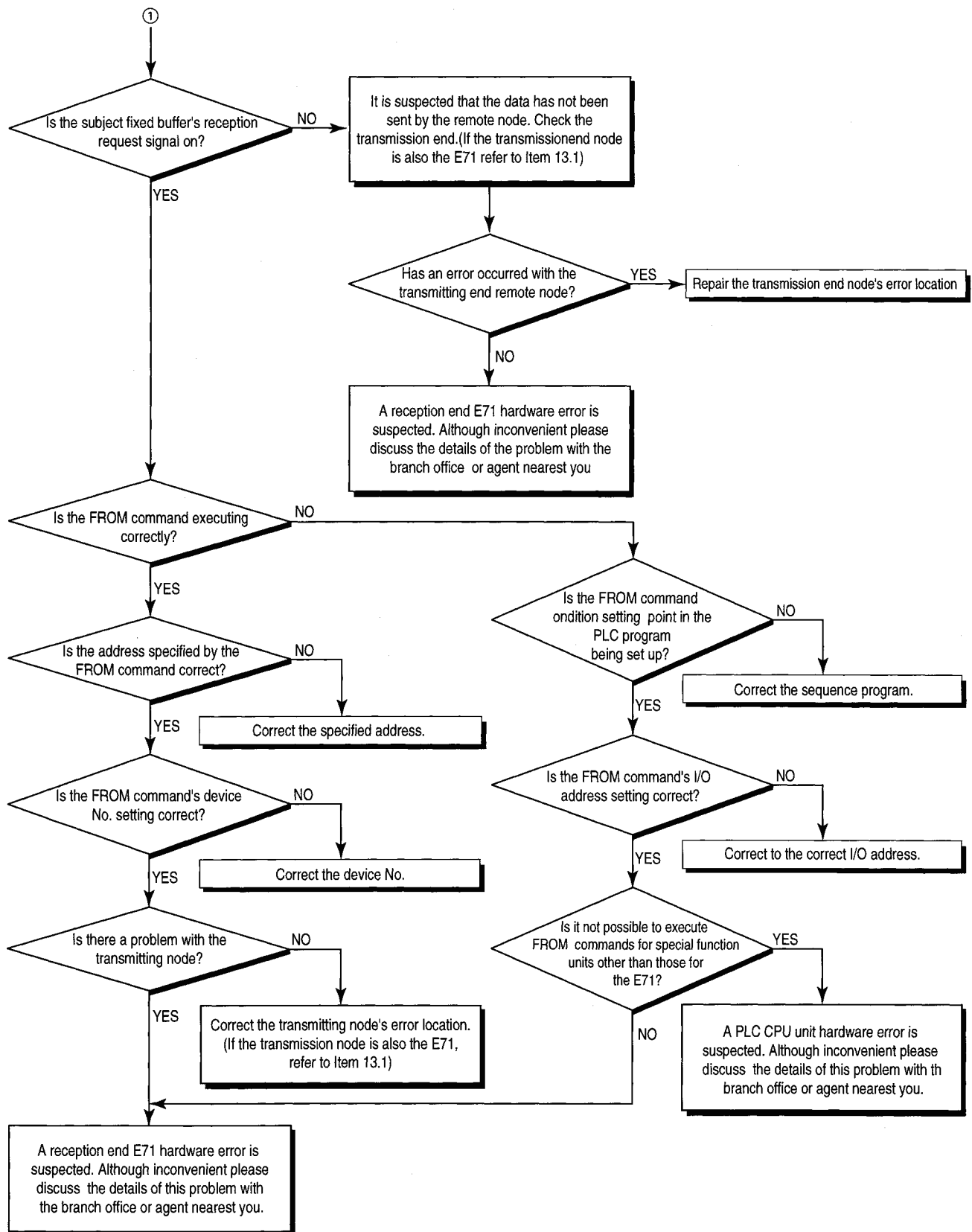
13.2.1 Transmission Error during Fixed Buffer Exchange (Common for Both with Procedure/without Procedure)



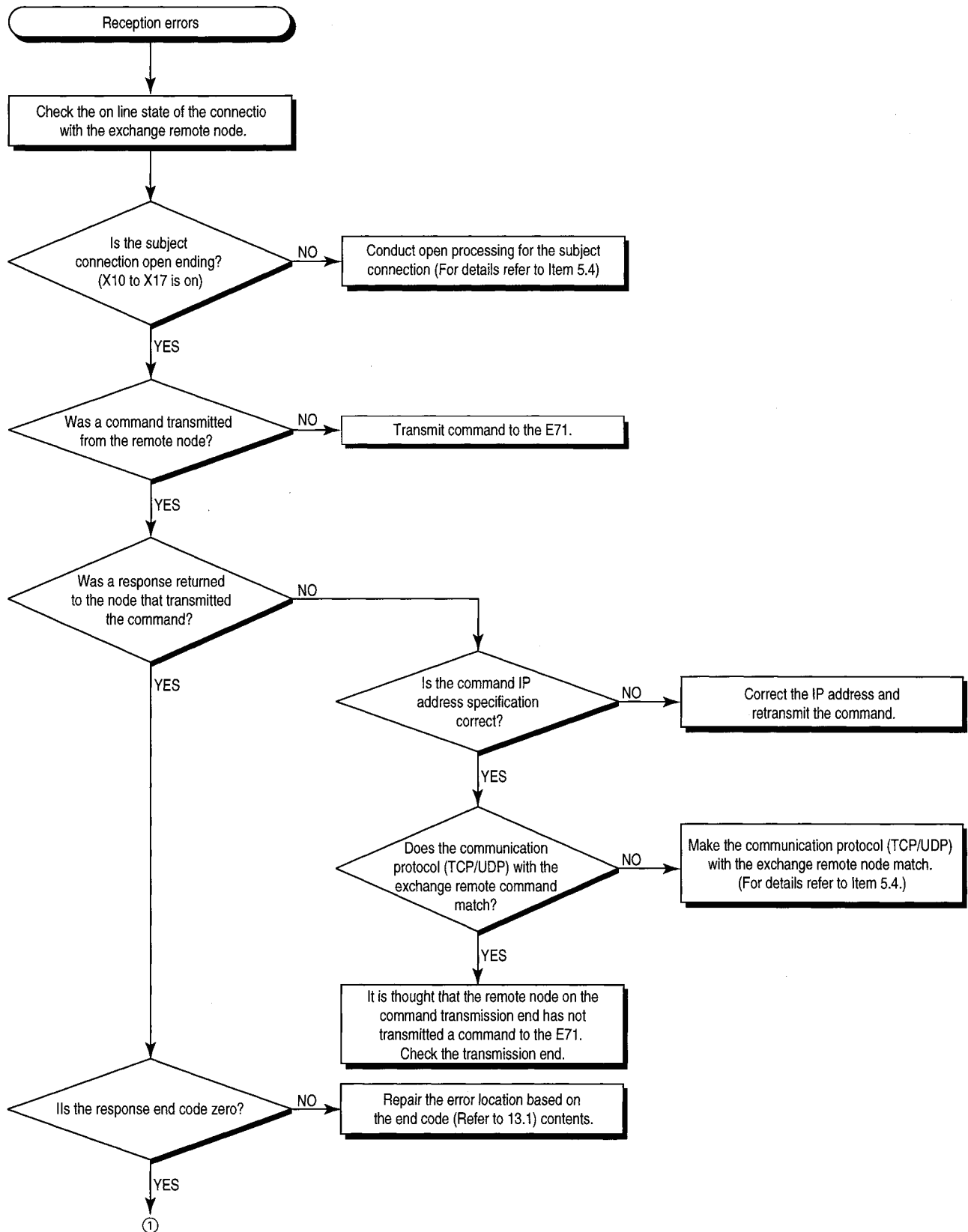


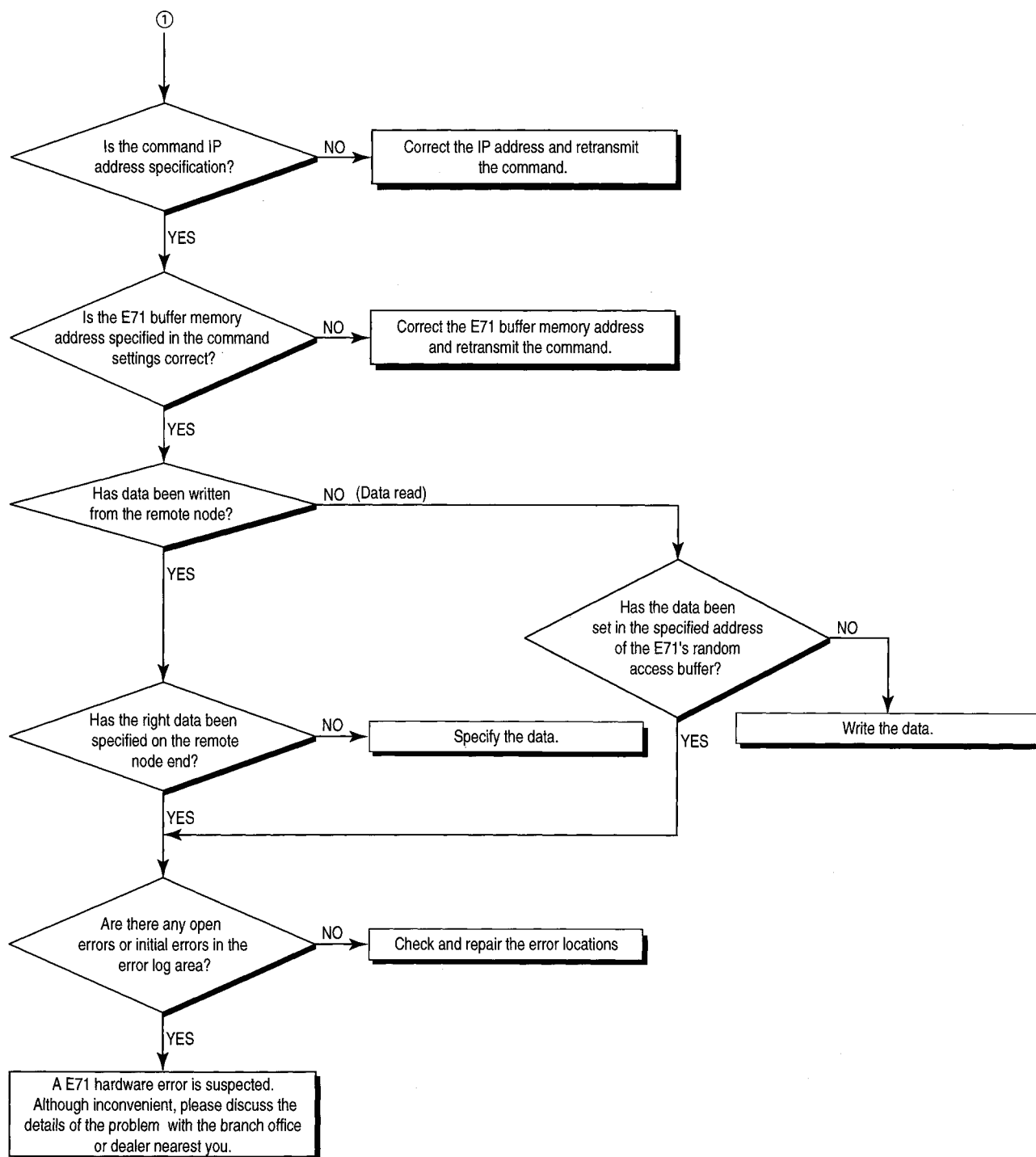
13.2.2 Reception Error during Fixed Buffer Exchange (Common for Both with Procedure/without Procedure)



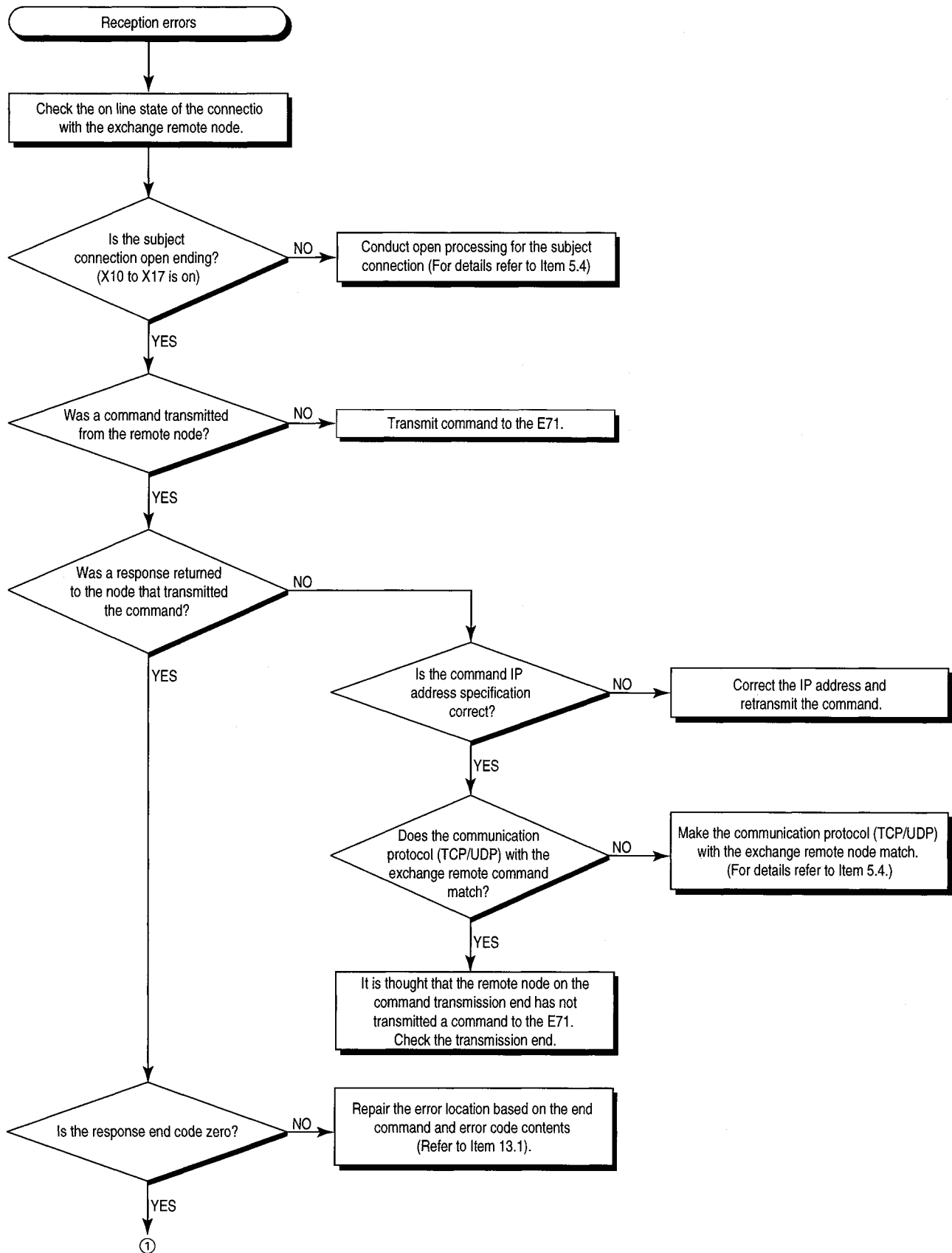


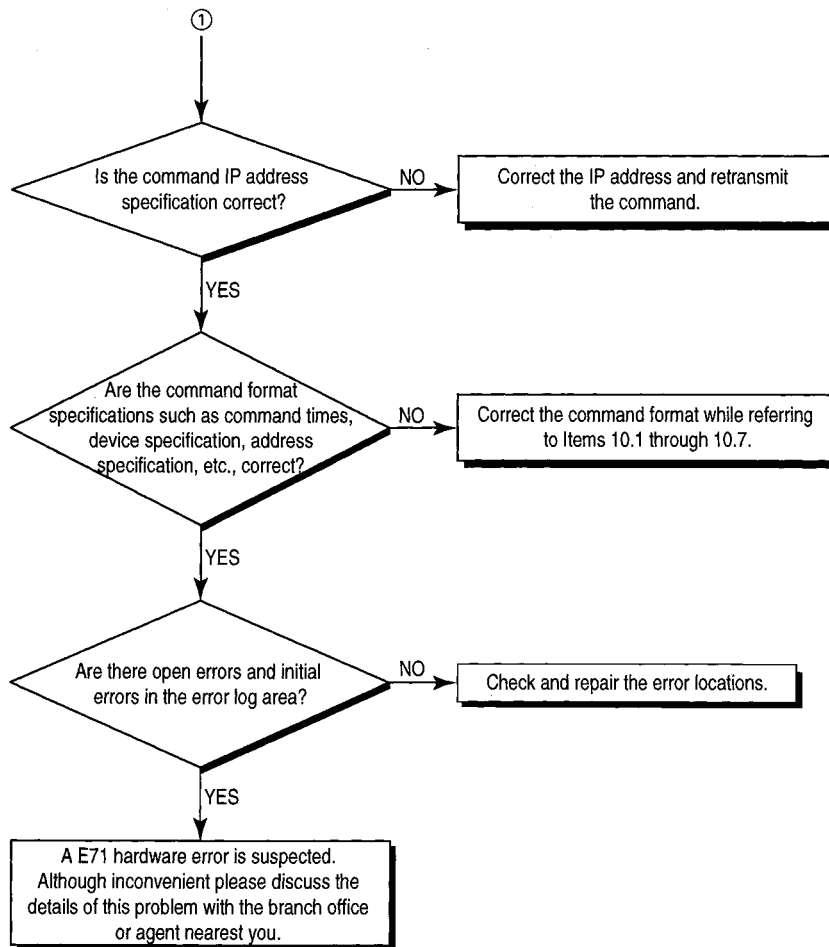
13.2.3 Error during Random Access Buffer Exchange





13.2.4 Error When Reading/Writing Data to the PLC CPU





13.3 Checking the Existence of Errors on the PLC Side and the Contents of Errors

This section explains how to check whether errors, which may occur during data exchange between the E71 and remote nodes, exist on the E71 side and the contents of errors.

1

Countermeasures for errors

If an error occur, be sure to obtain the following details for each device as much as possible:

- ① Type of the trouble occurred
- ② The current phenomenon and the contents of the error that is occurring on the E71 side and the data exchange partner device side.
- ③ Which node side is causing the problem?
- ④ What made the trouble to occur (also check the contents of data exchange at the error occurrence)?
- ⑤ Did any installation environment change before and after the occurrence of the trouble (such as noise)?
- ⑥ Did any system configuration change before and after the occurrence of the trouble (such as system expansion)?
- ⑦ Check to see if the situation will improve by replacing the E71/remote node, if such replacement is possible.
- ⑧ Is the E71 side normal?
 - Check whether hardware is normal. (Check with the self diagnostic test by referring to Item 4.6.)
 - Check whether initial processing has normally been completed. (Check with the RDY LED (flashing) and the input signal X19 (ON) by referring to Items 4.4 and 5.3.)
 - Check whether the connection between the remote node with which data exchange is being performed is open. (Check with the BUFn LED (lit) and the input signals X10 to X17 (ON) by referring to Items 4.4 and 5.4.)

2

How to check the existence of errors on the E71 side and the contents of errors

The following methods are used to check whether there is an error on the E71 side and the contents of the error. Using one of the following methods, check whether an error exists, its content if there is any error, and then take an appropriate corrective action.

- (a) Checking via the LED indicator on the E71

Whether there is an error on the E71 side can be checked by the on/off status of the LED indicator on the E71. (See Item 4.4 and *1 in Item 13.2.)

- (b) Checking via GX Developer

Using GX Developer, various states on the E71 side and the error code corresponding to the contents of the error currently occurring can be checked. It can also perform tests.

- ① Ethernet diagnostics (using a dedicated screen)
 - Various setting status monitoring (see Item 13.4.1)
 - PING test (see Item 13.4.2.)
 - COM.ERR off (see Item 3.6.2 11.)

- ② Buffer memory batch monitoring

Error codes can be checked by monitoring the buffer memory of the E71.

- Exchange state storage area (see Item 5.5)

- (c) Checking the contents of the error from the error code (see Item 13.1)

In order to take a corrective action, the contents of an error can be checked from the error code displayed on the above dedicated screen or via buffer memory monitoring as well as by referring to Item 13.1.

Remarks

If line errors and other errors occur, the user needs to isolate the malfunctioning sections using a line analyzer, etc.

13.4 How to Check an Error Through GX Developer

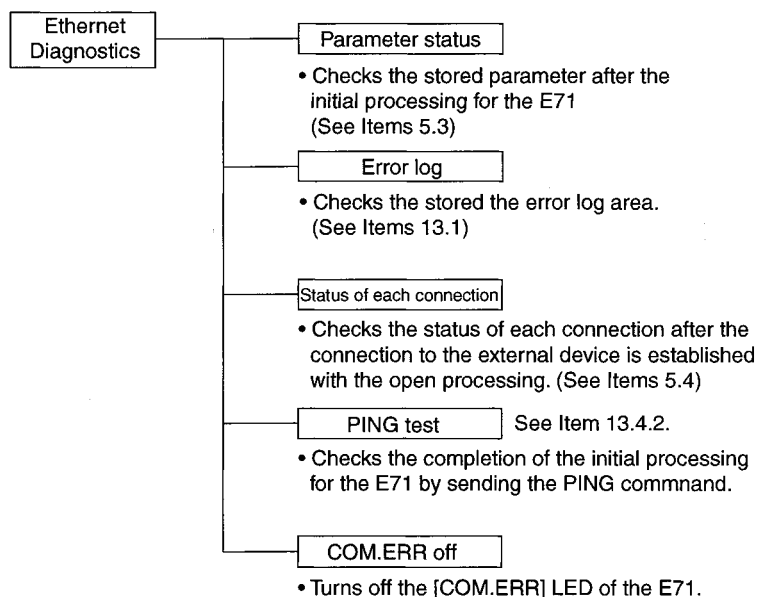
The status of the various settings for the E71 can be checked using the GX Developer (Version 6 or later).

1 Ethernet diagnostics (See Item 13.4.1.)

The status of an E71, parameter settings, exchange status, error log and others can be checked using the Ethernet diagnostic function.

This function cannot be used if an E71 is used with a QnACPU.

The following are the functions of the Ethernet diagnostics.



2 Buffer memory batch monitor (See Item 13.4.3.)

The buffer memory of an Ethernet module is monitored.

Point

See Item 13.4.1 for the buffer memory that can be checked on the "Ethernet diagnostics" screen.

13.4.1 Ethernet Diagnostics

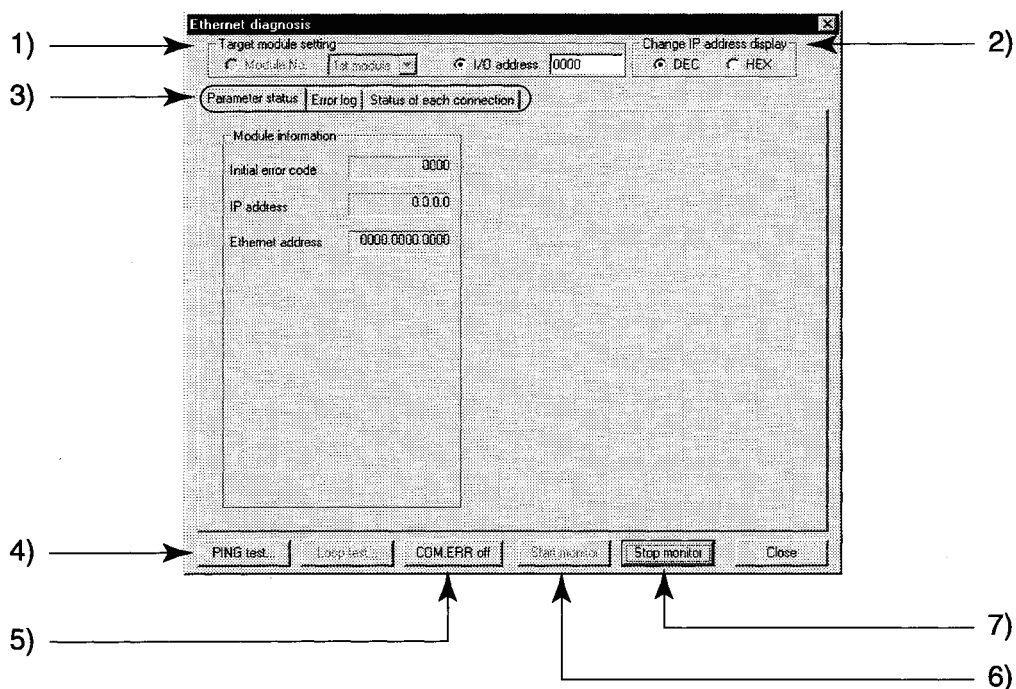
[Purpose]

The status of the E71, parameter setting, communication status, error log and other items can be checked using the Ethernet diagnostic function of GX Developer.

[Operating procedure]

GX Developer → [Diagnostics] → Ethernet diagnostics

[Ethernet diagnostics screen]



[Ethernet diagnostics screen]

No.	Item	Description	Setting range
1	Target module setting	Designates the E71 to be monitored.	1st to 6th
2	Change IP address display	Switches the IP address display between decimal and hexadecimal.	Decimal/hexadecimal
3	Selection of information to be monitored	Selects the E71's various information to be monitored. (For the buffer memory that corresponds to the display content, see the following.)	—
4	PING test	Performs a PING test on remote nodes. (See Section 13.4.2.)	
5	COM.ERR off	Clicking this button turns off the [COM. ERR] LED. (See Item (11) of Section 3.6.2.)	
6	Start monitor	Clicking this button executes Ethernet diagnostics. The display is updated while monitoring is being performed.	
7	Stop monitor	Clicking this button stops Ethernet diagnostics. The display is retained while monitoring is being stopped.	

[Descriptions of Various Status Monitor Displays]

The following shows the buffer memory whose contents can be displayed on the "Ethernet diagnosis" screen of GX Developer.

The screen displays and the contents of displays shown are applicable to the Ethernet diagnosis screen.

Ethernet diagnostics display screen	Display contents		Applicable buffer memory		
			Address Decimal (Hexadecimal)	Name	
Status of each connection	Connection No. 1	Existence confirmation	16 (10H)	Connectio No. 1	Usage available settings
		Pairing open			
		Protocol			
		Open system			
	Connection No. 2 to 8 (same as connection No. 1)		17 to 23 (11H to 17H)	Connection No. 2 to 8 (same as connection No. 1)	
Parameter status	Module information	Initial error code	80 (50H)	Initial error code	
		IP address	81 to 82 (51H to 52H)	Local station E71's IP address	
		Ethernet address	83 to 85 (53H to 55H)	Local station E71's Ethernet address	
Status of each connection	Connection No. 1	Local station port No.	89 (59H)	Connection No. 1	Information by connection
		Exchange partner's IP address	90 to 91 (5AH to 5BH)		
		Destination port No.	92 (5CH)		
		Open error code	93 (5DH)		
		Fixed buffer transfer error code	94 (5EH)		
		Connection end code	95 (5FH)		
	Connection No. 2 to 8 (same as connection No. 1)		99 to 168 (63H to A8H)	Connection No. 2 to 8 (same as connection No. 1)	
Error log	Error code	Lastest to 10	169 to 179 (A9H to B3H)	Error log	Area 1 to 11

Remarks

For more information on how to operate the Ethernet diagnostics screen, refer to the operating manual of GX Developer being used.

13.4.2 PING test using GX Developer (via the Ethernet board)

The following explains how to check the completion status of the E71's initial processing using the PING test function of Ethernet diagnostics.

(1) PING test (via Ethernet board)

- (a) The PING test is performed with GX Developer on the E71 (*1) for which initial processing has been completed on the same Ethernet line or the external device (such as a personal computer) having the designated IP address.

* The PING test can also be performed on the Q/QnA Series Ethernet modules.

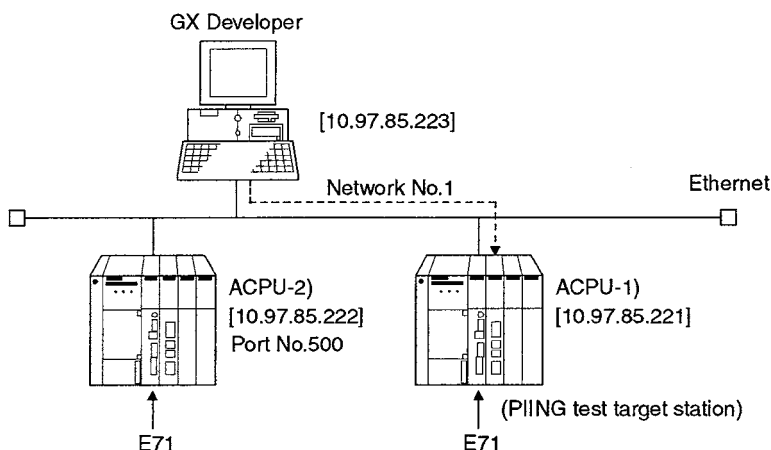
- (b) The following items can be checked by performing the PING test.

- ① Whether line connection with the test target device is properly established
- ② When the target device is a PLC, whether the parameters for initial processing are set correctly, and whether the initial processing is completed normally

- (c) The PING test can be performed for the modules on the same Ethernet as that of the local station (same network address).

(2) Executing the PING test

The example below explains the PING test methodology and GX Developer settings when a PING test is performed with GX Developer for the modules on the same Ethernet.



(a) Settings on the PLC side

- ① Set the switches of the E71.

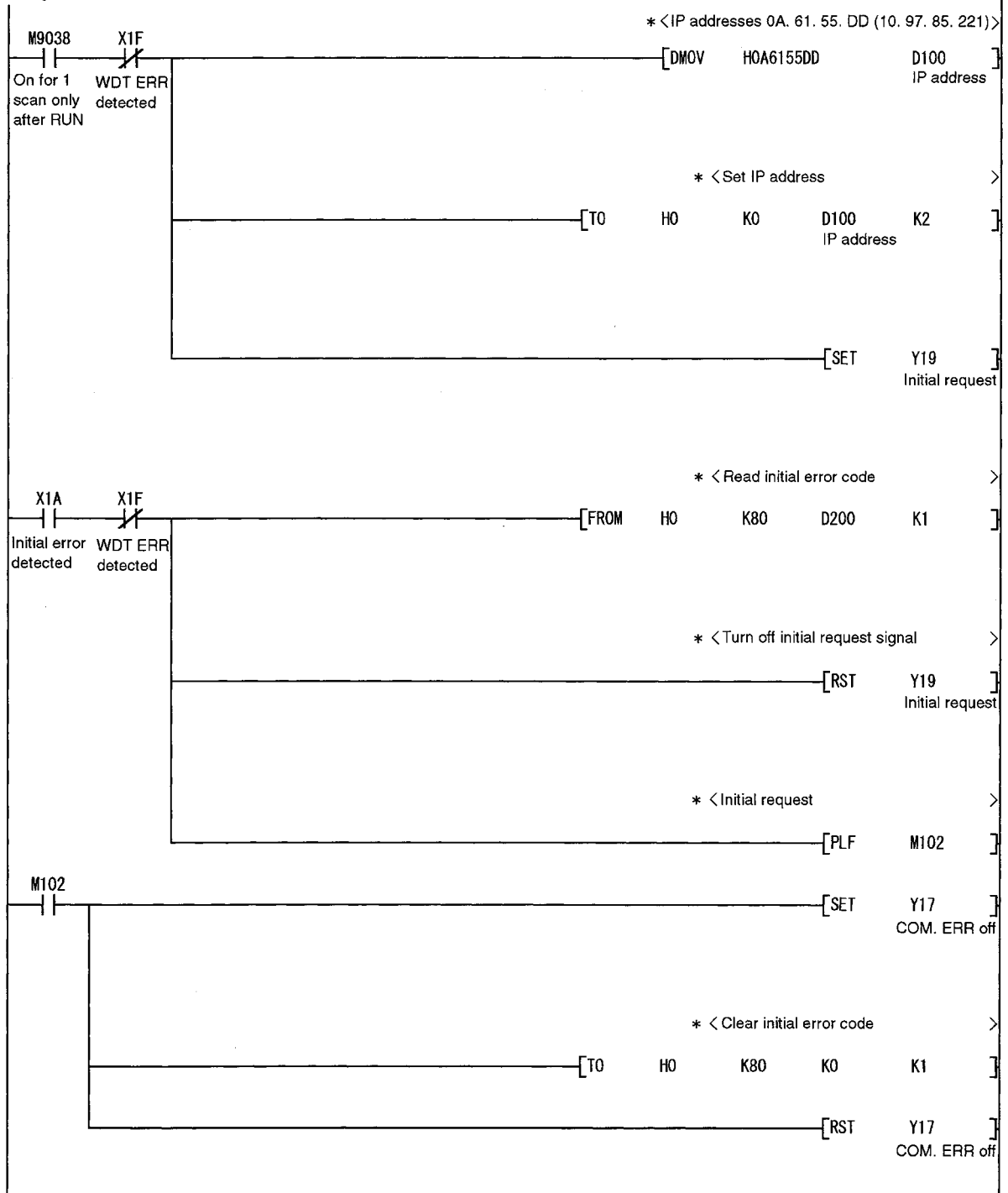
Setting switch			Set value	Setting description
Operation mode setting switch			0	Online
Exchange condition setting switches	SW1	Line processing selection during TCP timeout error	OFF	Closes
	SW2	Data code setting	OFF	binary code
	SW7	CPU exchange timing setting	ON	Write enable
	SW8	Initial timing setting	OFF	Quick start

(Set all switches SW3 to SW6 (use not possible) to off.)

- ② Perform the E71's initial processing and open processing using a sequence program. (Only initial processing is performed on the ACPU-1) side.)
When initial processing ends normally, the E71's [RDY] LED lamp flashes.

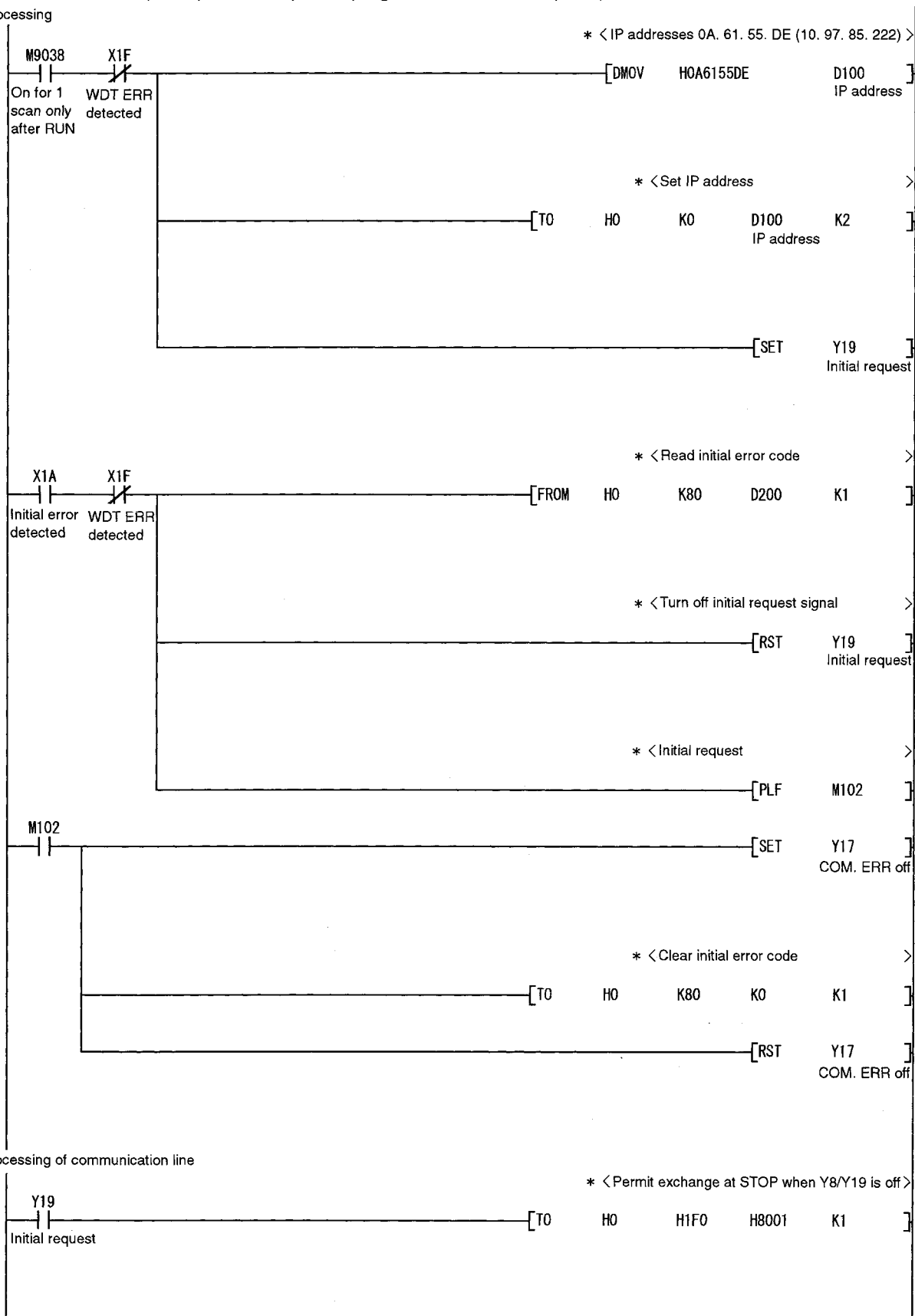
(Example of a sequence program on the ACPU-1) side)

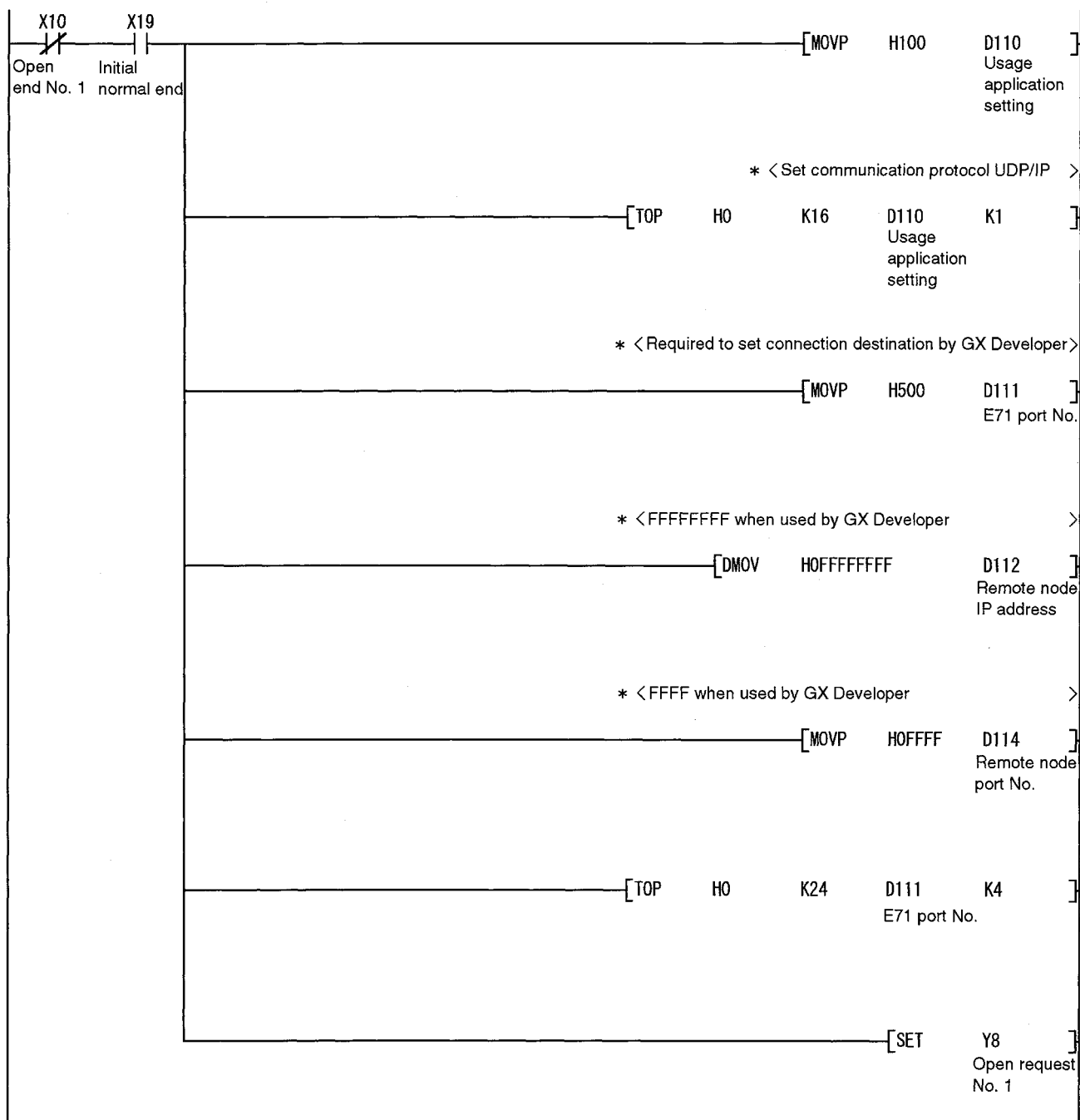
* Initial processing



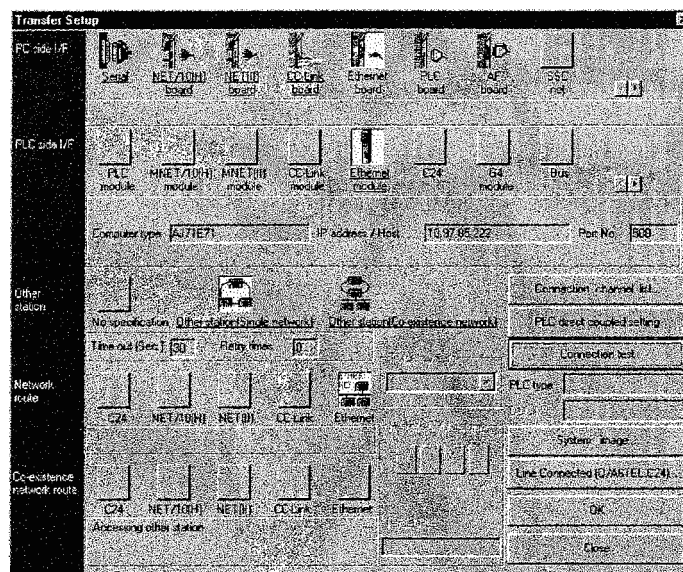
(Example of a sequence program on the ACPU-2) side)

* Initial processing





(b) Connection from GX Developer to ACPU-2)



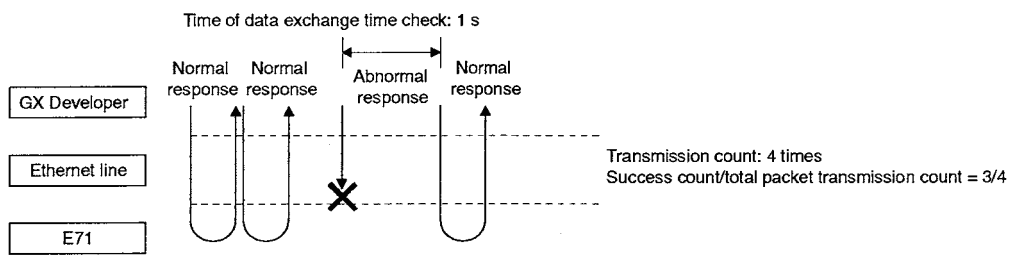
(c) Executing the PING test using GX Developer

- ① Select the PING test on the Ethernet diagnostics screen.

GX Developer → [Diagnostics] → [Ethernet diagnostics] → **PING test**

- ② Perform the settings indicated below, then click the Execute button.
The execution results of the PING test are displayed.

(Example) The following shows the flow of the PING test when "4" is designated as the transmission count.



Normal response occurs when the response for the PING test is received within the time of data exchange time check.

[PING test screen (via the Ethernet board)]

PING test

Input item

Address specification

IP address: 10 | 97 | 85 | 221 IP address input form: DEC HEX

IP address/Host name: []

Option specification

Display the host name: [] Default

Specify the data size: 32 bytes

Specify the time of the communication time check: 1 seconds

Specify the number of transmissions: 4 times

Execute Cancel

Result

Pinging 10.97.85.221 with 32 bytes of data:

```

Reply from 10.97.85.221: bytes=32 time=8ms TTL=250
Reply from 10.97.85.221: bytes=32 time=4ms TTL=250
Reply from 10.97.85.221: bytes=32 time=5ms TTL=250
Reply from 10.97.85.221: bytes=32 time=5ms TTL=250
Packets transmitted = 4, Received = 4, Lost = 0
Round-trip (ms) Min = 4, Max = 8, Avg = 5
  
```

success/transmissions = 4 / 4 Close

(Example of normal completion)

PING test

Input item

Address specification

IP address: 10 | 97 | 85 | 221 IP address input form: DEC HEX

IP address/Host name: []

Option specification

Display the host name: [] Default

Specify the data size: 32 bytes

Specify the time of the communication time check: 1 seconds

Specify the number of transmissions: 4 times

Execute Cancel

Result

Pinging 10.97.85.221 with 32 bytes of data:

```

Request timed out.
Request timed out.
Request timed out.
Request timed out.
Request timed out.
Packets transmitted = 4, Received = 0, Lost = 4
Round-trip (ms) Min = 0, Max = 0, Avg = 0
  
```

success/transmissions = 0 / 4 Close

(Example of abnormal completion)

[Display contents]

Item name		Description of item setting	Setting range/option
Address specification	IP address	Specify the IP address of the PING test target station.	(IP address of the target station)
	IP address input format	Select the input format of the IP address.	Decimal/hexadecimal
	Host name	Specify the host name of the PING test target station.	—
Option specification	Display the host name	Results are displayed using the host name corresponding to the IP address in the result display field.	—
	Specify the data size	Specify the size of the system data to be transmitted during the PING test.	1 to 8192 bytes (Specify 1460 bytes or less for the E71.)
	Specify the exchange time check	Specify the completion wait time for the PING test.	1 to 30 seconds
	Specify the transmission count	Specify the transmission count.	<ul style="list-style-type: none"> Specify the count. Execute until interrupted.
Result		Display the result of the PING test.	—
Success count/total packet transmission count		Display the total packet transmission count and its success count during the PING test execution.	—

(Address specification)

The PING test target station (external device subject to the PING test) is specified by the IP address or the host name.

① Specification using the IP address

- Select the input format for the IP address (select: Decimal or hexadecimal)
- Specify the IP address of the external device according to the input format (decimal or hexadecimal).

② Specification using the host name

Specify the host name of the external device set in the DNS server or the HOSTS file for the PC on which GX Developer is installed.

* The IP address can also be entered in the host name specification field.

(Option specification)

Set the details for the PING test. (No setting required if the default settings are used.)

- ① Display the host name.
Select the host name in the result display field instead of the IP address for the PING test destination device.
- ② Specify the data size.
Specify the size of the system data to be transmitted for the test during the PING test.
Input range: 1 to 8192 bytes (default: 32 bytes)
* The E71 will return a response of 1460 bytes if the PING test is performed by specifying a data size of 1460 bytes or more.
- ③ Specify the time of the data exchange time check.
Specify the response wait time for the PING test.
Input range: 1 to 30 s (default: 1 s)
- ④ Specify the transmission count.
Specify the number of times the PING test is to be executed.

Selection item	Description of item	Remark
Specify the count	The PING test is executed for the number of times specified.	Transmission count: 1 to 50 times (default: 4 times)
Execute until interrupted	Execute until interrupted	—

(Result)

The results of the PING test are displayed.

<When the test is completed abnormally>

Upon checking the following, perform the PING test again.

- Whether the E71 is mounted on the base unit properly
- The connection status to the Ethernet.
- The contents of the parameters loaded to the PLC CPU.
- The operating status of the PLC CPU (whether any errors have occurred)
- The IP addresses set in the PC installed with GX Developer and the PING test target station
- Whether the external devices were reset when the E71 was replaced

(Success count/total packet transmission count)

The success count and the total packet transmission count when the PING test is executed are displayed.

13.4.3 Checking the Error Information by the Buffer Memory Batch Monitoring Function

It is explained how the error codes stored in the buffer memory of the E71 can be monitored using the "Buffer memory batch monitoring" function of GX Developer.

[Operating procedure]

(Step 1) Select [Online] - [Monitor] - [Buffer memory batch] from the GX Developer, and start the "Buffer memory batch monitoring" screen.

(Step 2) Enter [Module start address:].

For the module start address, enter the start I/O number of the E71 to be monitored as four digits.

(Example)

Start I/O signal: Enter "0020" when monitoring modules of X/Y0020 to 003F

(Step 3) Enter [Buffer memory start address:].

Enter the buffer memory address to be monitored, using the selected input format (decimal/hexadecimal).

For a list of the buffer memory addresses where error codes are stored, see Section 13.1, "Error Code List".

(Example)

When monitoring the initial abnormal code (buffer memory storage address: 80H):

Enter "80" + "hexadecimal"

(Step 4) Click the Start Monitor button.

The contents of the buffer memory after the specified address are displayed. (In case of the above example, the contents of 80H and succeeding addresses are displayed.)

Remarks

The display format can be modified as follows:

Monitor format : Bits & words/ Multiple bit points/ Multiple word points

Display : 16-bit integer/32-bit integer/real number/ASCII character

Numerical value: Decimal/hexadecimal

For details, refer to the "Operating Manual" for GX Developer.

This image shows a single sheet of white paper with horizontal ruling lines. The lines are evenly spaced and run across the width of the page. There are no margins, text, or other markings on the paper.

APPENDICES

Appendix 1 Utilizing Programs for Previous products and Replacing Modules

Data exchange between the PLC CPU and a remote node on the Ethernet performed by an A series Ethernet interface module (previous product such as AJ71E71 and AJ71E71-S3) can also be performed using the E71.

The following explains how to utilize programs used for previous products and how to replace modules in order to perform data exchange using the E71.

* The response speed is different between the previous E71 products and the new E71 product. (For the processing time, see Appendix 3.) After a previous E71 product is replaced with a new E71 product, or programs used for a previous E71 product are utilized for a new E71 product, be sure to check the operation and adjust the exchange time interval and the retry count as necessary.

Appendix 1.1 Module Compatibility

The following explains module compatibility when an old A series Ethernet interface module (previous product such as AJ71E71 and AJ71E71-S3, which is abbreviated as the E71 (previous product) hereinafter) is replaced with a new E71 described in this manual.

- (1) The hardware specifications of the E71 and E71 (previous product, product dropped from production) are the same. (They are compatible.)

The following table shows the interfaces and 5VDC internal current consumption values for the E71 and E71 (previous product, product dropped from production).

	Model name	Interface			5VDC internal current consumption value
		10BASE2	10BASE5	10BASE-T	
E71 (previous product, product dropped from production)	AJ71E71 (-S3)	○	○	×	1.50A (Hardware version: Version B or earlier) 0.48A (Hardware version: Version C or later)When 10BASE2 is used 0.26A (Hardware version: Version C or later)When 10BASE5 is used
	AJ71E71N-B5T *1	×	○	○	0.55A
	AJ71E71N-T	×	×	○	0.55A
	A1SJ71E71-B2(-S3)	○	×	×	0.52A (Hardware version: Version A) 0.57A (Hardware version: Version B to D) 0.49A (Hardware version: Version E or later)
	A1SJ71E71-B5(-S3)	×	○	×	0.35A
	A1SJ71E71N-B5T *2	×	○	○	0.55A
	A1SJ71E71N-T	×	×	○	0.56A
E71	AJ71E71N-B2	○	×	×	0.67A
	AJ71E71N-B5	×	○	×	0.55A
	AJ71E71N3-T	×	×	○	0.69A
	A1SJ71E71N-B2	○	×	×	0.66A
	A1SJ71E71N-B5	×	○	×	0.57A
	A1SJ71E71N3-T	×	×	○	0.69A

*1 The hardware specifications are the same as those of the AJ71E71N-T and AJ71E71N-B5. (There is compatibility.)
The 10BASE-T and 10BASE5 interfaces cannot be used together.

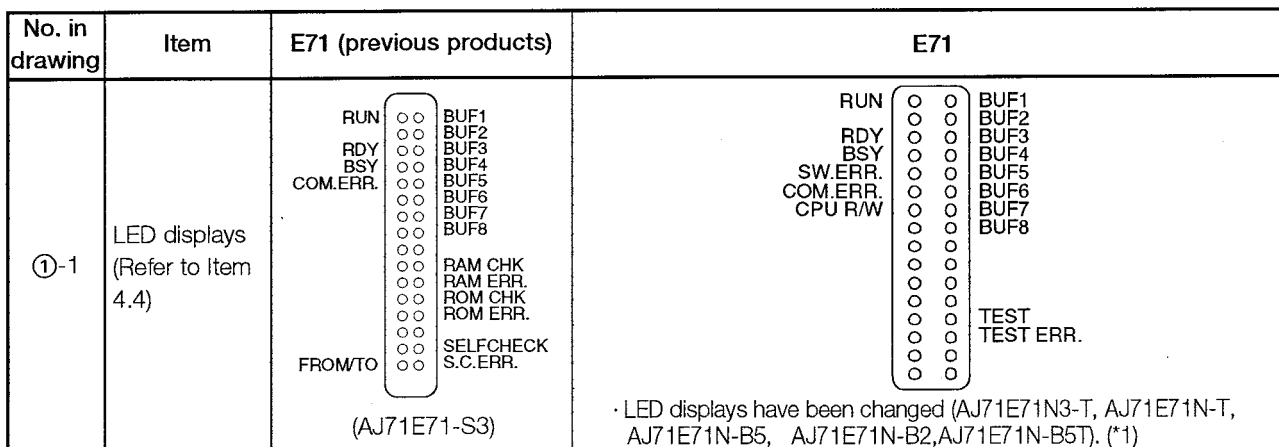
*2 The hardware specifications are the same as those of the A1SJ71E71N-T and A1SJ71E71N-B5. (There is compatibility.)
The 10BASE-T and 10BASE5 interfaces cannot be used together.

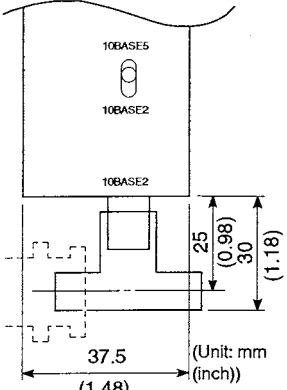
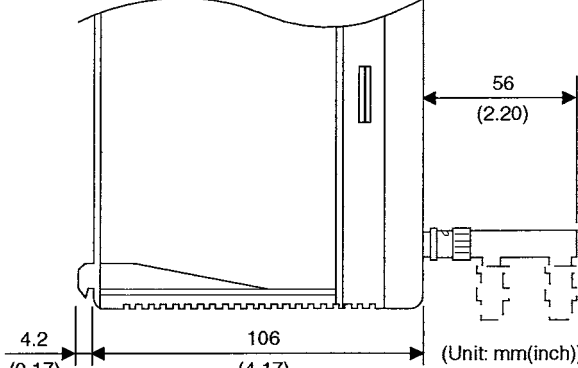
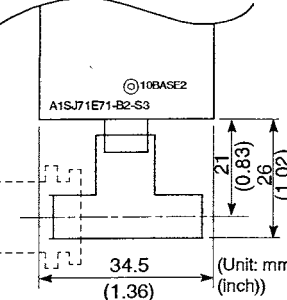
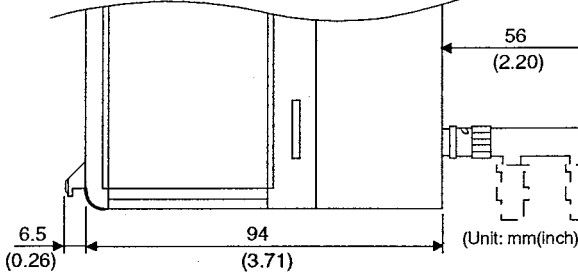


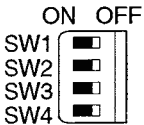
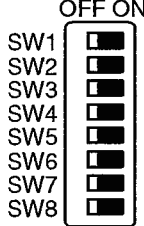
- (2) Precautions for replacing the E71 (previous product, product dropped from production) with the E71


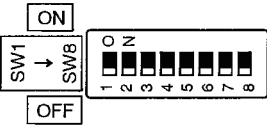
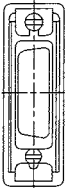
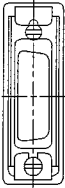
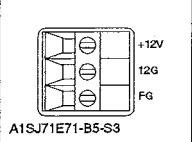
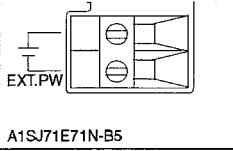


When replacing the E71 (previous product) with the E71, make sure that the current capacity of the used power supply module is sufficient.

If it is not sufficient, replace the power supply module with the one that can supply necessary currents.

[E71 (previous products)]



No. in drawing	Item	E71 (previous products)	E71
①-2	LED displays (Refer to Item 4.4)	RUN B1 RAM CHK B2 RAM ERR. RDY B3 ROM CHK B4 ROM ERR. B5 S.C. B6 S.C. ERR. BSY B7 COM.ERR. B8 FROM/TO (A1SJ71E71-B5-S3, A1SJ71E71-B2-S3)	RUN <input type="checkbox"/> BUF1 RDY <input type="checkbox"/> BUF2 BSY <input type="checkbox"/> BUF3 SW.ERR. <input type="checkbox"/> BUF4 COM.ERR. <input type="checkbox"/> BUF5 TEST <input type="checkbox"/> BUF6 TEST ERR. <input type="checkbox"/> BUF7 CPU R/W <input type="checkbox"/> BUF8 LED displays have been changed (A1SJ71E71N3-T, A1SJ71E71N-T, A1SJ71E71N-B5, A1SJ71E71N-B2, A1SJ71E71N-B5T). (*1)
②-1	10BASE2 interface connector position (Refer to Appendix 6)	 (AJ71E71-S3)	 The connector position has been changed from the bottom side to the front side (AJ71E71N-B2).
②-2		 (A1SJ71E71-B2-S3)	 The connector position has been changed from the bottom side to the front side (A1SJ71E71N-B2).
③	Operation mode setting switch (Refer to Item 4.3.1)	 (A1SJ71E71-B5-S3, A1SJ71E71-B2-S3)	 (A1SJ71E71N3-T, A1SJ71E71N-T, A1SJ71E71N-B5, A1SJ71E71N-B2, A1SJ71E71N-B5T) The operation mode setting switch (rotary switch) has been changed. It can be used with the same setting as the E71 (previous products).
④-1	Communication condition setting switch (Refer to Item 4.3.2)	 (A1SJ71E71-B5-S3, A1SJ71E71-B2-S3)	 (A1SJ71E71N3-T, A1SJ71E71N-T, A1SJ71E71N-B5, A1SJ71E71N-B2, A1SJ71E71N-B5T) The ON and OFF positions of the communication condition setting switch have been swapped. The communication condition setting switch has been changed from a 4-setting switch to an 8-setting switch. ① The CPU communication timing setting has been changed from SW3 to SW7. ② The initial timing setting has been changed from SW4 to SW8. The communication condition setting switch has been changed from the front side to the left side (A1SJ71E71-T, A1SJ71E71N-B5).

No. in drawing	Item	E71 (previous products)	E71
④-2	Communication condition setting switch (Refer to item 4.3.2)		 <ul style="list-style-type: none"> The communication condition setting switch of the hardware version "B" or later is oriented horizontally. (A1SJ71E71N-B5)
⑤	10BASE5 interface connector orientation (Refer to Appendix 6)	 (A1SJ71E71-B5-S3)	 <ul style="list-style-type: none"> The orientation of the connector has been rotated by 180 degrees. (A1SJ71E71N-B5, A1SJ71E71N-B5T).
⑥	External power supply terminal (Refer to Item 4.7.2)	 (A1SJ71E71-B5-S3)	 (A1SJ71E71N-B5, A1SJ71E71N-B5T) <ul style="list-style-type: none"> The FG terminal has been removed from the external power supply terminal (transceiver power supply connection terminal). If the external power supply side of the power supply source for the transceiver handles the FG signal, ground the FG signal on the external power supply side.
⑦	10BASE-T interface connector orientation (Refer to Appendix 6)	 (AJ71E71N-T, AJ71E71N-B5T, A1SJ71E71N-T, A1SJ71E71N-B5T)	 (AJ71E71N3-T, A1SJ71E71N3-T) <ul style="list-style-type: none"> The orientation of the connector has been rotated by 180 degrees.

* 1: The LED displays of the E71 have been renamed and/or added compared to the E71 (previous products) as shown below:

LED name of E71	LED name of E71 (previous product)	LED name of E71 Description of display
RUN	RUN	Indicates normal operation status
RDY	RDY	Indicates communication ready complete status
BSY	BSY	Indicates communication processing in execution status
SW. ERR.	—	(For system)
COM. ERR.	COM. ERR.	Indicates communication error detection status
CPU R/W	FROM/TO	Indicates data exchange in execution status with the PLC CPU
BUF1 to BUF8	B1 to B8 BUF1 to BUF8	Connection status of communication line
TEST	RAM CHK	Indicates self-diagnosis in execution status
	ROM CHK	
	S.C. SELFCHK	
TEST ERR.	RAM ERR.	Indicates result of self-diagnosis
	ROM ERR.	
	S.C. ERR.	

Appendix 1.2 Program Utilization

The following explains the data communication program compatibility when substituting the E71 for the E71 (previous products).

Appendix 1.2.1 Remote Node Side Program Utilization

The following explains the remote node side data communication program compatibility when the E71 is substituted for the E71 (previous products).

- (1) The program for the following exchange function portion for the E71 (previous products) can be utilized to conduct data exchange for the E71. However, the response performance differs somewhat between the E71 (previous products) and the E71, so there are times when data exchange cannot be utilized as is. The response timeout time between the E71 and the remote node needs to be adjusted.
- (2) The functions that can be used for exchange for program utilization are as follows.
The remote node in the table is the node that is conducting data exchange to the E71 (previous products).

Exchange Partner Utilization program		Remote Node to E71	E71 to Remote Node	E71 to E71 (previous products)	E71 to E71 (previous products)
Exchange Functions	Fixed buffer exchange (With procedure)	○	○	○	○
	Random access buffer exchange	○			
	Reading/writing data to the PLC CPU	○			

○ : Exchange is possible by diverting as is the program for the E71 (previous products) from the remote node

Remarks

When using E71 exchange functions other than those above, create a new exchange program.

- (3) The E71 IP address class must be changed to class A through class C. Set the IP address to be set in the E71 and conduct open processing/data exchange.

Appendix 1.2.2 Sequence Program Utilization

The following explains the PLC CPU data exchange program compatibility when substituting the E71 for the E71 (previous products).

- (1) It is possible to conduct data exchange to a remote node from the E71 by utilizing the fixed buffer exchange (with procedure) function portion of the program for the remote node. However, the response performance between the AJ71E71 and E71 differs somewhat, so there are times the data exchange cannot be utilized as is. It is necessary to adjust the response timeout time between the E71 and the remote node.
- (2) The functions for which exchange is possible when utilizing the program are as shown below. The remote nodes shown in the table are remote nodes with which data exchange is being conducted with the E71 (previous products).

Exchange Partner Utilization program		Remote Node to E71	E71 to Remote Node	E71 to E71 (previous products)	E71 to E71 (previous products)
Exchange Functions	Fixed buffer exchange (With procedure)	○	○	○	○
	Random access buffer exchange				
	Reading/writing data to the PLC CPU				

○ : Exchange is possible by diverting as is the program for the E71(previous products)

Remarks

When using E71 exchange functions other than those above, create a new exchange program.

- (3) Change the E71 IP address class to class A through class C. Set the IP address to be set in the E71 for the primary remote node and conduct open processing/data exchange.
- (4) There is no restriction on the number of nodes that can be communicated by one initial processing of the E71.
(For the AJ71E71-S3, A1SJ71E71-B5-S3 and A1SJ71E71-B2-S3, if the software version is "version K" or earlier, the maximum number of nodes that can be communicated by one initial processing is 20 stations.)

Appendix 2 Adding the Ethernet Interface Module to the Existing System

The E71 and E71 (previous products) can coexist in the same Ethernet. The wiring used for the E71 (previous products) can be used as is in the existing system Ethernet that contains the E71.

Appendix 3 Processing Time

Use the following formulas to calculate the minimum transmission delay time for each function.

However, the transmission delay time is sometimes increased by the network load rate (line connections), each node window size, the number of connections used at the same time, and the system configuration. The value found using the following formula is used as the transmission delay time measure when conducting exchange while using one connection.

1 Fixed buffer exchange minimum transmission delay time (When exchanging between E71 and E71)

(a) For TCP/IP

$$\left(\begin{array}{l} \text{When communicating} \\ \text{in binary code} \end{array} \right) 21 + (0.012 \times \frac{\text{Command data length}}{\text{The unit is 1 byte}}) + (0.012 \times \frac{\text{Response data length}}{\text{The unit is 1 byte}}) \\ + (\text{Transmission scan time}) + (\text{Reception scan time}) \text{ (ms)}$$

$$\left(\begin{array}{l} \text{When communicating} \\ \text{in ASCII code} \end{array} \right) 21 + (0.017 \times \frac{\text{Command data length}}{\text{The unit is 1 byte}}) + (0.017 \times \frac{\text{Response data length}}{\text{The unit is 1 byte}}) \\ + (\text{Transmission scan time}) + (\text{Reception scan time}) \text{ (ms)}$$

(b) For UDP/IP

$$\left(\begin{array}{l} \text{When communicating} \\ \text{in binary code} \end{array} \right) 18 + (0.012 \times \frac{\text{Command data length}}{\text{The unit is 1 byte}}) + (0.012 \times \frac{\text{Response data length}}{\text{The unit is 1 byte}}) \\ + (\text{Transmission scan time}) + (\text{Reception scan time}) \text{ (ms)}$$

$$\left(\begin{array}{l} \text{When communicating} \\ \text{in ASCII code} \end{array} \right) 18 + (0.017 \times \frac{\text{Command data length}}{\text{The unit is 1 byte}}) + (0.017 \times \frac{\text{Response data length}}{\text{The unit is 1 byte}}) \\ + (\text{Transmission scan time}) + (\text{Reception scan time}) \text{ (ms)}$$

Command data length : This is the data length including the subheader, data length, and text data specified in the command application data portion for fixed buffer data transmission. The unit is 1 byte.

	Command Data Length	
	With Procedure	Without Procedure
Exchange using binary code	$4 + (\text{Data length}) \times 2$	(Number of text bytes)
Exchange using ASCII code	$8 + (\text{Data length}) \times 4$	—

Response data length : This is the data length that is fixed in the response application data portion and includes the subheader and end code when receiving data using the fixed buffer. The unit is 1 byte.

	Response Data Length	
	With Procedure	Without Procedure
Exchange using binary code	2	—
Exchange using ASCII code	4	—

[Example calculation]

The minimum transmission delay time when transmitting 1017 words of data between the E71 and an E71 when TCP/IP binary code exchange is used for the protocol.

(The transmission scan time is 10ms, and the reception scan time is 10ms.)

$$21 + (0.012 \times (4 + (1017 \times 2))) + (0.012 \times 2) + 10 + 10 \approx 65 \text{ (ms)}$$

2**Random access buffer exchange minimum transmission delay time**

(a) For TCP/IP

$$\left(\begin{array}{l} \text{When communicating} \\ \text{in binary code} \end{array} \right) 6 + (0.005 \times \frac{\text{Command data length}}{\text{The unit is 1 byte}}) + (0.002 \times \frac{\text{Response data length}}{\text{The unit is 1 byte}}) + (\text{Remote node ACK processing time}) \text{ (ms)}$$

$$\left(\begin{array}{l} \text{When communicating} \\ \text{in ASCII code} \end{array} \right) 6 + (0.009 \times \frac{\text{Command data length}}{\text{The unit is 1 byte}}) + (0.003 \times \frac{\text{Response data length}}{\text{The unit is 1 byte}}) + (\text{Remote node ACK processing time}) \text{ (ms)}$$

(b) For UDP/IP

$$\left(\begin{array}{l} \text{When communicating} \\ \text{in binary code} \end{array} \right) 4 + (0.005 \times \frac{\text{Command data length}}{\text{The unit is 1 byte}}) + (0.002 \times \frac{\text{Response data length}}{\text{The unit is 1 byte}}) \text{ (ms)}$$

$$\left(\begin{array}{l} \text{When communicating} \\ \text{in ASCII code} \end{array} \right) 4 + (0.008 \times \frac{\text{Command data length}}{\text{The unit is 1 byte}}) + (0.003 \times \frac{\text{Response data length}}{\text{The unit is 1 byte}}) \text{ (ms)}$$

Command data length : This is the data length including the subheader, data length, text data, that is set in the command application data portion when reading and writing is conducted to and from the random access buffer. The unit is 1 byte.

	Command Data Length	
	Read	Write
Exchange using binary code	6	6 + ((Data length) × 2)
Exchange using ASCII code	12	12 + ((Data length) × 4)

Response data length : This is the data length including the subheader and end code, that is set in the response application data portion when reading from and writing to the random access buffer is conducted.

	Response Data Length	
	Read	Write
Exchange using binary code	2 + ((Data length) × 2)	2
Exchange using ASCII code	6 + ((Data length) × 4)	6

Remote node ACK processing : This is the time from when the read/write to or from the random buffer ends until the remote node returns ACK.

[Example calculation 1]

The minimum transmission delay time when 508 words of data are read from the data register (D) using the UDP/IP's ASCII code exchange as the protocol

$$4 + (0.008 \times 12) + (0.003 \times (6 + (508 \times 4))) \approx 10 \text{ (ms)}$$

[Example calculation 2]

The minimum transmission delay time when 508 words of data are written to the data register (D) using the UDP/IP's ASCII code as the exchange protocol

$$4 + (0.008 \times (12 + (508 \times 4))) + (0.003 \times 6) \approx 20 \text{ (ms)}$$

3

Minimum transmission delay time when reading/writing (word units) to the PLC CPU

(a) For TCP/IP

$$\begin{aligned}
 &\left(\begin{array}{l} \text{When communicating} \\ \text{in binary code} \end{array} \right) 18 + \underbrace{(0.020 \times (\text{Command data length}))}_{\text{The unit is 1 byte.}} + \underbrace{(0.007 \times (\text{Response data length}))}_{\text{The unit is 1 byte.}} \\
 &\quad + (\text{PLC CPU processing time}) + (\text{Remote node ACK processing time}) \text{ (ms)} \\
 &\left(\begin{array}{l} \text{When communicating} \\ \text{in ASCII code} \end{array} \right) 20 + \underbrace{(0.036 \times (\text{Command data length}))}_{\text{The unit is 1 byte.}} + \underbrace{(0.012 \times (\text{Response data length}))}_{\text{The unit is 1 byte.}} \\
 &\quad + (\text{PLC CPU processing time}) + (\text{Remote node ACK processing time}) \text{ (ms)}
 \end{aligned}$$

(b) For UDP/IP

$$\begin{aligned}
 &\left(\begin{array}{l} \text{When communicating} \\ \text{in binary code} \end{array} \right) 14 + \underbrace{(0.010 \times (\text{Command data length}))}_{\text{The unit is 1 byte.}} + \underbrace{(0.006 \times (\text{Response data length}))}_{\text{The unit is 1 byte.}} \text{ (ms)} \\
 &\quad + (\text{PLC CPU processing time}) \\
 &\left(\begin{array}{l} \text{When communicating} \\ \text{in ASCII code} \end{array} \right) 15 + \underbrace{(0.012 \times (\text{Command data length}))}_{\text{The unit is 1 byte.}} + \underbrace{(0.006 \times (\text{Response data length}))}_{\text{The unit is 1 byte.}} \text{ (ms)} \\
 &\quad + (\text{PLC CPU processing time})
 \end{aligned}$$

Command data length : This is the data length including the subheader, data length, and text data, that is specified in the command application data portion when data is read from or written to the PLC CPU. The unit is 1 byte. The command data length changes depending on the commands used. Refer to Items 10.1 and 10.2 through 10.7.

Response data length : This is the data length including the subheader and the when data is read from or written to the PLC CPU. The unit is 1 byte. The command data length varies depending on the commands used. Refer to the Item 10.1 and Items 10.2 through 10.7.

PLC CPU processing time : This is the processing time of the request to read/write data to the PLC CPU. This time is determined by the read/write data, number of points to process, and the PLC CPU scan time. Refer to (c).

PLC CPU processing time =

$$\frac{(\text{Specified number of points}) + (\text{Number of points processed in one sequence program scan}) \times (\text{Scanner time})}{\text{Rounded off below the decimal point}}$$

Remote node ACK processing : This is the time from the PLC CPU data read/write end time until the remote node returns an ACK.

[Example calculation 1]

The minimum transmission delay time (Scan time is 10ms, and includes its lengthened time shown in (c).) when reading data register (D) of 100 points of data while using the UDP/IP's binary code for the exchange protocol.

Command data length = 12 bytes

Response data length = 202 bytes

PLC CPU process time = $\frac{100}{64} \times 10 = 20 \text{ (ms)}$
 ≈ 2

Minimum transmission delay time = $14 + (0.010 \times 12) + (0.006 \times 202) + 20$
 $\approx 35 \text{ (ms)}$

[Example calculation 2]

The minimum transmission delay time (Scan time is 10ms, and includes its lengthened time shown in (c).) when writing 100 points of data to the data register D100 using the UDP/IP's binary code as the exchange protocol.

Command data length = 212 bytes

Response data length = 2 bytes

PLC CPU process time = $\frac{100}{64} \times 10 = 20 \text{ (ms)}$
 ≈ 2

Minimum transmission delay time = $14 + (0.010 \times 212) + (0.006 \times 2) + 20$
 $\approx 36 \text{ (ms)}$

(c) Scan time extension time

In response to a request from the E71, the PLC CPU main unit processes the number of process points for 1 scan of the sequence program for each END when running. The intervention time for this scan time and the number of scans required for processing are shown below.

Item				Intervening time (scan extension)		Maximum number of processing points between the E71 and the remote node	Number of processing points for each sequence program scan	Number of scans required for processing	
				AnSCPU A1SJCPU A0J2HCPU AnNCPU	A2ASCPU AnACPU AnUCPU				
Device data	Device memory	Batch read	Bit unit		0.76ms	1.38ms	256 points	256 points	1 scan
			Word unit	Bit device	1.13ms	2.42ms	128 words (2048 points)	32 words (512 points)	(Specified number of points/32) scan Decimal point rounded off (Maximum 4 scans)
				Word device	1.13ms	2.42ms	256 points	64 points	Other than device R (Specified number of points/64) scan Decimal point rounded off (Maximum 4 scans)
			Batch write	Word unit	Word device	256 points	64 points	Device R (Specified number of points/64) + 1 scan Decimal point rounded off (Maximum 5 scans)	
								2 scans (1 scan when set to possible during RUN.)	
								(Specified number of points/10) + 1 scan Decimal point rounded off "0" when set to possible during RUN. (Maximum 5 scans)	
		Batch write	Word unit	Word device	256 points	64 points	Other than device R (Specified number of points/64) + 1 scan Decimal point rounded off "0" when set to possible during RUN. (Maximum 5 scans)		
							Device R (Specified number of points/64) + 1 scan Decimal point rounded off (Maximum 5 scans)		

Item				Intervening time (scan extension)		Maximum number of processing points between the E71 and the remote node	Number of processing points for each sequence program scan	Number of scans required for processing		
				AnSCPU A1SJCPU A0J2HCPU AnNCPU	A2ASCPU AnACPU AnUCPU					
Device data	Device memory	Test (random write)	Bit unit		1.13ms	1.06ms	80 points	20 points	(Specified number of points/20) + 1 scan Decimal point rounded off <div>↓</div> "0" when set to possible during RUN. (Maximum 5 scans)	
			Bit device		1.13ms	1.06ms	40 words (640 points)	10 words (160 points)	(Specified number of points/10) + 1 scan Decimal point rounded off <div>↓</div> "0" when set to possible during RUN. (Maximum 5 scans)	
			Word unit		1.13ms	1.06ms	40 points	10 points	Other than device R <div>(Specified number of points/10) + 1 scan Decimal point rounded off <div>↓</div> "0" when set to possible during RUN. (Maximum 5 scans)</div>	
									Word device	Device R <div>(Specified number of points/10) + 1 scan Decimal point rounded off (Maximum 5 scans)</div>
			Monitor data register		Bit unit		—	—	—	—
				Word unit				—	—	1 scan for device R only
		Monitor		Bit unit		2.02ms	1.46ms	40 points	40 points	1 scan
				Word unit	Bit device	2.08ms	1.47ms	320 points (20 words)	320 points (20 words)	1 scan
					Word device	2.08ms	1.47ms	20 points	20 points	
	Exten- sion file register		Batch read		1.27ms	2.42ms	256 points	64 points	(Specified number of points/64) + 1 scan Decimal point rounded off (Maximum 5 scans)	
			Batch write		1.27ms	2.60ms	256 points	64 points		
			Direct read		—	2.30ms	256 points	64 points		
			Direct write		—	2.57ms	256 points	64 points		
			Test (random write)		1.31ms	0.97ms	40 points	10 points		
Monitor data register			—	—	—	—	— — —			
		Monitor		1.75ms	1.42ms	20 points	20 points	1 scan		
Special func- tion module buffer memory		Batch read		FROM instruction process- ing time + 1.13 ms	FROM instruction process- ing time + 0.75 ms	256 bytes	128 bytes	(Specified number of points/128) scan Decimal point rounded off (Maximum 2 scans)		
		Batch write						(Specified number of points/128) + 1 scan Decimal point rounded off <div>↓</div> "0" when set to possible during RUN. (Maximum 3 scans)		

Item				Intervening time (scan extension)		Maximum number of processing points between the E71 and the remote node	Number of processing points for each sequence program scan	Number of scans required for processing
				AnSCPU A1SJCPU AQJ2HCPU AnNCPU	A2ASCPU AnACPU AnUCPU			
Program	Sequence program	Batch read	Main	1.20ms	0.70ms	256 steps	64 steps	(Specified number of steps/64) scan Decimal point rounded off (Maximum 4 scans)
			Sub	1.20ms	0.70ms			
		Batch write	Main	0.67ms	0.49ms	256 steps	64 steps	(Specified number of steps/64) + 1 scan Decimal point rounded off ↓ "0" when set to possible during RUN. (Maximum 5 scans)
			Sub	0.67ms	0.49ms			
	Micro- computer program	Batch read	Main	1.35ms	—	256 bytes	128 bytes	(Specified number of bytes/128) + 1 scan Decimal point rounded off (Maximum 3 scans)
			Sub	1.35ms				
		Batch write	Main	1.35ms				
			Sub	1.53ms				
	Comment	Batch read		1.35ms	2.42ms	256 bytes	128 bytes	(Specified number of bytes/128) + 1 scan Decimal point rounded off (Maximum 3 scans)
		Batch write		1.53ms	2.60ms			
	Extension comment	Batch read		—	2.31ms	256 bytes	128 bytes	(Specified number of bytes/128) + 1 scan Decimal point rounded off (Maximum 3 scans)
		Batch write		—	2.59ms			
	Parameter	Batch read		0.68ms	2.42ms	256 bytes	128 bytes	(Specified number of bytes/128) + 1 scan Decimal point rounded off (Maximum 3 scans)
		Batch write		—	—	—	—	—
		Analysis request		—	—	—	—	—
PLC CPU		Remote RUN		—	—	—	—	—
		Remote STOP		—	—	—	—	—
		PLC model name read		—	—	—	—	1 scan

Point

- (1) Because the PLC CPU can only process one of the above items during END processing, for the corresponding PLC CPU to access the A6GPP, E71, etc. at the same time it must wait until the other processing is finished. This further increases the number of scans required for processing.
- (2) The scan time is extended by approximately 0.2 ms (A2AS, AnA, and AnUCPU are 0.1 ms) even when the E71 is not installed and link is not conducted.

Appendix 4 ASCII Code Table

LSD	MSD	0	1	2	3	4	5	6	7
		000	001	010	011	100	101	110	111
0	0000	NUL	DLE	SP	0	@	P	,	p
1	0001	SOH	DC1	!	1	A	Q	a	q
2	0010	STX	DC2	!!	2	B	R	b	r
3	0011	ETX	DC3	#	3	C	S	c	s
4	0100	EOT	DC4	\$	4	D	T	d	t
5	0101	ENQ	NAK	%	5	E	U	e	u
6	0110	ACK	SYN	&	6	F	V	f	v
7	0111	BEL	ETB	/	7	G	W	g	w
8	1000	BS	CAN	(8	H	X	h	x
9	1001	HT	EM)	9	I	Y	i	y
A	1010	LF	SUB	*	:	J	Z	j	z
B	1011	VT	ESC	+	;	K	[k	{
C	1100	FF	FS	,	<	L	/	l	
D	1101	CR	GS	—	=	M]	m	}
E	1110	SO	RS	.	>	N	↑	n	~
F	1111	SI	VS	/	?	O	←	o	DEL

Appendix 5 Reference Documents

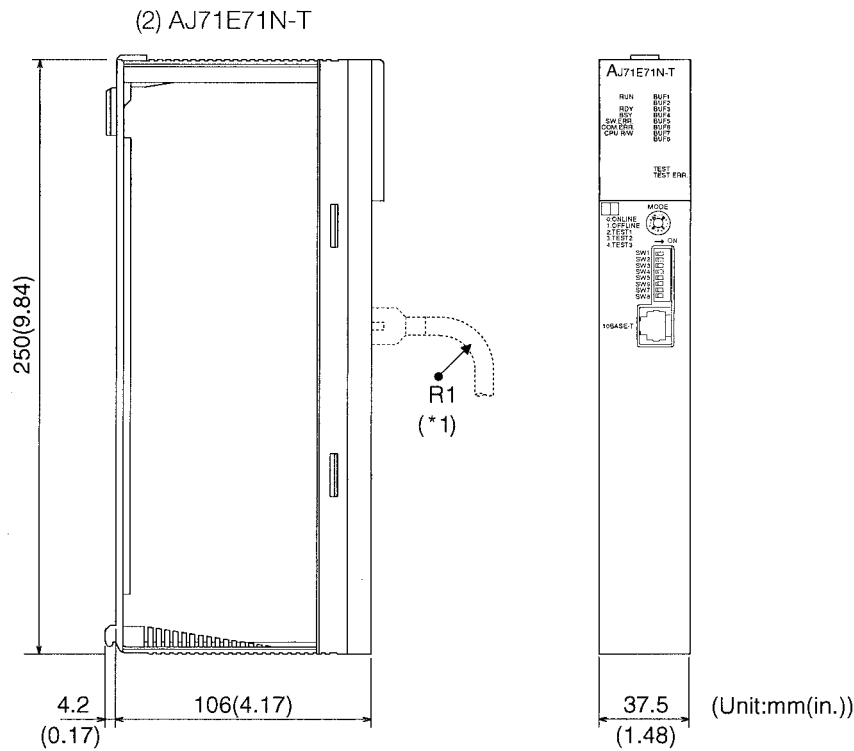
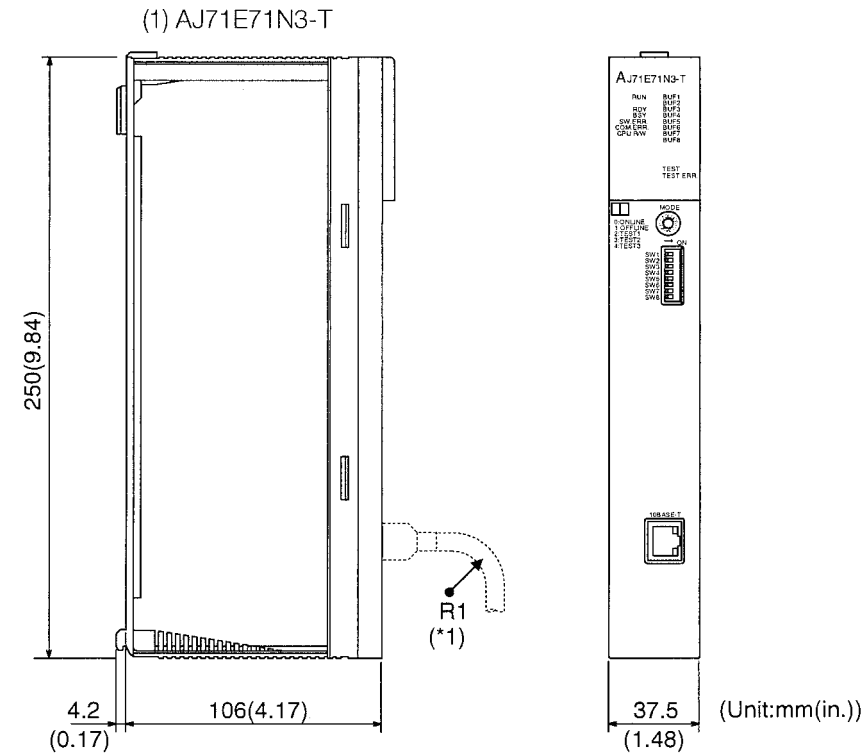
For details regarding TCP/IP refer to the DDN Protocol Handbook (3 volumes).
Publisher

DDN Network Information Center
SRI International
333 Ravenswood Avenue, EJ291
Menlo Park, California 94025

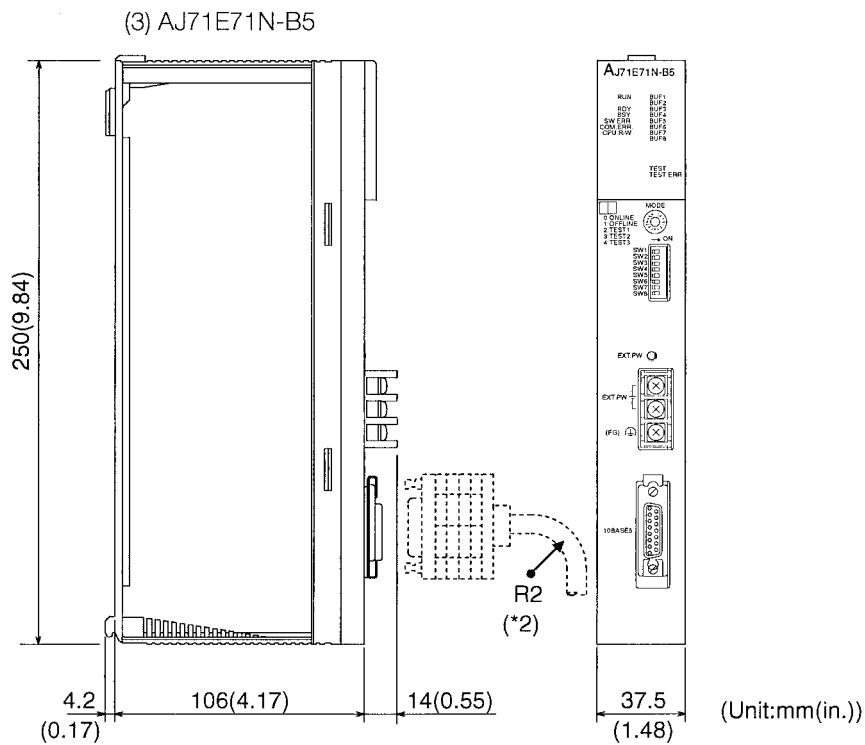
RFC No.

TCP RFC793
UDP RFC768
IP RFC791
ICMP RFC792
ARP RFC826

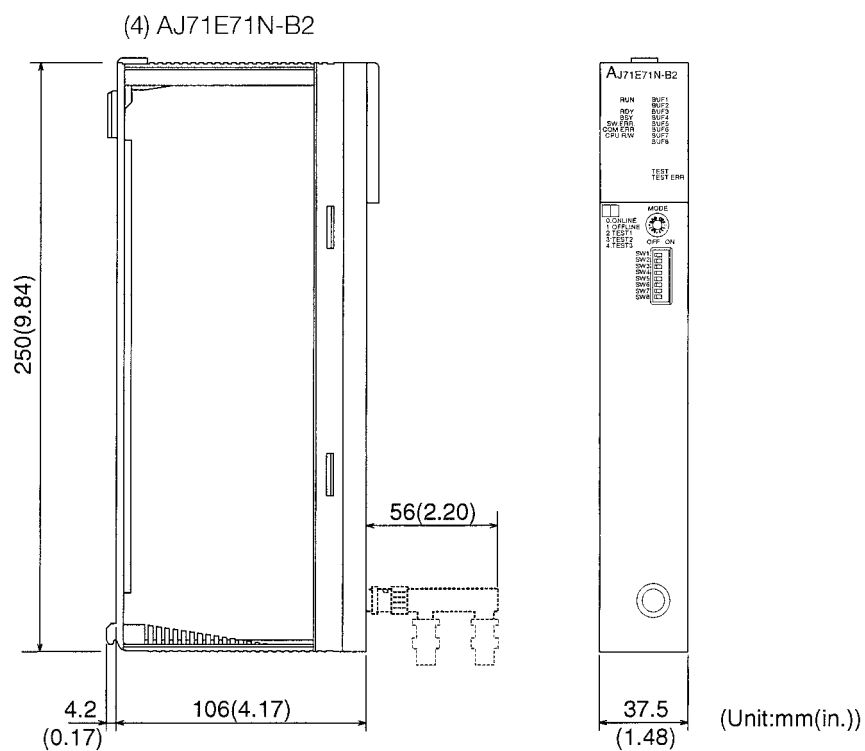
Appendix 6 Diagram of External Dimensions

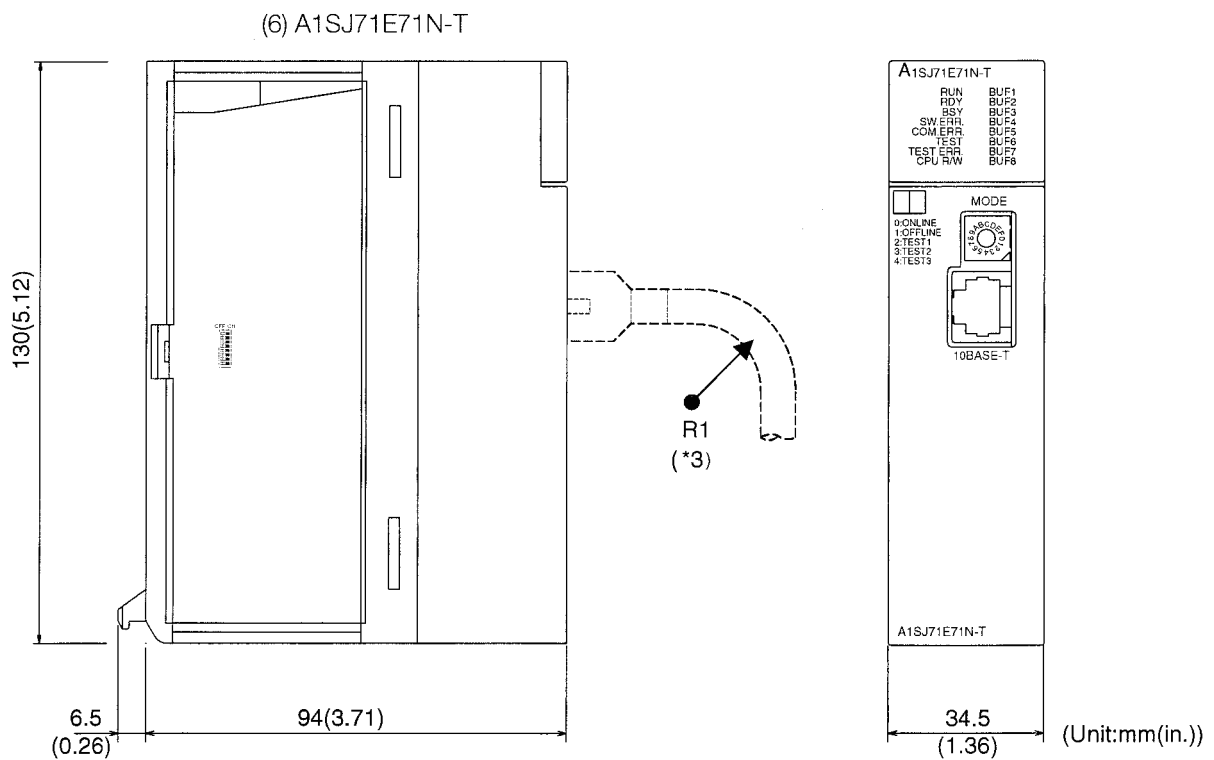
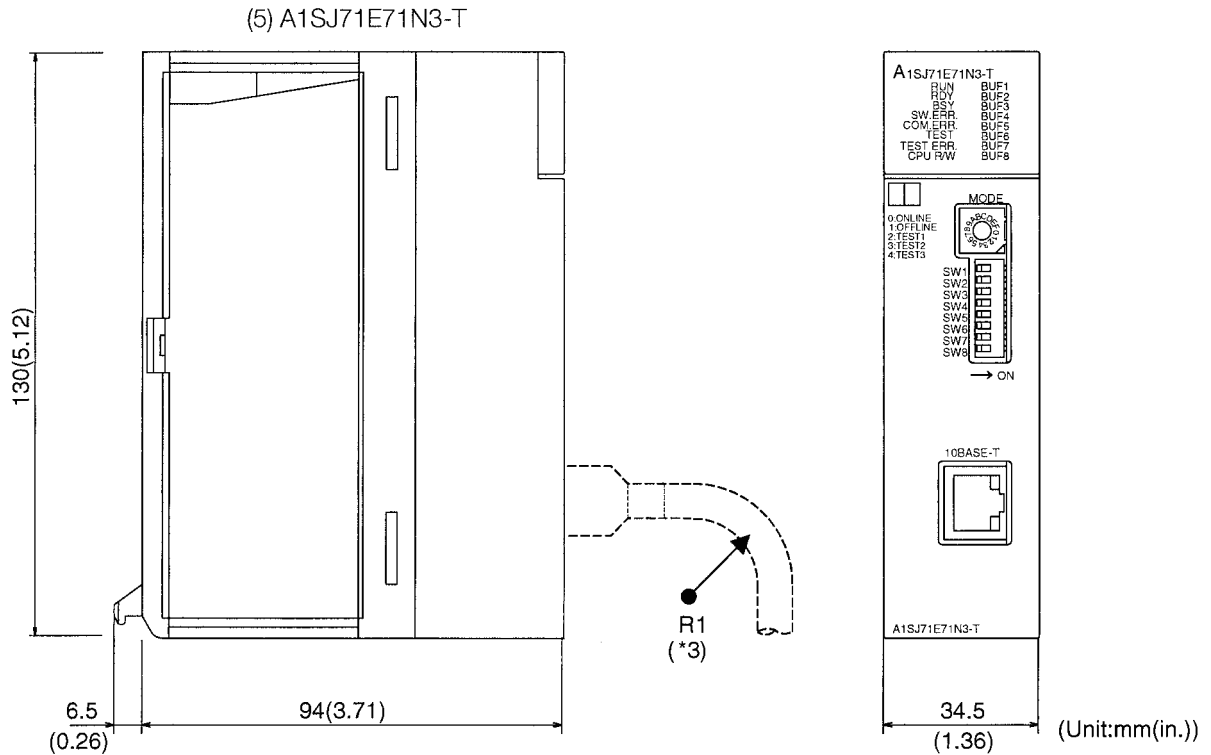


*1 When connecting the twisted pair cable, set the bending radius near the connector (reference value: R1) as four times the cable's outside diameter or larger.



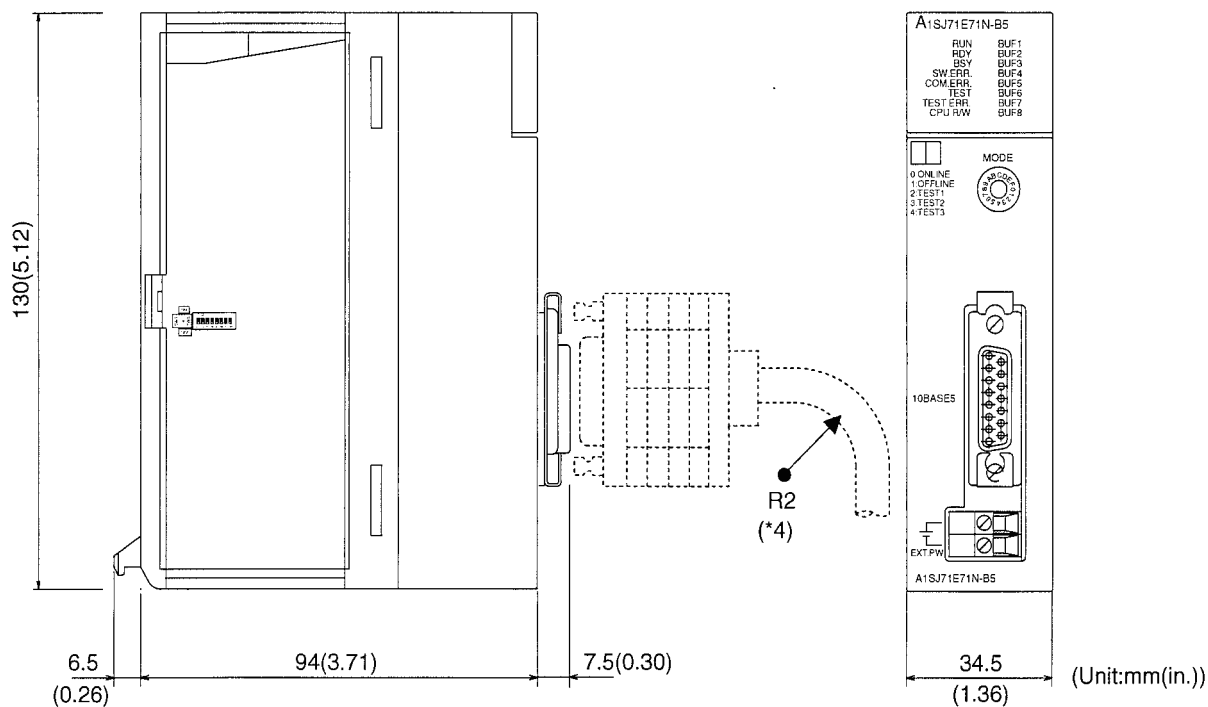
*2 When connecting the AUI cable, set the bending radius near the connector (reference value: R2) as four times the cable's outside diameter or larger.





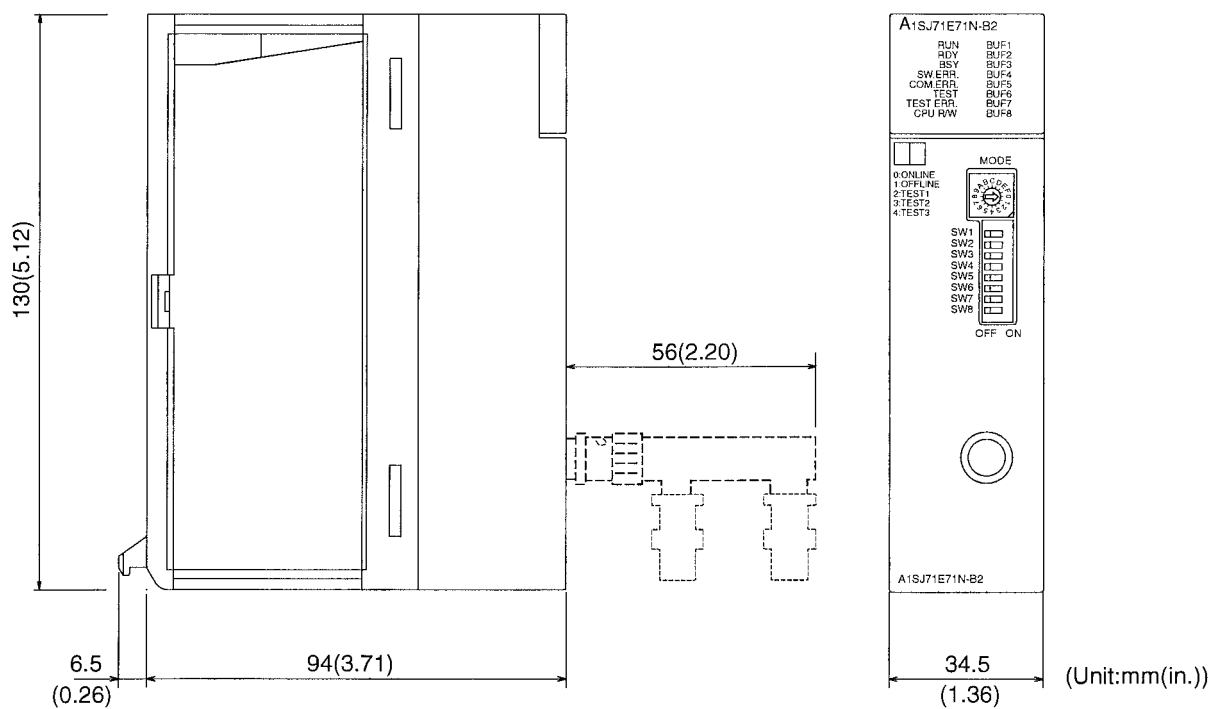
*3 When connecting the twisted pair cable, set the bending radius near the connector (reference value: R1) as four times the cable's outside diameter or larger.

(7) A1SJ71E71N-B5



*4 When connecting the AUI cable, set the bending radius near the connector (reference value: R2) as four times the cable's outside diameter or larger.

(8) A1SJ71E71N-B2



Appendix 7 Sample Program

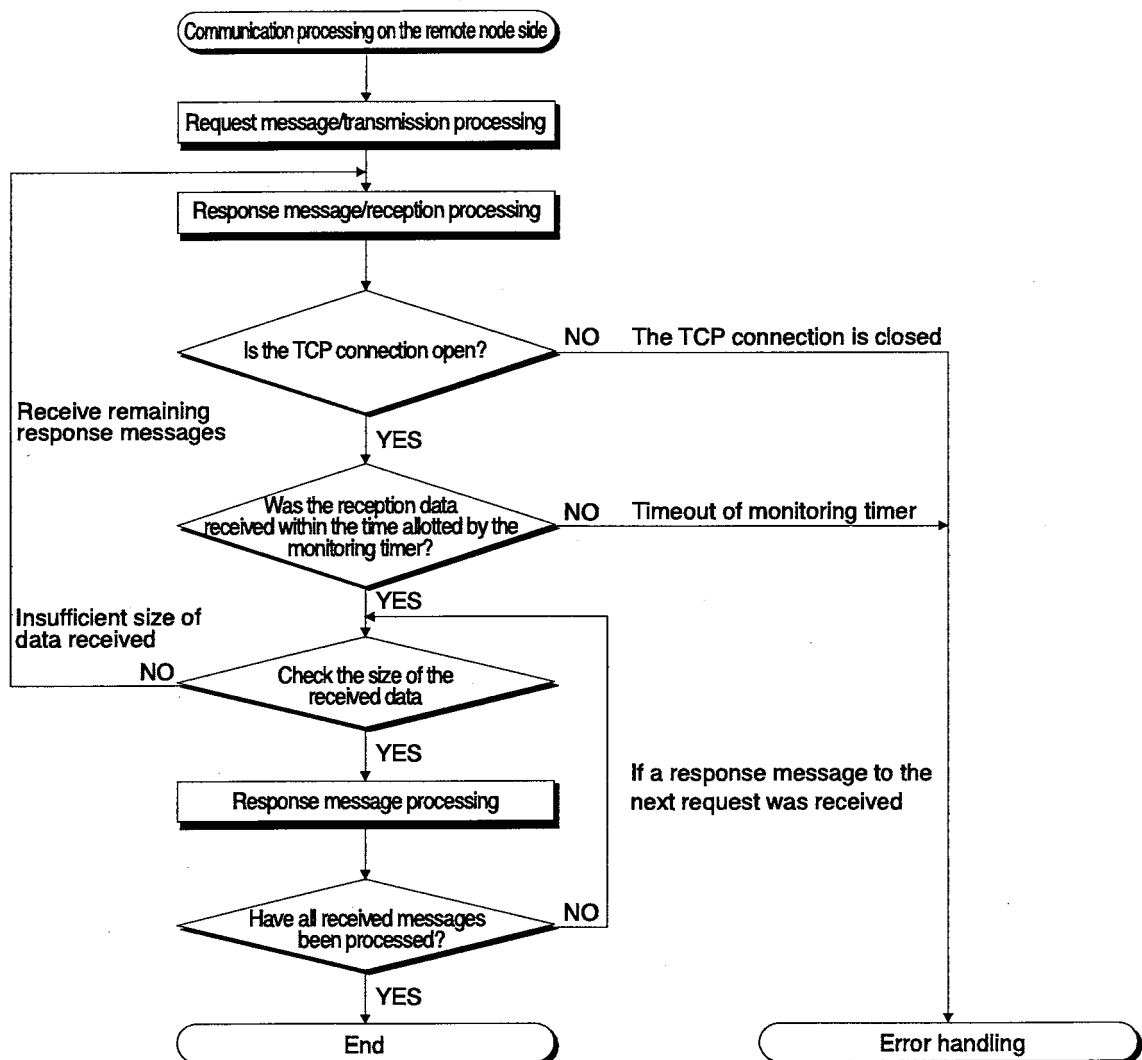
This shows the sample program between the PLC CPU of the station installed in the E71 and the remote node in order to conduct a connection test between the E71 and a remote node (IBM-PC/AT) connected to the same Ethernet.

The programs only conduct the minimum processing required to conduct the exchange test. Change the IP address and the port No. to match those of the system. In addition, make separate additions when error processing, etc., is included.

- PLC CPU side : Make additions as explained in Chapters 5 and 13 of this manual.
- IBM-PC/AT side : Make additions to match the system specifications.

1 Reception processing of remote node

An example of reception processing on the remote node side is shown below.



Background

For Ethernet communications, the TCP socket functions are used inside the personal computer. However, these functions do not have any limits. Therefore, when the "send" function is executed once to transmit data, the receiving end (node) needs to execute the "recv" function once or more in order to read the data ("send" and "recv" is not proportional to 1:1 execution). For this reason, the receiving procedure explained above, is required.

2**When the reception processing of the remote node is incompatible**

When the reception processing of the remote node is not as described in above **1**, communication under the "Enable TCP Maximum Segment transmission" setting may cause the following:

- Normal data cannot be read if batch read is executed in read/write of PLC CPU data from the remote node.
- Normal data cannot be read after the E71 that does not support TCP Maximum Segment transmission is replaced with a module that support it.
- Reception is disabled although the total number of received TCP packets (address: 190H) in the buffer memory is updated.

In this case, change the setting to "Disable TCP Maximum Segment transmission".

Appendix 7.1 Program for Reading/Writing Data in the PLC CPU

The sample program or execution environment and data exchange contents are shown below.

1

Sample program execution environment

(a) PLC CPU side (*1)

- | | |
|--|--|
| ① PLC CPU model name of the station installed in the E71 | : A3UCPU |
| ② E71 I/O signal | : X/Y000 to X/Y01F |
| ③ Ethernet address | : Setting not required because this is an ARP function |
| ④ E71 IP address | : C0.00.01.FD _H (192.00.01.253) |
| ⑤ E71 port No. | : 2000 _H (8192) |

(b) Remote node (IBM-PC/AT side)

- | | |
|------------------------------------|--|
| ① Operation environment | : Microsoft® Windows®95 Operating System |
| ② Ethernet interface board name | : Board that supports WINSOCK |
| ③ Library | : WSOCK32.LIB |
| ④ Software development environment | : Uses Microsoft® Corporation's Visual C++ (Ver.4.0) |
| ⑤ Ethernet address | : Setting not required because this is an ARP function |
| ⑥ IP address | : Reception when opening Active |
| ⑦ Port No. | : Reception when opening Active |

- | | |
|----------------------------|----------|
| (c) Communication protocol | : TCP/IP |
|----------------------------|----------|

2

Sampling program overview

(a) PLC CPU side PLC program

Only conducts initial processing and open processing.

(b) Remote node (IBM-PC/AT) side program

The above library is used to conduct exchange for reading/writing data in the following PLC CPU.

- Word unit write (D0 to D4 5 points) : Refer to Item 10.2.5
- Word unit read (D0 to D4 5 points) : Refer to Item 10.2.3

(c) When exchanging ASCII code data is exchanged.

3

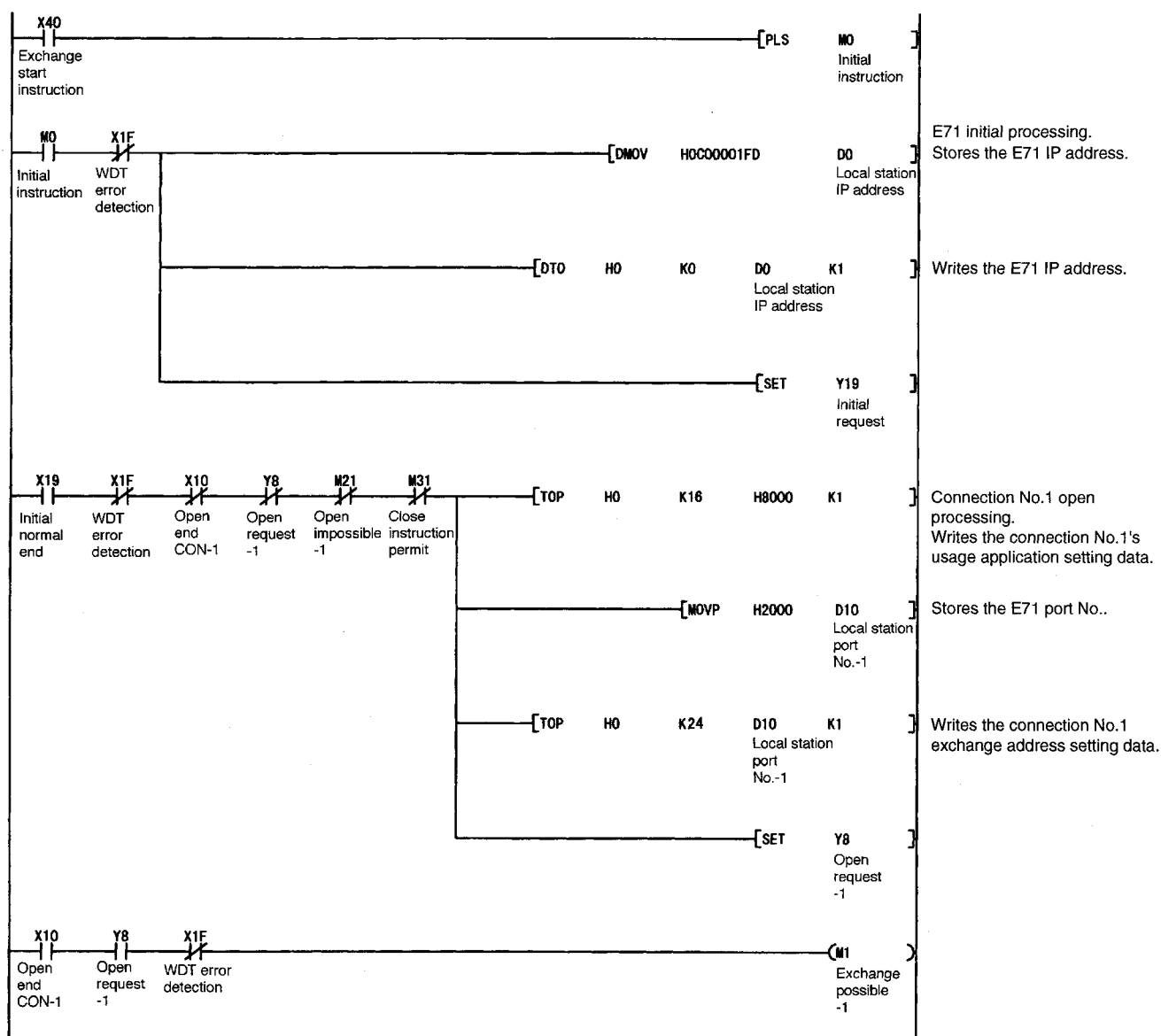
Sequence program

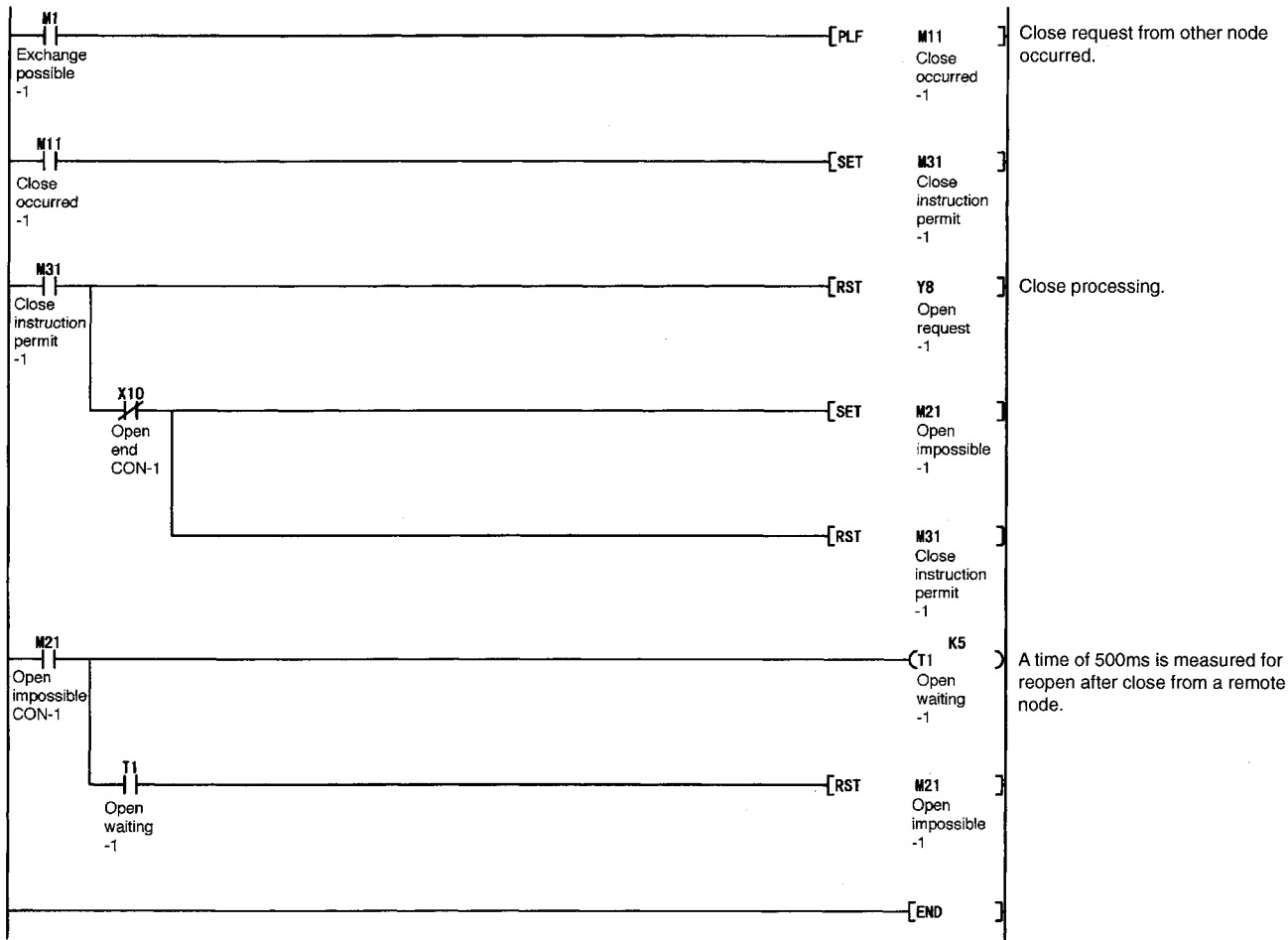
Following is an example of an exchange partner E71 installed station A3UCPU sequence program.

(*1) The E71 switch settings are as follows.

Setting switch			Set value	Setting description
Operation mode setting switch			0	Online
Exchange condition setting switches	SW1	Line processing selection during TCP timeout error	OFF	Closes
	SW2	Data code setting	ON	ASCII code
	SW7	CPU exchange timing setting	ON	Write enable
	SW8	Initial timing setting	OFF	Quick start

(Set all switches SW3 to SW6 (use not possible) to off.)





4

Remote node (IBM-PC/AT) side program

Following is an example remote node program for accessing an A3UCPU while the station installed in the E71.

Executing this program displays in order the following exchange message contents.

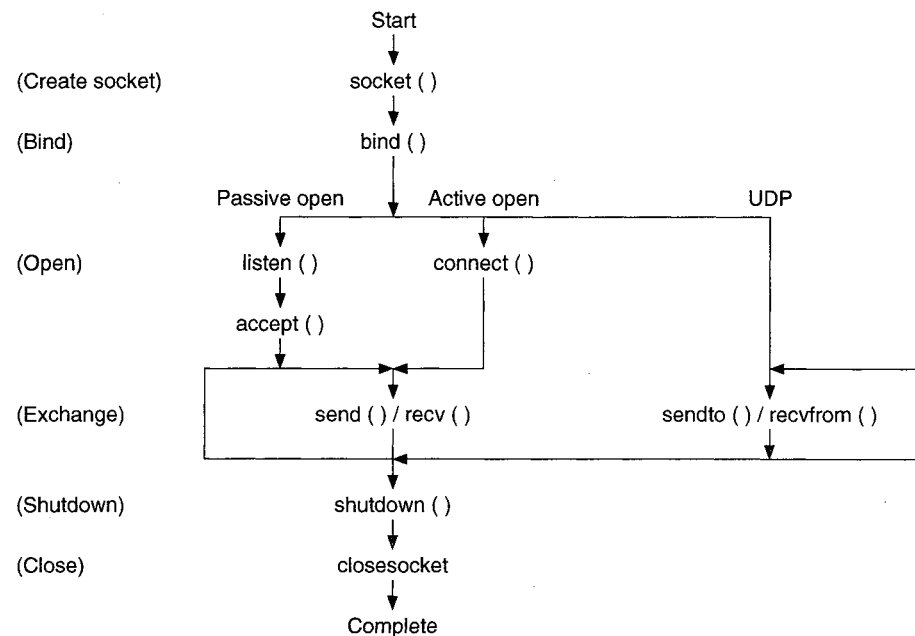
- ① Word unit batch write command message
- ② Word unit batch write response message
- ③ Word unit batch read command message
- ④ Word unit batch read response message

Remarks

(1) Following is a summary of the compiling procedure for the program created using Microsoft® Corporation's Visual C++ (Ver. 4.0).

- ① Boot up Visual C++.
- ② Conduct program creation preparation.
Select file to new file and create a console application from the project work space screen.
- ③ Open the AJSAMP.C file and create a program. (Refer to the example program on the next page.)
- ④ Execute compiling for the program created from the build menu compile screen.
- ⑤ Link the WSOCK.LIB from the build menu setting screen.
- ⑥ Create an execute file (AJSAMP.EXE) at the build menu build screen.
- ⑦ End Visual C++.
- ⑧ Execute AJSAMP.EXE.

(2) General procedure for socket routine call




```

/*****
/**
/**      sample program
/**
/**      This program is a sample program to conduct a
/**      connection test between the E71 and remote node.
/**      This program accesses the data register (D) of
/**      the PLC CPU installed together with the E71.
/**
/**
/**      Copyright (C) 1996 Mitsubishi Electric
/**      Corporation
/**      All Rights Reserved
/**
/**
/*****/

#include <stdio.h>
#include <winsock.h>

#define FLAG_OFF          0      // Completion flag OFF
#define FLAG_ON           1      // Completion flag ON
#define SOCK_OK           0      // Normal completion
#define SOCK_NG           -1     // Abnormal completion
#define BUF_SIZE          4096   // Receive buffer size

#define ERROR_INITIAL     0      // Initial error
#define ERROR_SOCKET      1      // Socket creation error
#define ERROR_BIND        2      // Bind error
#define ERROR_CONNECT     3      // Connection error
#define ERROR_SEND        4      // Send error
#define ERROR_RECEIVE     5      // Receive error
#define ERROR_SHUTDOWN    6      // Shutdown error
#define ERROR_CLOSE       7      // Line close error

//Definitions for checking the receiving sizes
#define RECV_ANS_1  4  // Receiving size of response message in reply to device write (1E frame)
//#define RECV_ANS_1 22 // Receiving size of response message in reply to device write (3E frame)
#define RECV_ANS_2  24 // Receiving size of response message in reply to device read (1E frame)
//#define RECV_ANS_2 42 // Receiving size of response message in reply to device read (3E frame)

typedef struct sck_inf{
    struct in_addr my_addr;
    unsigned short my_port;
    struct in_addr aj_addr;
    unsigned short aj_port;
};

```

```

int nErrorStatus;           // Error information storage variable
int Dmykeyin;               // Dummy key input
int Closeflag;              // Connection completion flag
int socketno;

int main()
{
    WORD wVersionRequested=MAKEWORD(1,1);    // Winsock Ver 1.1 request
    WSADATA wsaData;
    int length;                             // Communication data length
    unsigned char s_buf[BUF_SIZE];           // Send buffer
    unsigned char r_buf[BUF_SIZE];           // Receive buffer
    int rbuf_idx;                            // Receive data storage head index
    int recv_size;                            // Number of receive data
    struct sock_inf sc;
    struct sockaddr_in hostdata;              // Remote node side data
    struct sockaddr_in aj71e71;              // E71 side data
    void Sockerror(int);                     // Error handling function

    unsigned long ulCmdArg ;                 // Non-blocking mode setting flag

    sc.my_addr.s_addr=htonl(INADDR_ANY);     // Remote node side IP address
    sc.my_port=htons(0);                     // Remote node side port number
    sc.aj_addr.s_addr=inet_addr("192.0.1.253"); // E71 side IP address
                                                // (C00001FDH)
    sc.aj_port=htons(0x2000);                // E71 side port number

    Closeflag=FLAG_OFF;                      // Connection completion flag off

    nErrorStatus=WSAStartup(wVersionRequested,&wsaData); // Winsock Initial processing

    if(nErrorStatus!=SOCK_OK) {
        Sockerror(ERROR_INITIAL);            // Error handling
        return(SOCK_NG);
    }

    printf("Winsock Version is %ld.%ld\n",HIBYTE(wsaData.wVersion),LOBYTE(wsaData.wVersion));
    printf("AJ_test Start\n");

    socketno=socket(AF_INET,SOCK_STREAM,0);   // Create socket for TCP/IP

    if(socketno==INVALID_SOCKET){
        Sockerror(ERROR_SOCKET);             // Error handling
        return(SOCK_NG);
    }

    hostdata.sin_family=AF_INET;
    hostdata.sin_port=sc.my_port;
    hostdata.sin_addr.s_addr=sc.my_addr.s_addr;

```

```

if(bind(socketno, (LPSOCKADDR)&hostdata, sizeof(hostdata))!=SOCK_OK){
    // Bind
    Sockerror(ERROR_BIND); // Error handling
    return(SOCK_NG);
}

aj71e71.sin_family=AF_INET;
aj71e71.sin_port=sc.aj_port;
aj71e71.sin_addr.s_addr=sc.aj_addr.s_addr;

if(connect(socketno, (LPSOCKADDR)&aj71e71, sizeof(aj71e71))!=SOCK_OK){
    // Connection (Active open)
    Sockerror(ERROR_CONNECT); // Error handling
    return(SOCK_NG);
}

Closeflag=FLAG_ON; // Connection completion flag ON

// Set to non-blocking mode
ulCmdArg = 1;
ioctlsocket(socketno, FIONBIO, &ulCmdArg); // Set to non-blocking mode

strcpy(s_buf, "03FF000A4420000000000500112233445566778899AA");
// D0 to D4 batch write request (1E frame)
// strcpy(s_buf, "500000FF03FF00002C000A14010000D*0000000005112233445566778899AA");
// D0 to D4 batch write request (3E frame)

length=strlen(s_buf);

if(send(socketno, s_buf, length, 0)==SOCKET_ERROR){ // Data sending
    Sockerror(ERROR_SEND); // Error handling
    return (SOCK_NG);
}
printf("\n send data\n%s\n", s_buf);

// Perform receiving size check and receiving processing simultaneously
rbuf_idx = 0; // Receive data storage head index initialization
recv_size = 0; // Initialize the number of receive data
while(1) {
    length = recv(socketno, &r_buf[rbuf_idx], (BUF_SIZE - rbuf_idx), 0);
    // Response data receiving
    if(length == 0) {
        // Is connection cut off?
        Sockerror(ERROR_RECIEVE); // Error handling
        return (SOCK_NG);
    }
}

```

```

        if(length == SOCKET_ERROR) {
            nErrorStatus = WSAGetLastError();
            if(nErrorStatus != WSAEWOULDBLOCK) {
                Sockerror(ERROR_RECIEVE); // Error handling
                return (SOCK_NG);
            } else {
                continue; // Repeat until messages are received
            }
        } else {
            rbuf_idx += length; // Update the receive data storage
                                // position
            recv_size += length; // Update the number of receive data
            if(recv_size >= RECV_ANS_1) // Have all response messages been
                                // received?
                break; // Stop repeating as messages have
                                // been received
        }
    }
    r_buf[rbuf_idx] = '\0' ; // Set NULL at the end of receive data

    printf("\n receive data\n%s\n",r_buf);

    strcpy(s_buf, "01FF000A4420000000000500"); // D0 to D4 batch read request
                                                // (1E frame)
//    strcpy(s_buf, "500000FF03FF000018000A04010000D*0000000005");
                                                // D0 to D4 batch read request
                                                // (3E frame)

    length=strlen(s_buf);

    if(send(socketno,s_buf,length,0)==SOCKET_ERROR){ // Data sending
        Sockerror(ERROR_SEND); // Error handling
        return (SOCK_NG);
    }
    printf("\n send data\n%s\n",s_buf);

    // Perform receiving size check and receiving processing simultaneously
    rbuf_idx = 0; // Receive data storage head index
                 // initialization
    recv_size = 0; // Initialize the number of receive data
    while(1) {
        length = recv(socketno, &r_buf[rbuf_idx], (BUF_SIZE - rbuf_idx), 0);
                                                // Response data receiving
        if(length == 0) { // Is connection cut off?
            Sockerror(ERROR_RECIEVE); // Error handling
            return (SOCK_NG);
        }
    }

```

```

        if(length == SOCKET_ERROR) {
            nErrorStatus = WSAGetLastError();
            if(nErrorStatus != WSAEWOULDBLOCK) {
                Sockerror(ERROR_RECIEVE);          // Error handling
                return (SOCK_NG);
            } else {
                continue;                          // Repeat until messages are received
            }
        } else {
            rbuf_idx += length;                    // Update the receive data storage
                                                // position
            recv_size += length;                  // Update the number of receive data
            if(recv_size >= RECV_ANS_2)           // Have all response messages been
                                                // received?
                break;                             // Stop repeating as messages have
                                                // been received
        }
    }
    r_buf[rbuf_idx] = '\0' ;                      // Set NULL at the end of receive data

    printf("\receive data\n%s\n", r_buf);

    if(shutdown(socketno,2)!=SOCK_OK){            // Processing to disable
                                                // sending/receiving
        Sockerror(ERROR_SHUTDOWN);               // Error handling
        return(SOCK_NG);
    }

    if(closesocket(socketno)!=SOCK_OK){           // Close processing
        Sockerror(ERROR_CLOSE);                  // Error handling
        return(SOCK_NG);
    }

    Closeflag=FLAG_OFF;                          // Connection completion flag off
    WSACleanup();                                // Release Winsock.DLL

    printf("\nAJ_test End.\n\n Normally completed. \n");
    printf("Press any key to exit the program.\n");
    Dmykeyin=getchar();                          // Wait for key input

    return(SOCK_OK);
}

void Sockerror(int error_kind)                    // Error handling function
{
    if(error_kind==ERROR_INITIAL){
        printf("Initial processing is abnormal.");
    }
}

```

```
else{
    nErrorStatus=WSAGetLastError();
    switch(error_kind){
    case ERROR_SOCKET:
        printf("Failed to create socket.");
        break;
    case ERROR_BIND:
        printf("Failed to bind.");
        break;
    case ERROR_CONNECT:
        printf("Failed to establish connection.");
        break;
    case ERROR_SEND:
        printf("Sending failed.");
        break;
    case ERROR_RECV:
        printf("Receiving failed.");
        break;
    case ERROR_SHUTDOWN:
        printf("Failed to shutdown.");
        break;
    case ERROR_CLOSE:
        printf("Failed to close normally.");
        break;
    }
}

printf("Error code is %d.\n", nErrorStatus);

if(Closeflag==FLAG_ON){
    nErrorStatus=shutdown(socketno,2);           // Shutdown processing
    nErrorStatus=closesocket(socketno);          // Close processing
    Closeflag=FLAG_OFF;                          // Connection completion flag off
}

printf("Press any key to exit the program.\n");
Dmykeyin=getchar();                             // Wait for a key input
WSACleanup();                                   // Release Winsock.DLL
return;
```

Appendix 7.2 Sequence Programs for All Functions

An example of a common sequence program for conducting exchange (with procedure) using a fixed buffer memory, exchange using a random access buffer memory, and data read/write in the PLC CPU is shown below.

1 Sample program execution environment

The execution environment for the PLC CPU side setting value and switch setting, etc., is the same as the execution environment shown in Appendix 7.1 Item 1(a).

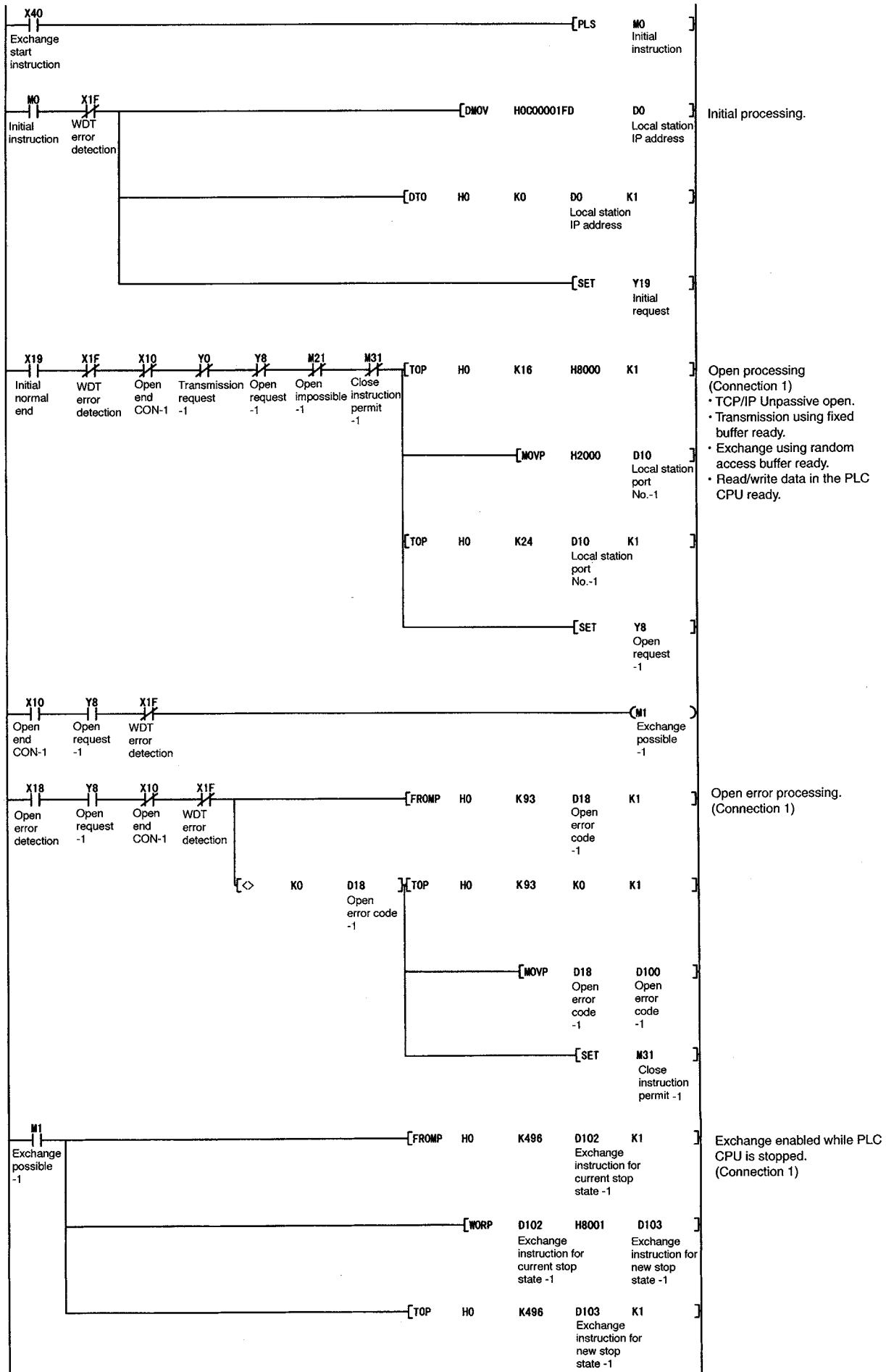
In addition, the port No. 2001H(8193) is used.

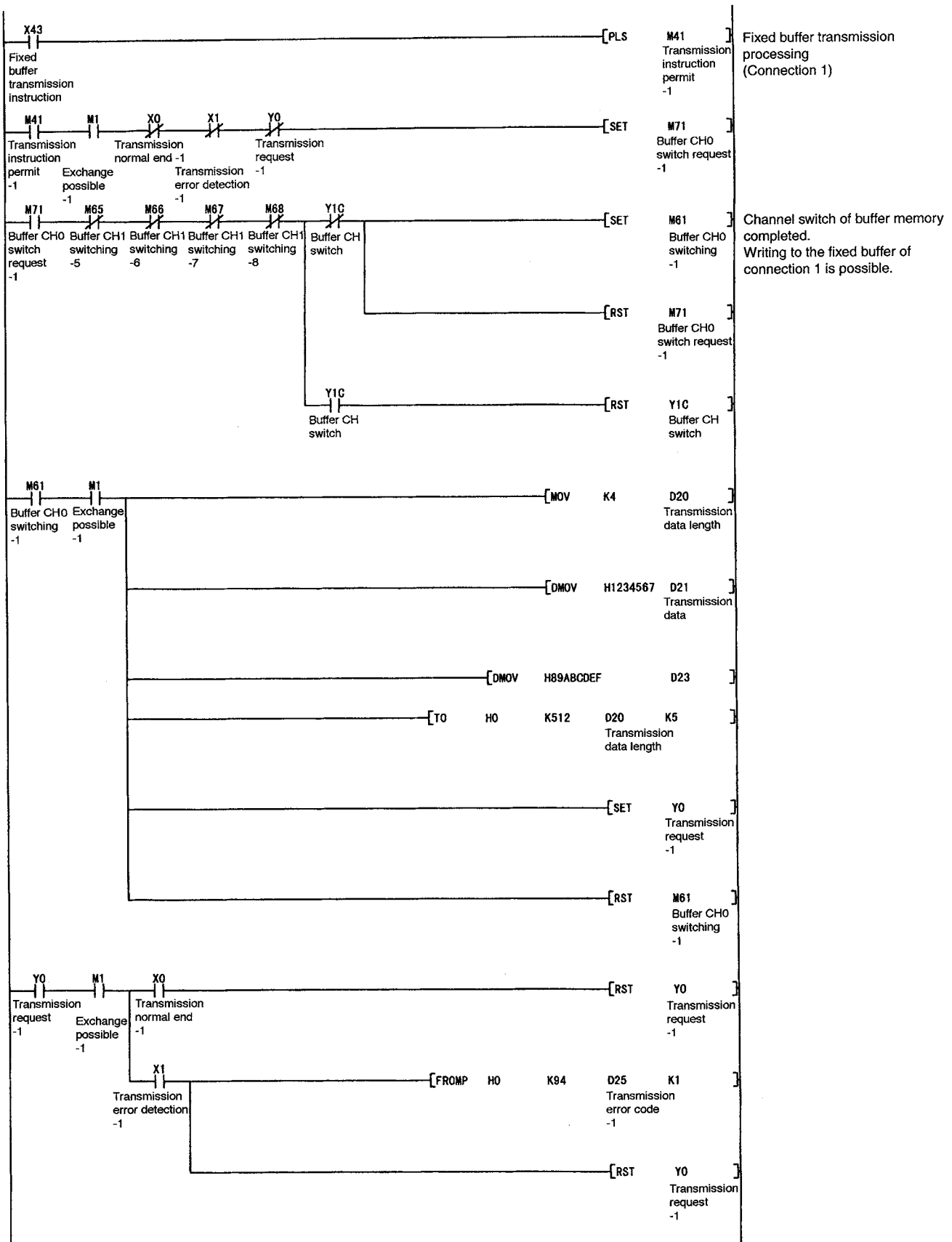
Freely set the IP Address on the remote node side. Use the same No. for the port No. as for the PLC CPU side.

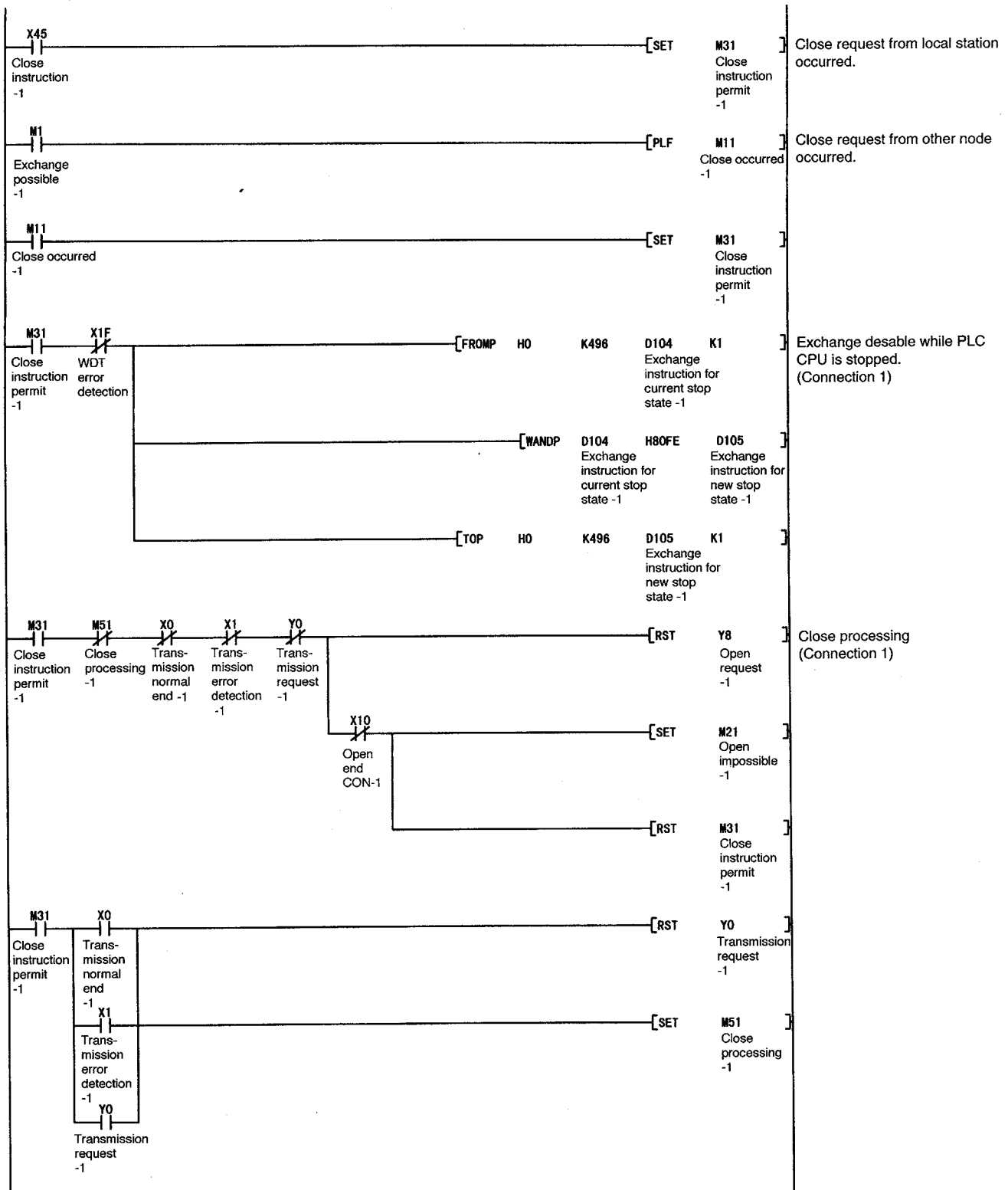
2 Sample program overview

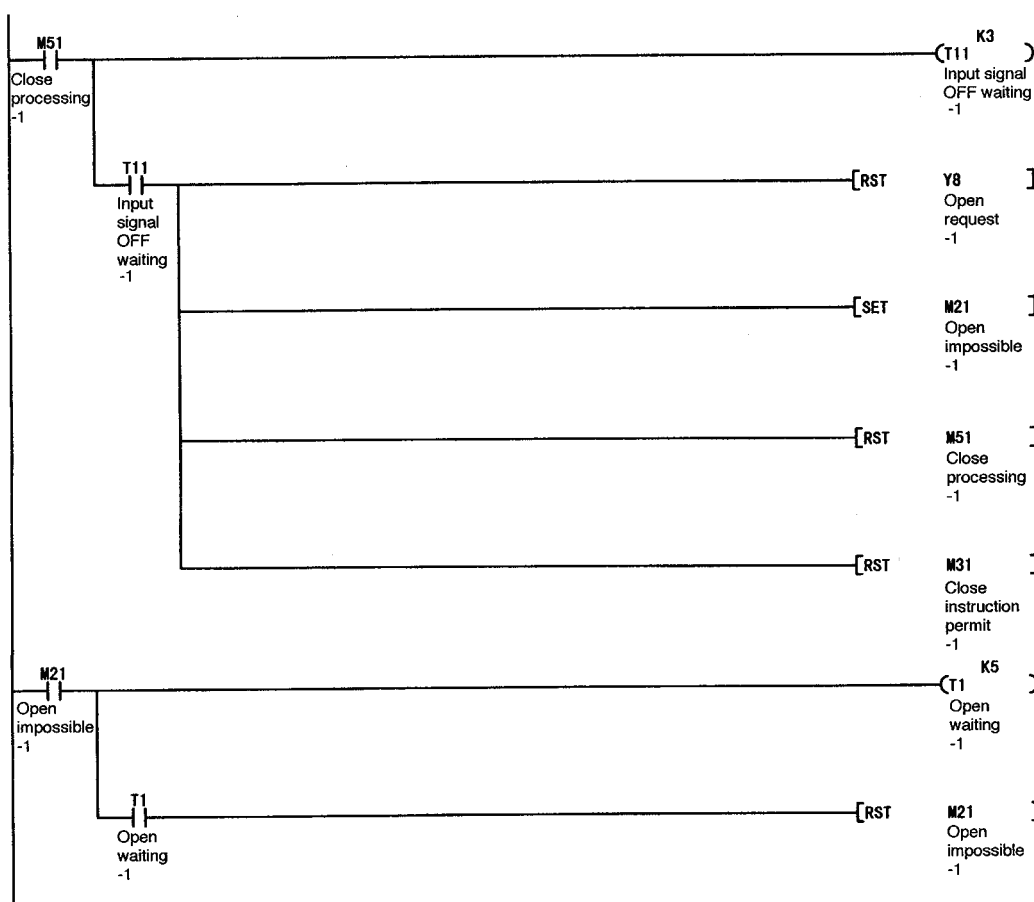
- ① Conducts initial processing.
- ② Conducts open processing. (Exchange while the PLC CPU is stopped is made possible.)
Two open processes are conducted to make possible exchange (transmission, reception) using a fixed buffer. In either case an Unpassive open is conducted and an Active open request from a remote node is awaited.
- ③ Exchange (transmission with procedure) using a fixed buffer, exchange using a random access buffer, and read/write data in the PLC CPU are conducted from the remote node. In addition, when not exchanging with the remote node, exchange (transmission with procedure) using a fixed buffer is conducted from the PLC CPU.
- ④ After data exchange is terminated, exchange will be prohibited while the PLC CPU is stopped and close processing will be conducted.
- ⑤ Termination processing is conducted after close processing is completed.

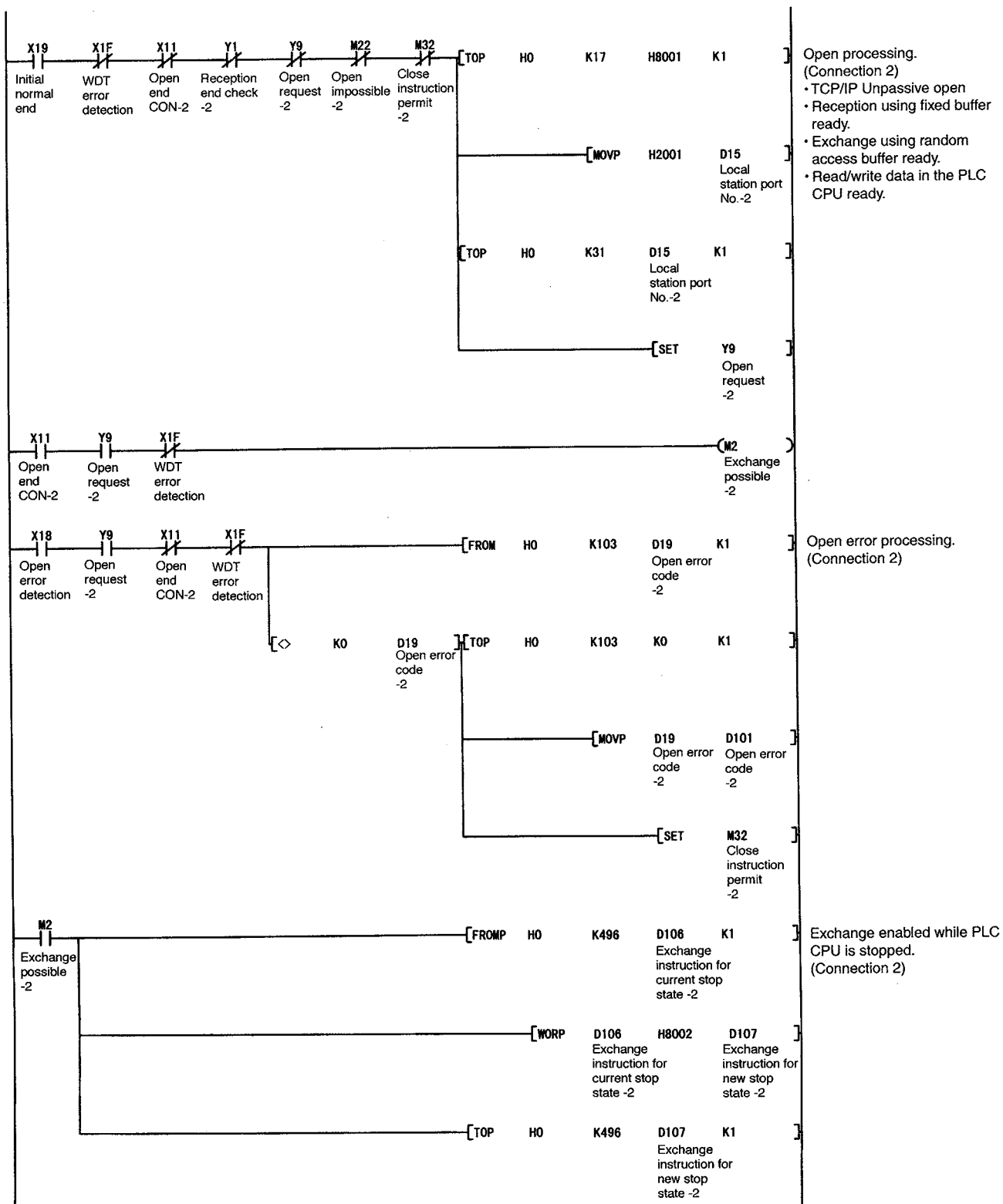
	Connection 1	Connection 2	Remark
E71 IP Address	C0. 00. 01. FD		Class= C, Network address= 1, Host address= FDH
E71 Port No.	2000H	2001H	Set the personal computer side to a free No.
Communication format	TCP/IP		Personal computer side is also TCP/IP
Open method	Unpassive	Unpassive	Personal computer side is Active open
Fixed buffer exchange	Transmission ready	Reception ready	---
Random access buffer exchange	Ready	Ready	Exchange is possible at either the connection 1 or connection 2.
Read/write data in the PLC CPU	Ready	Ready	

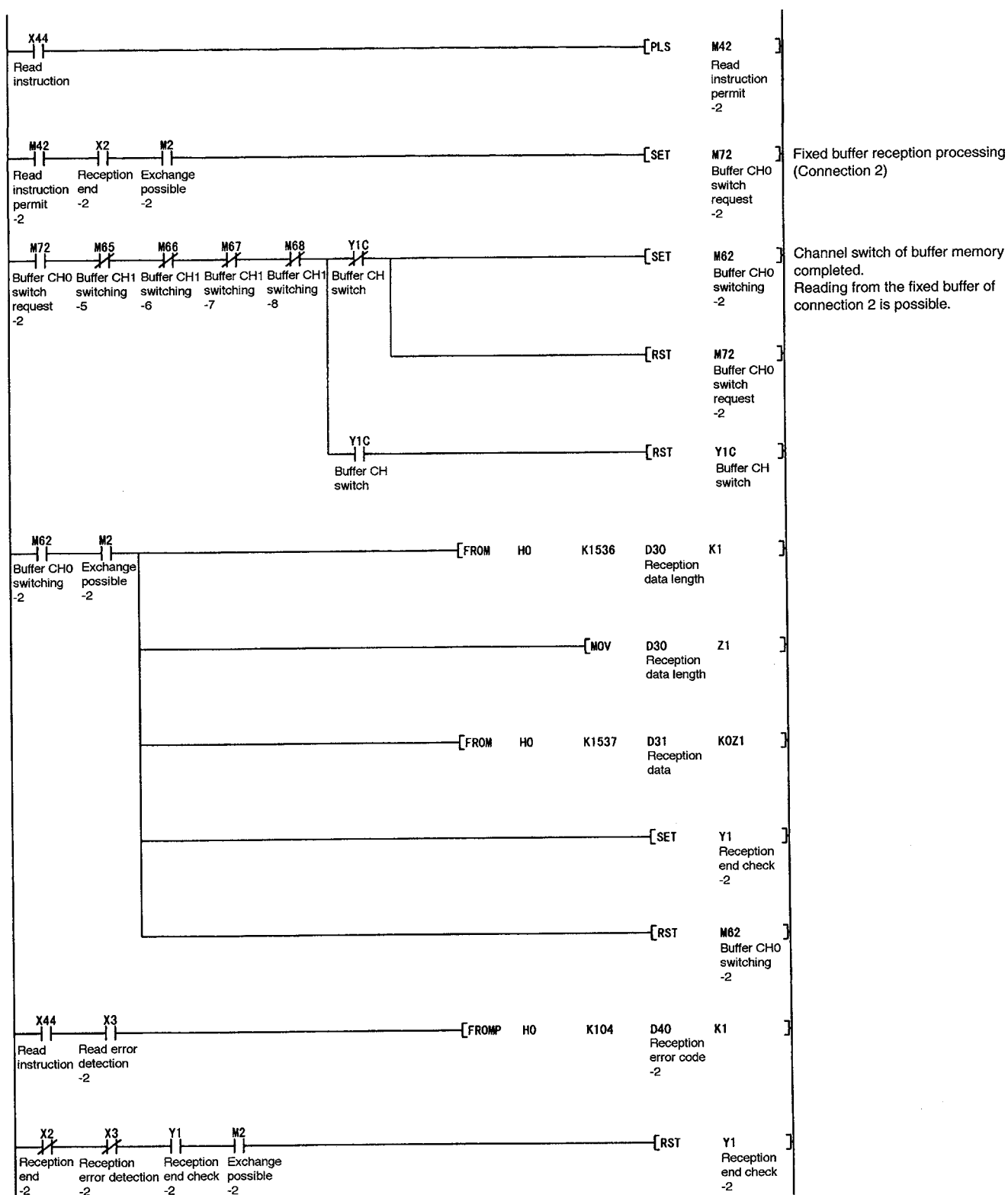


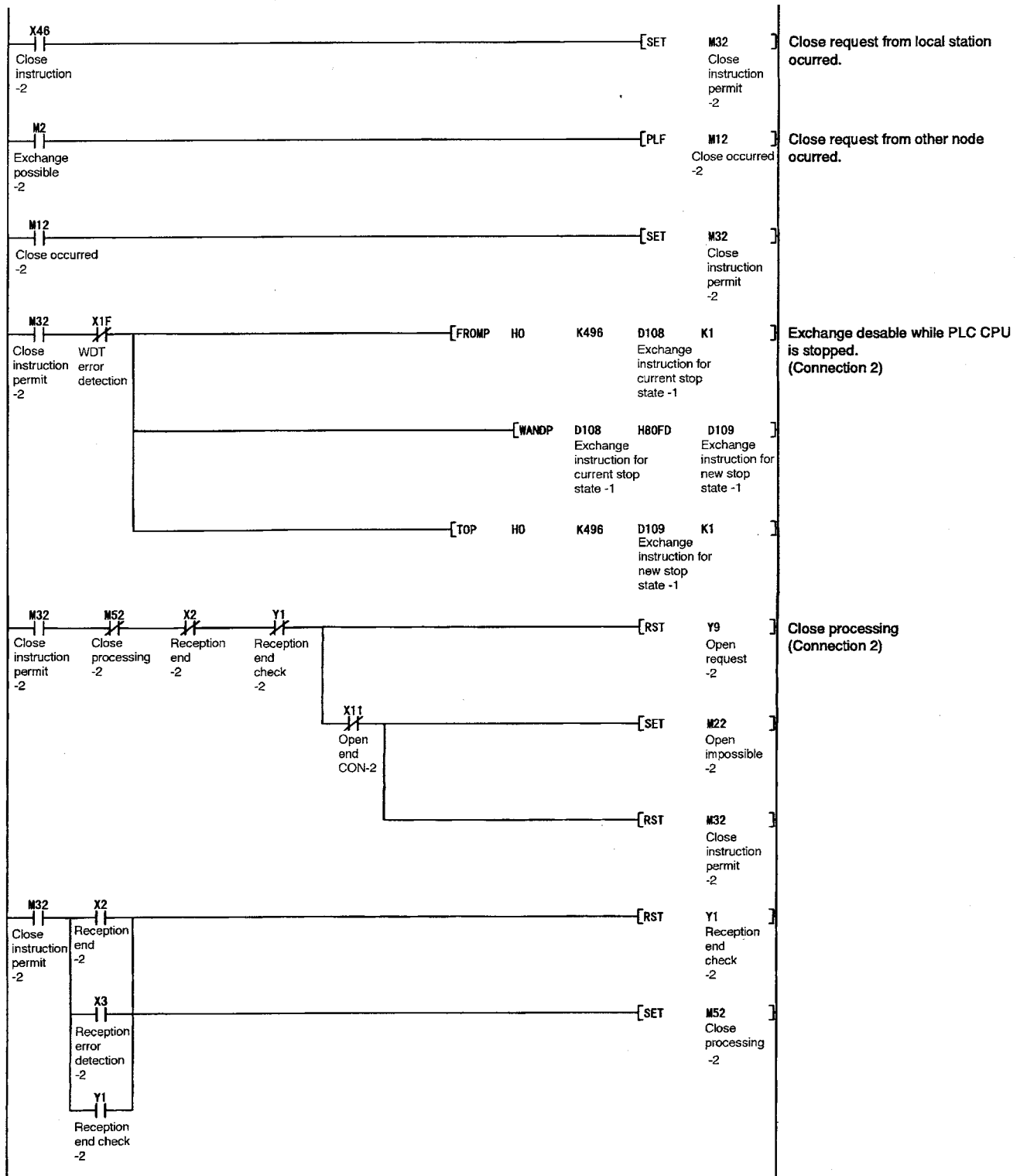


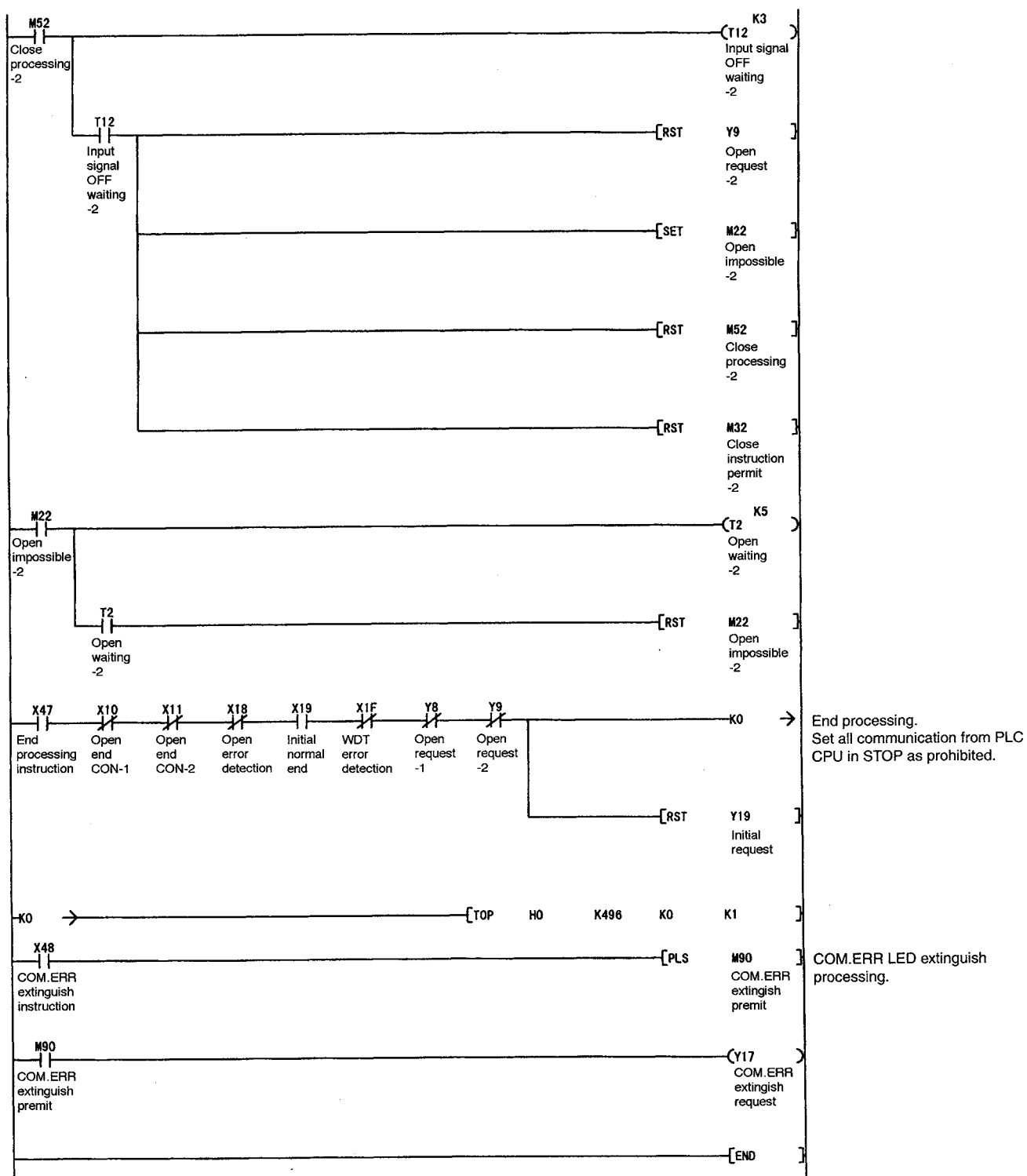












Appendix 7.3 Program for Reading/Writing Data in the PLC CPU

An example of the remote node side program that reads and writes data inside the PLC CPU is shown below. Also explained is the sample program, execution environment, and data exchange.

1

Sample program execution environment

- ① The setting values and the switch settings on the PLC CPU side are the same as for the execution environment shown in Appendix 7.1 Item 1(a).
- ② Except for the following software development environment the node side has the same execution environment as that described in Appendix 7.1 Item 1(b).
 - Software development environment : Microsoft® Corporation's visual basic (Ver.6.0) is used.
 - The IP Address and port No. are allocated free Nos.
- ③ The communication format is TCP/IP.

2

Overview of the sample program

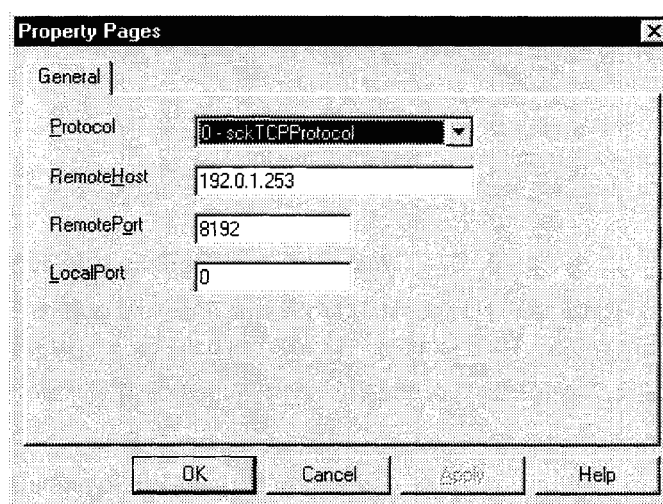
This program reads data from D0 through D4 (five points) of the ACPU of the station on which the E71 is mounted with the E71 command (01: batch read in word units).

Note that the program shown in Appendix 7.2 is used as a program on the PLC CPU side.

3

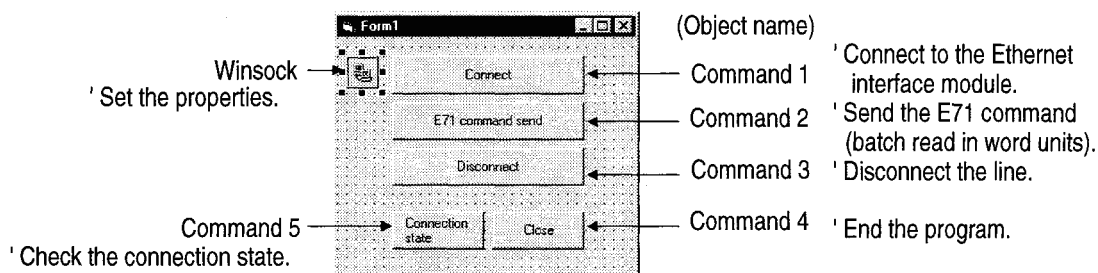
Outline of the sample program

- (a) Create a new project and form.
- (b) Create the (example) window shown in (4) below using "Command Button" in the toolbox.
- (c) Add "Microsoft Winsock Control 6.0" using the component's control. Add a "Winsock" object from the toolbox to the form and set the Property Pages screen as follows:



- (d) Create the program shown in (5).

4 Window example (Form 1)



5 Sample program (Form 1)

Option Explicit

```
Private Sub Command1_Click()
'Connect to the Ethernet interface module
Me.Winsock1.Connect
```

End Sub

```
Private Sub Form_Load()
'When calling an Active open method from a PC to the Ethernet interface module,
'The property screen should be used or the settings performed as follows.
'Specify the protocol to be used.
'Winsock1.Protocol = sockTCPProtocol / sockUDPPProtocol
'Specify the IP address of the Ethernet interface module.
'Winsock1.RemoteHost = "192.0.1.253"
'Specify the port No. used by the Ethernet interface module.
'Winsock1.RemotePort =8192:H2000
'If the open method of the Ethernet interface module is Fullpassive open,
'specify the set port No.
'If the open method of the Ethernet interface module is Unpassive open,
'"0" - use any port No.
'Winsock1.LocalPort = 0 :Unpassive open
```

End Sub

```
Private Sub winsock1_connect()
'Use the Connect event to perform confirmation processing at the time when the connection
processing is normally completed.
'The Connect event occurs when the connection processing is completed.
MsgBox "Connection Completed"
```

End Sub

```
Private Sub Command2_Click()
Dim SData As String
'Read D0 to D4 (5 points) with the E71 command.
SData = "01ff000a4420000000000500"
'Send the data.
Me.Winsock1.SendData SData
```

End Sub

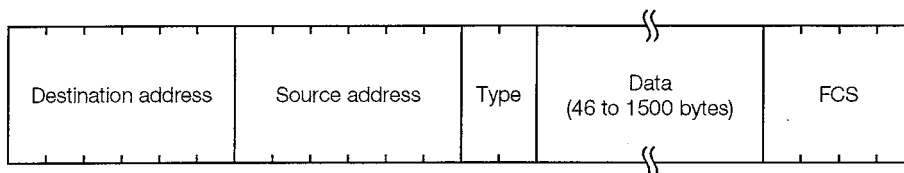
```
Private Sub Command3_Click()  
'Close the TCP connection socket (disconnect the line).  
Me.Winsock1.Close  
  
End Sub  
  
Private Sub Command4_Click()  
'End the program.  
End  
  
End Sub  
  
Private Sub Command5_Click()  
'Check the state of Winsock.  
'See the Help of Visual Basic for details.  
MsgBox Winsock1.State  
  
End Sub  
  
Private Sub Winsock1_DataArrival(ByVal bytesTotal As Long)  
'The DataArrival event occurs when new data arrives.  
  
Dim RData As String  
'Obtain the current data block and save it as a variant type variable.  
'Read the response from the PLC CPU.  
Me.Winsock1.GetData RData  
MsgBox RData  
  
End Sub
```

Appendix 8 Difference between Ethernet and IEEE802.3

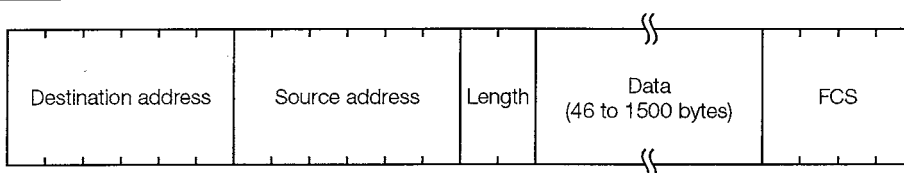
The Ethernet header for the data link layer supported by the E71 meets the specifications of the Ethernet frame.

The E71 does not communicate with another node whose Ethernet header for data link layer has the IEEE 802.3 (ISO/IEC 8802-3) specifications.

1 Ethernet



2 IEEE802.3



Remarks

The hardware specifications of the E71 are in accordance with the IEEE802.3.

Appendix 9 E71 Support's ICMP Protocol

Shows the ICMP types and E71 processing supported by the E71.

ICMPType	ICMP Name/Description	E71 Processing
0	Echo Reply IP packet echo results	The E71 transmits this message when it receives an Echo Request.
3	Destination Unreachable The IP packet could not reach the partner destination.	The E71 transmits this message when data is received by a UDP connection that has not been opened.
8	Echo Request IP packet echo request	The E71 transmits this message when conducting an existence check if the destination existence check is set in the buffer memory.(*1)
Other	—	Ignored by the E71. (Not yet supported)

*1 The E71 can receive 2 ICMP ECHO requests (type 8, PING Message) used for existence check, etc., at the same time and conducts the corresponding processing. When 3 or more ICMP ECHO requests are received at the same time the requests from the third and later are ignored. When an ICMP ECHO request is transmitted to the E71 from the remote node and a response is not returned to the remote node, retransmit the ICMP ECHO request to the E71.

The E71 is able to receive a maximum of 1460-byte ICMP message at one time. Do not send an ICMP message request exceeding 1460 bytes to the E71.

Appendix 10 Communication Support Tool (MX Component)

MX Component is an ActiveX control library that supports any types of communication paths between IBM PC/AT compatible personal computers and PLCs. It allows the users to establish communication using simple processing only, without having to know about the different communication protocols used in the individual communication.

It also supports various programming languages, allowing for a wide range of application development.

This section provides the overview of the functions provided by MX Component and explains the procedure up to creation of applications.

* Refer to Operating Manual and Programming Manual of MX Component for the details.

Appendix 10.1 Overview of MX Component

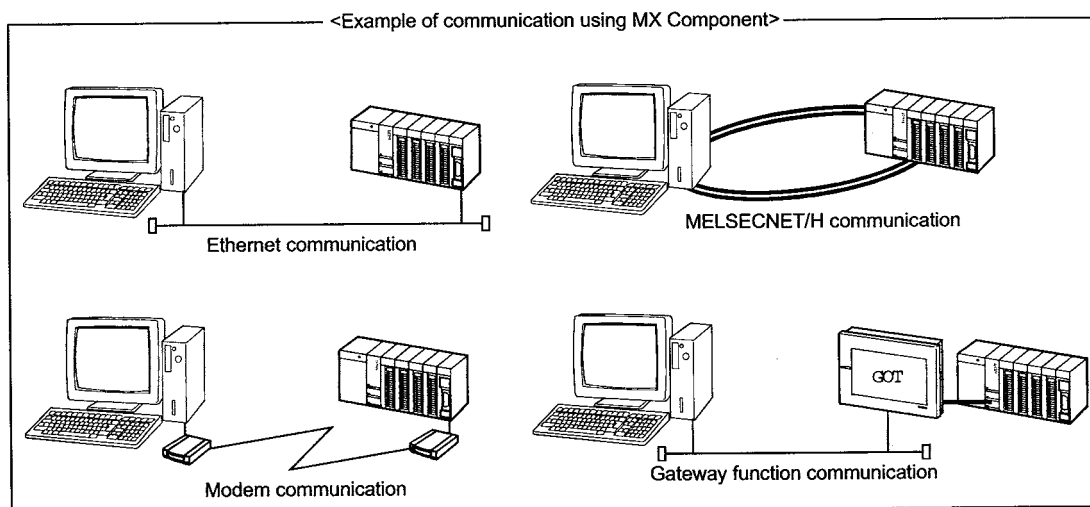
This section provides the overview of MX Component.

Different communication paths, operating systems, programming languages, and functions are supported depending on the version of MX Component used.

(1) Support for a wide range of communication paths to PLCs

MX Component supports a wide range of communication paths to PLCs.

It is possible to construct systems according to the needs of the users.



(2) Dramatic improvement of application development efficiency

MX Component provides wizard-style communication setting utilities.

The user only needs to select settings from menus displayed on the screen in an interactive manner to achieve the communication settings required to access the target PLC CPU.

Moreover, once the communication setting has been performed, it is possible to access the PLC CPU simply by specifying the logical station number that is stored via the communication setting utilities.

(3) Support for wide choice of basic operating systems

MX Component can run on IBM PC/AT compatible personal computers running the following basic operating systems:

- Microsoft ® Windows ® 95 Operating System
- Microsoft ® Windows ® 98 Operating System
- Microsoft ® Windows NT ® Workstation Operating System Version 4.0
- Microsoft ® Windows ® Millennium Edition Operating System
- Microsoft ® Windows ® 2000 Professional Operating System
- Microsoft ® Windows ® XP Professional Operating System
- Microsoft ® Windows ® XP Home Edition Operating System

(4) Support for a wide variety of programming languages

MX Component supports the following programming languages.

It allows the user to develop a wide range of customized applications.

Programming language	Development software
Visual Basic ®	Microsoft ® Visual Basic ® 6.0
Visual C++ ®	Microsoft ® Visual C++ ® 6.0
VBScript	Text editors and commercially available HTML tools
VBA	Microsoft ® Excel 2000, Microsoft ® Excel 2002, Microsoft ® Access 2000 or Microsoft ® Access 2002

(5) Support for functions dedicated for data communication with PLCs

MX Component provides the functions necessary for data communication with PLCs, including functions for opening/closing communication lines and reading/writing devices. Multi-function communication programs can thus easily be developed with MX Component.

Function name	Function
Connect	Connect Connects a telephone line.
Open	Opens a communication line.
Close	Closes a communication line.
Disconnect	Disconnects a telephone line.
GetErrorMessage	Displays error definition and corrective action.
ReadDeviceBlock	Batch-reads data from devices. (LONG type)
WriteDeviceBlock	Batch-writes data to devices. (LONG type)
ReadDeviceBlock2	Batch-reads data from devices. (SHORT type/INT type)
WriteDeviceBlock2	Batch-writes data to devices. (SHORT type/INT type)
ReadDeviceRandom	Randomly reads data from devices. (LONG type)
WriteDeviceRandom	Randomly writes data to devices. (LONG type)
ReadDeviceRandom2	Randomly reads data from devices. (SHORT type/INT type)
WriteDeviceRandom2	Randomly writes data to devices. (SHORT type/INT type)
SetDevice	Sets one device. (LONG type)
GetDevice	Acquires the data of one device. (LONG type)
SetDevice2	Sets one device. (SHORT type/INT type)
GetDevice2	Acquires data of one device. (SHORT type/INT type)

Function name	Function
ReadBuffer	Reads from buffer memory.
WriteBuffer	Writes to buffer memory.
GetClockData	Reads clock data from PLC CPU.
SetClockData	Writes clock data to PLC CPU.
GetCpuType	Reads a PLC CPU type.
SetCpuStatus	Remote RUN/STOP/PAUSE of PLC CPU
EntryDeviceStatus	Registers device status monitor.
FreeDeviceStatus	Deregisters device status monitor.
OnDeviceStatus	Announces event.

(6) Collecting data on Excel without programming

Using MX component and MX Sheet (SWnD5C-SHEET-E) allows users to collect PLC device data on Excel with only simple setting and without any programming.

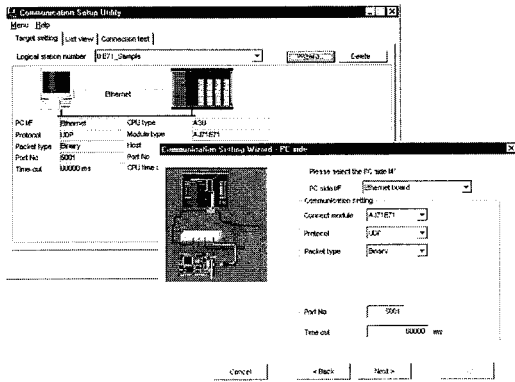
Appendix 10.2 Usage Procedure of MX Component

This section explains the procedure for creating programs and sample programs using MX Component.

(1) Procedure for creating programs

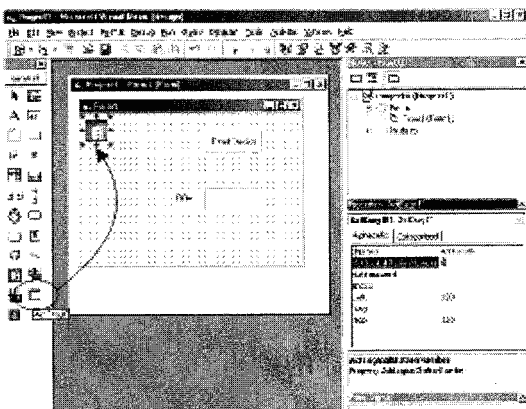
The procedure for creating programs is outlined below.

The usage procedure below uses Visual Basic ® as an example.

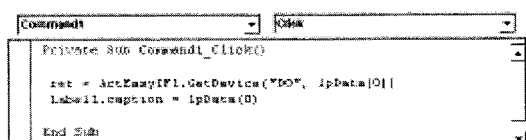


- 1) Perform the communication settings from a IBM PC/AT compatible personal computer to the PLC by following the wizard. (Some types of controls are set only by programs without using the wizard.)

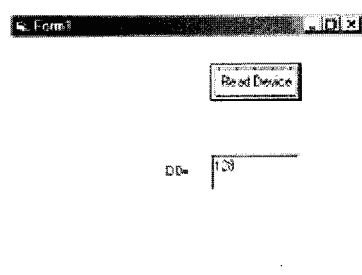
The wizard allows the user to perform the settings required for the communication such as logical station number, connected module type, and PLC to be connected.



- 2) Paste the ACT control icon onto the form and assign the logicalstation number set in step 1 to the property of the pasted control.



- 3) Use the functions provided by the software to write a program that reads the device data.



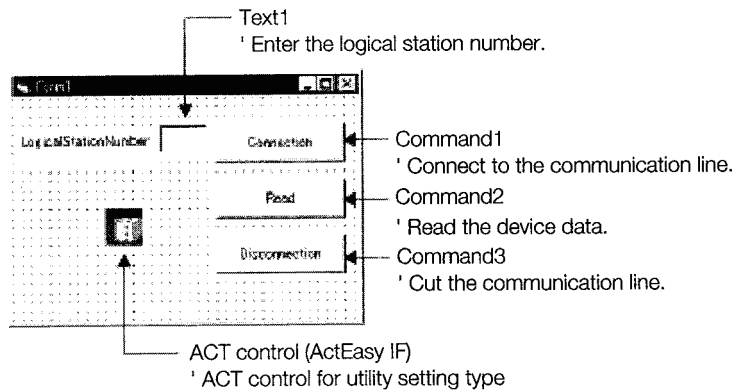
Completed

(2) Sample program

The following sample program reads D0 to D4 (five points) of the target PLC using the logical station number.

(a) When Visual Basic® is used

1) Screen example (Form1)



2) Program example

```
Private Sub Command1_Click()
```

```
' *****
```

```
' Connection
```

```
' *****
```

```
Dim rtn As Long
```

```
' Get LogicalStationNumber
```

```
ActEasyIF1.ActLogicalStationNumber = Val(Text1.Text)
```

```
' Connection
```

```
rtn = ActEasyIF1.Open()
```

```
If rtn = 0 Then
```

```
    MsgBox "The connection was successful"
```

```
Else
```

```
    MsgBox "Connection Error:" & Hex(rtn)
```

```
End If
```

```
End Sub
```

```
Private Sub Command2_Click()
```

```
' *****
```

```
' Read
```

```
' *****
```

```
Dim rtn As Long
```

```
Dim idata(5) As Integer
```

```
' D0-D4 are read
```

```
rtn = ActEasyIF1.ReadDeviceBlock2("D0", 5, idata(0))
```

```
If rtn = 0 Then
```

```
    MsgBox "D0-D5 = " & idata(0) & ", " & idata(1) & ", " & idata(2) & ", " & idata(3) & ", " & idata(4)
```

```
Else
```

```
    MsgBox "Read Error:" & Hex(rtn)
```

```
End If
```

```
End Sub
```



```

Private Sub Command3_Click()
' *****
'   Disconnection
' *****
Dim rtn As Long
'   Disconnection
rtn = ActEasyIF1.Close()
If rtn = 0 Then
    MsgBox "The disconnection was successful"
Else
    MsgBox "Disconnection Error:" & Hex(rtn)
End If
End Sub

```

(b) When Visual C++ ® is used

```

// *****
//   Connection
// *****
void CVCDlg::OnOpen()
{
    long lRet;
    CString szMessage;

    UpdateData();
    // Get LogicalStationNumber
    m_actEasyIF.SetActLogicalStationNumber ( m_lLogicalStationNumber );
    // Connection
    lRet = m_actEasyIF.Open();
    if ( lRet == 0 ) {
        MessageBox ( "The connection was successful" )
    } else {
        szMessage.Format ( "Connection Error: %x", lRet );
        MessageBox ( szMessage )
    }
}
}

```

```
// *****
// Read
// *****
void CVCDlg::OnRead()
{
    long lRet;
    short sData[5];
    CString szMessage;
    // D0-D4 are read
    lRet = m_actEasyIf. ReadDeviceBlock2 ( "D0", 5, sData );
    if ( lRet == 0 ) {
        szMessage.Format ( "D0-D5 = %d, %d, %d, %d, %d",
                           sData[0], sData[1], sData[2], sData[3], sData[4] );
        MessageBox ( szMessage );
    } else {
        szMessage.Format ( "Read Error: %x", lRet );
        MessageBox ( szMessage )
    }
}

// *****
// Disconnection
// *****
void CVCDlg::OnOpen()
{
    long lRet;
    CString szMessage;
    // Disconnection
    lRet = m_actEasyIf. Close();
    if ( lRet == 0 ) {
        MessageBox ( "The disconnection was successful" )
    } else {
        szMessage.Format ( "Disconnection Error: %x", lRet );
        MessageBox ( szMessage )
    }
}
```

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For A Ethernet Interface Module

User's Manual

MODEL	E71N-U-SY-E
MODEL CODE	13JR45
SH(NA)-080192-D(0606)MEE	



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